Synthesis & Implementation Results

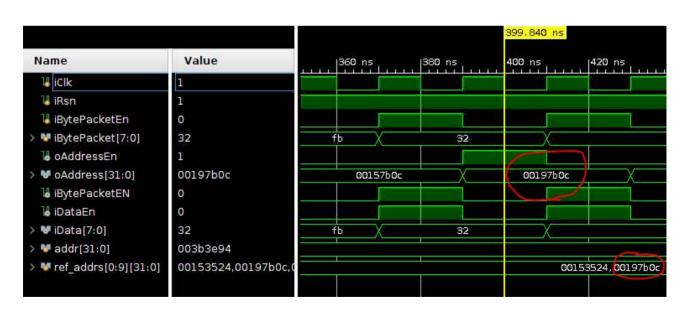


Vivado Simulation

1) Packet 000



2) Packet 001



3) Packet 002

