M54523P/FP

7-UNIT 500mA DARLINGTON TRANSISTOR-ARRAY WITH CLAMP DIODE

DESCRIPTION

M54523P and M54523FP are seven-circuit Darlington transistor arrays with clamping diodes. The circuits are made of NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

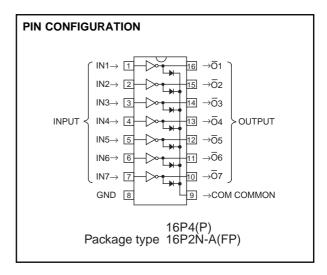
- High breakdown voltage (BVcEo ≥ 50V)
- High-current driving (Ic(max) = 500mA)
- With clamping diodes
- Driving available with PMOS IC ouput
- Wide operating temperature range (Ta = -20 to +75°C)

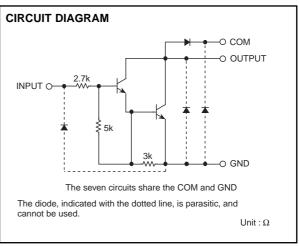
APPLICATION

Drives of relays and printers, digit drives of indication elements (LEDs and lamps), and interfaces between standard MOS-bipolar logic IC

FUNCTION

The M54523P and M54523FP each have seven circuits consisting of NPN Darlington transistors. These ICs have resistance of $2.7k\Omega$ between input transistor bases and input pins. A spike-killer clamping diode is provided between each output pin (collector) and COM pin. The output transistor emitters are all connected to the GND pin (pin 8). The collector current is 500mA maximum. Collector-emitter supply voltage is 50V maximum.The M54523FP is enclosed in a molded small flat package, enabling space-saving design.





ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, Ta = $-20 \sim +75$ °C)

Symbol	Parameter	Conditions	Ratings	Unit
VCEO	Collector-emitter voltage	Output, H	−0.5 ~ + 50	V
Ic	Collector current	Current per circuit output, L	500	mA
Vı	Input voltage		−0.5 ~ + 30	V
lF	Clamping diode forward current		500	mA
VR	Clamping diode reverse voltage		50	V
Pd	Power dissipation	Ta = 25°C, when mounted on board	1.47(P)/1.00(FP)	W
Topr	Operating temperature		-20 ~ +75	°C
Tstg	Storage temperature		−55 ~ + 125	°C



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RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted, Ta = $-20 \sim +75$ °C)

Cumbal	Parameter		Limits			l lmit
Symbol			min	typ	max	Unit
Vo	Output voltage		0	-	50	V
	Collector current (Current per 1 circuit when 7 circuits	Duty Cycle P : no more than 8% FP : no more than 8%	0	_	400	mA
IC	are coming on si- multaneously)	Duty Cycle P : no more than 30% FP : no more than 25%	0	_	200	IIIA
\ /···	"H" input voltage	Ic≤400mA	3.85		25	.,
VIH		Ic≤200mA	3.4	_	25	V
VIL	"L" input voltage		0	_	0.6	V

ELECTRICAL CHARACTERISTICS (Unless otherwise noted, Ta = -20 ~ +75°C)

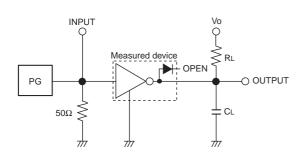
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	Utilit
V (BR) CEO	Collector-emitter breakdown voltage	ICEO = 100μA	50	_	_	V
VCE(sat)	Collector-emitter saturation voltage	VI = 3.85V, IC = 400mA	_	1.2	2.4	٧
		VI = 3.4V, IC = 200mA	_	1.0	1.6	
I.	Input current	VI = 3.85V	_	1.2	1.8	mA
l II		VI = 25V	_	9.5	18	
VF	Clamping diode forward volltage	IF = 400mA	_	1.4	2.4	V
lR	Clamping diode reverse current	VR = 50V	Ė	_	100	μΑ
hFE	DC amplification factor	VCE = 4V, IC = 350mA, Ta = 25°C	1000	2500		_

^{*:} The typical values are those measured under ambient temperature (Ta) of 25°C. There is no guarantee that these values are obtained under any conditions.

SWITCHING CHARACTERISTICS (Unless otherwise noted, Ta = 25° C)

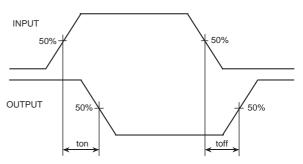
Symbol	Parameter	Test conditions		Limits		
		lest conditions	min	typ	max	Unit
ton	Turn-on time	0. 45-5 (4-4)	_	10	_	ns
toff	Turn-off time	CL = 15pF (note 1)	_	120	_	ns

NOTE 1 TEST CIRCUIT



- (1)Pulse generator (PG) characteristics : PRR=1kHz, tw = 10 μ s, tr = 6ns, tf = 6ns, Zo = 50 Ω VP = 3.85VP-P
- $\label{eq:local_local_local} \begin{tabular}{ll} (2) Input-output conditions: RL = 25\Omega, Vo = 10V \\ (3) Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes \\ \end{tabular}$

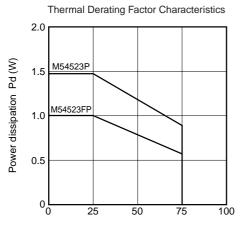
TIMING DIAGRAM



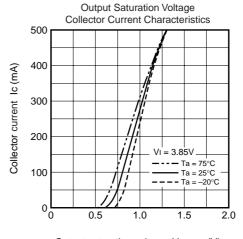
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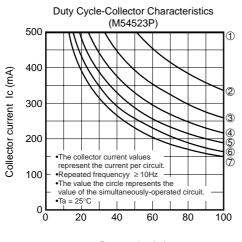
TYPICAL CHARACTERISTICS



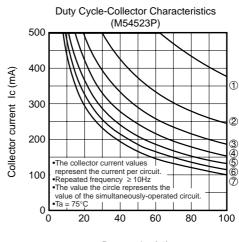




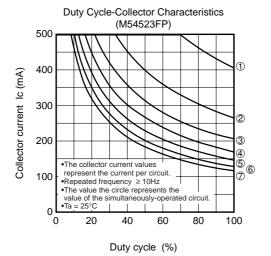
Output saturation voltage VCE(sat) (V)



Duty cycle (%)



Duty cycle (%)



Duty Cycle-Collector Characteristics (M54523FP)

400

400

400

7

The collector current value represent the current per circuit.

Repeated frequency ≥ 10Hz

The value of the simultaneously-operated circuit.

•Ta = 75°C

0 20 40 60 80 100

Duty cycle (%)



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