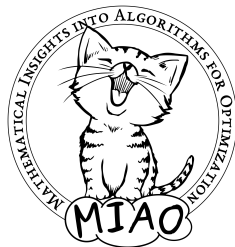


A One-Size-Fits-All Proof Logging System?

Jakob Nordström

University of Copenhagen
and Lund University

NII Shonan Meeting 180 “Art of SAT”
Shonan Village Center, Japan
October 4, 2023



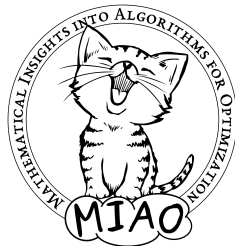
*Based on joint work with Bart Bogaerts, Stephan Gocht,
Ciaran McCreesh, Magnus O. Myreen, Andy Oertel, and Yong Kiam Tan*

The Art of Certifying Correctness

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The Art of Combinatorial Solving (and the Dirty Little Secret)

- Astounding progress last couple of decades on **combinatorial solvers** for, e.g.:
 - Boolean satisfiability (SAT) solving and optimization [BHvMW21]
 - Constraint programming [RvBW06]
 - Mixed integer linear programming [AW13, BR07]
 - Satisfiability modulo theories (SMT) solving [BHvMW21]
- Solvers very fast, but **sometimes wrong** (even best commercial ones) [BLB10, CKSW13, AGJ⁺18, GSD19, GS19, BMN22, BBN⁺23]
- Even get feasibility of solutions wrong (though this should be straightforward!)
- And how to check the absence of solutions?
- Or that a solution is optimal? (Even off-by-one mistakes can snowball into large errors if solver used as subroutine)

What Can Be Done About Solver Bugs?

- **Software testing**

Hard to get good test coverage for sophisticated solvers

Inherently can only detect presence of bugs, not absence

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Prove that solver implementation adheres to formal specification

Current techniques cannot scale to this level of complexity

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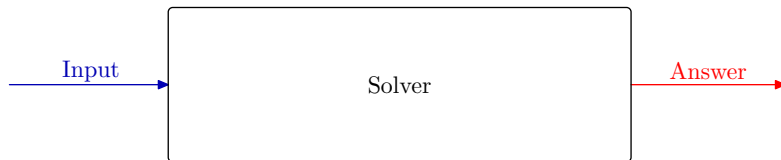
Prove that solver implementation adheres to formal specification
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- **Proof logging**

Make solver **certifying** [ABM⁺11, MMNS11] by adding code so that it outputs

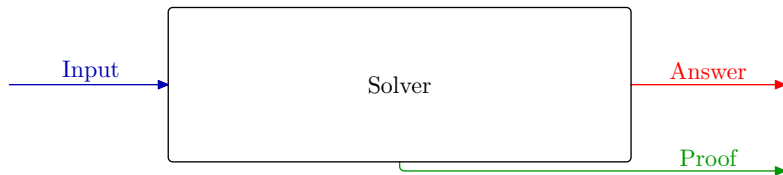
- ① not only **answer** but also
- ② simple, machine-verifiable **proof** that answer is correct

Proof Logging with Certifying Solvers: Workflow



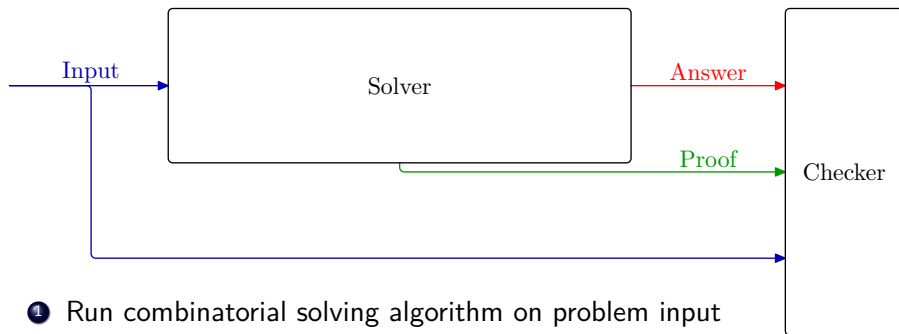
- 1 Run combinatorial solving algorithm on problem input

Proof Logging with Certifying Solvers: Workflow



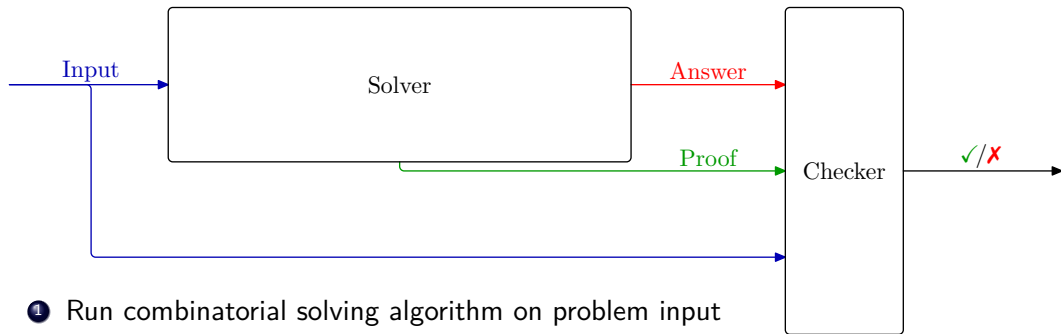
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Proof Logging with Certifying Solvers: Workflow



- ① Run combinatorial solving algorithm on problem input
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- ③ Feed input + answer + proof to proof checker

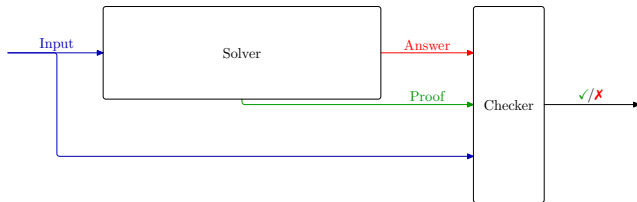
Proof Logging with Certifying Solvers: Workflow



- ① Run combinatorial solving algorithm on problem input
- ② Get as output not only answer but also proof
- ③ Feed input + answer + proof to proof checker
- ④ Verify that proof checker says answer is correct

Proof Logging Desiderata

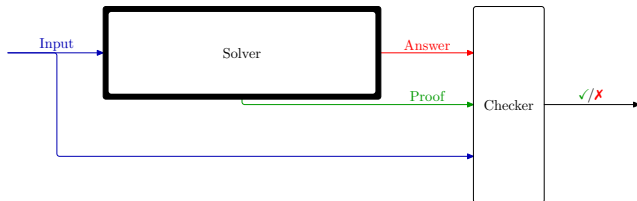
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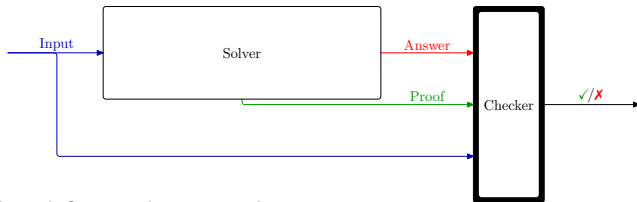
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Proof Logging Desiderata

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- **dead simple:** checking correctness of proofs should be trivial

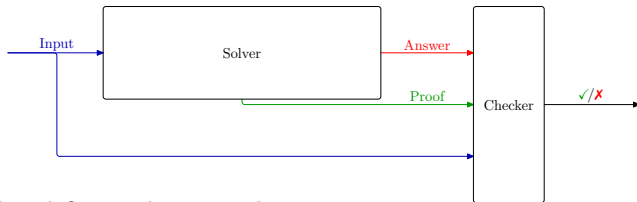


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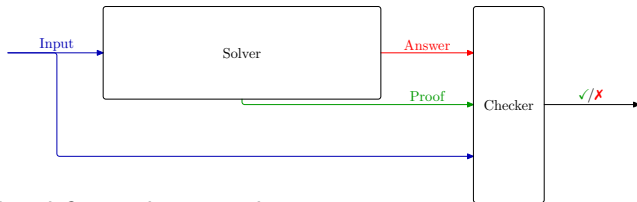
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Asking for both perhaps a little bit too good to be true?

This Talk

Proof logging for combinatorial optimization is possible with **single, unified method!**

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- Build on successes in SAT solving with proof formats such as DRAT [HHW13a, HHW13b, WHH14], GRIT [CMS17], LRAT [CHH⁺17], ...
- But represent constraints as **0–1 integer linear inequalities**
- Formalize reasoning using **cutting planes** [CCT87] proof system
- Add well-chosen **strengthening rules** [Goc22, GN21, BGMN22]
- Implemented in **VERiPB** (<https://gitlab.com/MIAOresearch/software/VeriPB>)

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- 1 Marketing pitch 😊
- 2 Solicit feedback

The Sales Pitch For Proof Logging

- ① Certifies correctness of computed results
- ② Detects errors even if due to compiler bugs, hardware failures, or cosmic rays
- ③ Provides debugging support during development
[EG21, GMM⁺20, KM21, BBN⁺23]
- ④ Facilitates performance analysis
- ⑤ Helps identify potential for further improvements
- ⑥ Enables auditability
- ⑦ Serves as stepping stone towards explainability

Design Principles for Proof Logging

Proof logging implementation

- Don't change solver
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- Proof logging overhead small constant fraction ($\lesssim 10\%$)
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Proof system

- Keep proof language maximally simple
- Reason about XOR constraints, CP propagators, symmetries, etc within language

Pseudo-Boolean Constraints

0-1 integer linear inequalities or pseudo-Boolean constraints:

$$\sum_i a_i \ell_i \geq A$$

- $a_i, A \in \mathbb{Z}$
- **literals** ℓ_i : x_i or \bar{x}_i (where $x_i + \bar{x}_i = 1$)
- variables x_i take values $0 = \text{false}$ or $1 = \text{true}$

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Sometimes convenient to use **normalized form** [Bar95] with all a_i, A positive (without loss of generality)

Some Types of Pseudo-Boolean Constraints

① Clauses

$$x \vee \bar{y} \vee z \quad \Leftrightarrow \quad x + \bar{y} + z \geq 1$$

② Cardinality constraints

$$x_1 + x_2 + x_3 + x_4 \geq 2$$

③ General pseudo-Boolean constraints

$$x_1 + 2\bar{x}_2 + 3x_3 + 4\bar{x}_4 + 5x_5 \geq 7$$

Pseudo-Boolean Proof Logging Wishlist

Paradigms

- SAT solving
- pseudo-Boolean solving
- graph solving
- constraint programming
- automated planning
- mixed integer linear programming
- SMT solving

Problem types

- decision / feasibility
- optimization
- multi-objective optimization
- projected model enumeration
- projected model counting
- preprocessing / problem reformulation

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Supported in VeriPB **presently**, **Real Soon Now™**, or
hopefully sometime in the future

Pseudo-Boolean Proof Logging — How and Why?

If problem is (special case of) 0-1 integer linear program

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Goldilocks compromise between expressivity and simplicity:

- ① 0-1 ILP **expressive formalism** for combinatorial problems (including objective)
- ② **Powerful reasoning** capturing many combinatorial arguments (even for SAT)
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$$r \Leftarrow x_1 + 2\bar{x}_2 + 3x_3 + 4\bar{x}_4 + 5x_5 \geq 7$$

$$9r + \bar{x}_1 + 2x_2 + 3\bar{x}_3 + 4x_4 + 5\bar{x}_5 \geq 9$$

VERIPB Proof Structure

① Preamble

Load input formula

Specify settings

② Derivation section

Derivations of new constraints

Logging of solutions

③ Output section

Listing of constraints currently in database

Input to next stage (or for debugging)

④ Conclusions section

Specification of what was established

- satisfiability / unsatisfiability
- optimality
- enumeration of solutions

VERIPB Proof Structure: Syntax

```
pseudo-Boolean proof version 2.0
f  $\langle M \rangle$ 
preserve_init  $\langle var1 \rangle \langle var2 \rangle \dots \langle varN \rangle$ 
 $\langle derivation\ part \rangle$ 
output  $\langle output\ part \rangle$ 
conclusion  $\langle conclusion\ part \rangle$ 
end pseudo-Boolean proof
```

VERIPB Proof Configuration

Core set \mathcal{C}

- Contains input formula at the start
- Maintains “equivalence” with input formula

Derived set \mathcal{D}

- All constraints derived during search
- Also intermediate constraints used in proof logging [but not used by solver]

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Objective $f = \sum_i w_i l_i + k$

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Order \mathcal{O}

- Pseudo-Boolean formula encoding pre-order (reflexive and transitive)
- Syntactic proof of properties required
- Applied to specified variable set \mathcal{Z}

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

From the input

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

Literal axioms

From the input

$$\overline{l_i \geq 0}$$

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

Literal axioms

Addition

From the input

$$\frac{\overline{\ell_i \geq 0} \quad \sum_i a_i \ell_i \geq A \quad \sum_i b_i \ell_i \geq B}{\sum_i (a_i + b_i) \ell_i \geq A + B}$$

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

Literal axioms

Addition

Multiplication for any $c \in \mathbb{N}^+$

From the input

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$$\frac{\sum_i a_i l_i \geq A}{\sum_i c a_i l_i \geq cA}$$

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

Literal axioms

Addition

Multiplication for any $c \in \mathbb{N}^+$

Division for any $c \in \mathbb{N}^+$
 (constraint in normalized form)

From the input

$$\begin{array}{c}
 \overline{\ell_i \geq 0} \\
 \hline
 \sum_i a_i \ell_i \geq A \quad \sum_i b_i \ell_i \geq B \\
 \hline
 \sum_i (a_i + b_i) \ell_i \geq A + B \\
 \\
 \frac{\sum_i a_i \ell_i \geq A}{\sum_i c a_i \ell_i \geq cA} \\
 \\
 \frac{\sum_i a_i \ell_i \geq A}{\sum_i \lceil \frac{a_i}{c} \rceil \ell_i \geq \lceil \frac{A}{c} \rceil}
 \end{array}$$

Pseudo-Boolean Reasoning: Cutting Planes [CCT87]

Input axioms

Literal axioms

Addition

Multiplication for any $c \in \mathbb{N}^+$

Division for any $c \in \mathbb{N}^+$
 (constraint in normalized form)

Saturation
 (constraint in normalized form)

From the input

$$\begin{array}{c}
 \overline{\ell_i \geq 0} \\
 \hline
 \sum_i a_i \ell_i \geq A \quad \sum_i b_i \ell_i \geq B \\
 \hline
 \sum_i (a_i + b_i) \ell_i \geq A + B \\
 \\
 \frac{\sum_i a_i \ell_i \geq A}{\sum_i c a_i \ell_i \geq cA} \\
 \\
 \frac{\sum_i a_i \ell_i \geq A}{\sum_i \lceil \frac{a_i}{c} \rceil \ell_i \geq \lceil \frac{A}{c} \rceil} \\
 \\
 \frac{\sum_i a_i \ell_i \geq A}{\sum_i \min(a_i, A) \cdot \ell_i \geq A}
 \end{array}$$

Cutting Planes Toy Example

$$w + 2x + y \geq 2$$

Cutting Planes Toy Example

$$\text{Multiply by 2} \quad \frac{w + 2x + y \geq 2}{2w + 4x + 2y \geq 4}$$

Cutting Planes Toy Example

$$\text{Multiply by 2} \quad \frac{w + 2x + y \geq 2}{2w + 4x + 2y \geq 4} \quad w + 2x + 4y + 2z \geq 5$$

Cutting Planes Toy Example

$$\begin{array}{rcl}
 \text{Multiply by 2} & \frac{w + 2x + y \geq 2}{2w + 4x + 2y \geq 4} & \\
 \text{Add} & \frac{2w + 4x + 2y \geq 4 \quad w + 2x + 4y + 2z \geq 5}{3w + 6x + 6y + 2z \geq 9} &
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 & & \frac{\bar{z} \geq 0}{2\bar{z} \geq 0} \text{ Multiply by 2}
 \end{array}$$

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 & \text{Add} & \text{Multiply by 2} \\
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 \text{Add} & \frac{3w + 6x + 6y + 2z \geq 9}{3w + 6x + 6y} & \geq 7
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 \text{Add} & \frac{3w + 6x + 6y}{3w + 6x + 6y + 2z} \geq 7 & \\
 \text{Divide by 3} & \frac{w + 2x + 2y \geq 2\frac{1}{3}}{} &
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 \end{array}$$

Naming constraints by integers and literal axioms by the literal involved as

$$\text{Constraint 1} \doteq 2x + y + w \geq 2$$

$$\text{Constraint 2} \doteq 2x + 4y + 2z + w \geq 5$$

$$\sim z \doteq \bar{z} \geq 0$$

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$$\sim z \doteq \bar{z} \geq 0$$

such a calculation is written in the proof log in reverse Polish notation as

pol 1 2 * 2 + ~z 2 * + 3 d

More About VERIPB Proofs

Variables

- start with a letter in A-Z or a-z
- continue with characters in A-Z, a-z, 0-9, or `[]{}-_^`
(square and curly brackets, hyphen, underscore, and caret)
- contain at least two characters

Constraints

Are referred to by positive integers (constraint IDs)

Derivation rules and requirements

Come in two flavours

- ① **kernel format** for formally verified proof checker
- ② **augmented format** with convenience rules such as **reverse unit propagation (RUP)**

Strengthening Rules

Witness ω : substitution mapping variables to truth values or literals

Redundance-based strengthening (witness ω show how to “patch assignment”)

Derive constraint C from $\mathcal{C} \cup \mathcal{D}$ if exists witness ω such that

$$\mathcal{C} \cup \mathcal{D} \cup \{\neg C\} \vdash (\mathcal{C} \cup \mathcal{D} \cup \{C\})|_{\omega} \cup \{f|_{\omega} \leq f\} \cup \mathcal{O}(\vec{z}|_{\omega}, \vec{z})$$

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Dominance-based strengthening (witness ω “drives down potential”)

Derive constraint C from $\mathcal{C} \cup \mathcal{D}$ if exists witness ω such that

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Strengthening Rules: Proof Format

```
red  $\langle \text{Constraint } C \rangle$  ;  $\langle var1 \rangle \rightarrow \langle val1 \rangle \dots \langle varN \rangle \rightarrow \langle valN \rangle$  ; begin  
    subproofs for proof goals  
end
```

```
dom  $\langle \text{Constraint } C \rangle$  ;  $\langle var1 \rangle \rightarrow \langle val1 \rangle \dots \langle varN \rangle \rightarrow \langle valN \rangle$  ; begin  
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end
```

- Witness ω should be explicitly specified in proof log
- Subproofs of proof goals should also be explicit
- But can be skipped for proof goals “obvious” to proof checker (like by RUP)

Checked and Unchecked Deletion

Important to allow deletions of constraints from database

But powerful strengthening rules create problems:

- Unsatisfiable formulas can turn satisfiable
- Satisfiable formulas can turn unsatisfiable(!)

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- ② OK from core set \mathcal{C} only if C can be rederived from $\mathcal{C} \setminus \{C\}$ with redundancy rule

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Similar to deletion rule in [JHB12] (but not implemented in DRAT)

Conclusions for Decision Problems

NONE

Status is undetermined

SAT [: *<assignment>*]

Propagate given assignment w.r.t. database, then check against original formula

If no assignment given, then

- solution should have been logged
- no unchecked deletion must have occurred

UNSAT [: *<constraint ID>*]

Only valid if no solution has been logged

Check that specified constraint is contradictory (technically: negative slack)

If no constraint given, check that database unit propagates to contradiction

Optimization Problems

Any solution α found is logged with `sol` “log solution and improve” command

- provided solution α checked against current core set \mathcal{C}
- **Objective-improving constraint** $\sum_i w_i \ell_i \leq -1 + \sum_i w_i \cdot \alpha(\ell_i)$ added to core set (forces search for better solutions)

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Note that

- α need not be solution for original formula
- but such solution can be reconstructed from the proof

Proof format supports not just optimality, but also non-tight upper and lower bounds

Conclusions for Optimization Problems

NONE

No solution or lower bound found

BOUNDS $\langle LB \rangle$ [: $\langle constraint\ ID \rangle$] $\langle UB \rangle$ [: $\langle assignment \rangle$]

$\langle LB \rangle$ and $\langle UB \rangle$ are integers or `inf`; optimality if $\langle LB \rangle = \langle UB \rangle$

Conclusions for Optimization Problems

NONE

No solution or lower bound found

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Lower bound

Constraint $\langle constraint\ ID \rangle$, if specified, should imply lower bound

Otherwise, $f \geq \langle LB \rangle$ should be “obvious” to proof checker from current database

Conclusions for Optimization Problems

NONE

No solution or lower bound found

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Otherwise, $f \geq \langle LB \rangle$ should be “obvious” to proof checker from current database

Upper bound

Propagate given assignment w.r.t. database, then check against original formula

If no assignment given, then

- solution with value $\langle UB \rangle$ should have been logged
- no unchecked deletion must have occurred

Projected Model Enumeration and Preserved Variables

Command

`preserve_init $\langle var1 \rangle$ $\langle var2 \rangle$... $\langle varN \rangle$`

in proof preamble (after loading formula) specifies set V of **preserved variables**

Projected Model Enumeration and Preserved Variables

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preserve_init  $\langle var1 \rangle$   $\langle var2 \rangle$  ...  $\langle varN \rangle$ 
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Preserved variables cannot appear in domain of any witness ω for strengthening rules

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Any solution α found is logged with “**log solution and exclude**” `solx` command

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- **Solution-excluding constraint** $\bigvee_{x \in V} (x \neq \alpha(x))$ added to core set (forces search for other solutions)

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(forces search for other solutions)

Can also add and remove variables from preserved set (if we can prove that they are determined by other preserved variables)

Conclusions for Projected Model Enumeration Problems

NONE

No solution or contradiction found

ENUMERATION PARTIAL : $\langle N \rangle$

The number of `solx` commands in the proof log is $\langle N \rangle$

No unchecked deletion must have occurred

ENUMERATION COMPLETE : $\langle N \rangle$ [: $\langle \textit{constraint ID} \rangle$]

The list of solutions found and enumerated is complete

The number of `solx` commands in the proof log is $\langle N \rangle$

Check that specified constraint is contradictory (technically: negative slack)

If no constraint given, check that database unit propagates to contradiction

No unchecked deletion must have occurred

Problem Reformulation and Output Section

NONE

No output

DERIVABLE

Any unsatisfiability / lower bound shown for output will be valid also for input

EQUISATISFIABLE

Input and output are equisatisfiable

True for decision problems with checked deletion

EQUIOPTIMAL

Input and output have same optimal value

(or optimal solution was found and the output is unsatisfiable)

EQUIENUMERABLE

Input and output have the same number of projected solutions

(and no solutions have been logged)

Objective Update

Objective function update command

```
obju  $\langle f_{\text{new}} \rangle$  ; begin  
    subproof of  $f_{\text{new}} \geq f_{\text{curr}}$   
    subproof of  $f_{\text{new}} \leq f_{\text{curr}}$   
end
```

changes objective function of (potentially reformulated) problem

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end
```

changes objective function of (potentially reformulated) problem

Subproofs may only use constraints in core set \mathcal{C}

If $f_{\text{new}} \geq f_{\text{curr}}$ and/or $f_{\text{new}} \leq f_{\text{curr}}$ are “obvious”, then subproofs can be skipped

Using VERIPB for SAT Solving

- ① Use dedicated tools for Gaussian elimination [GN21], symmetry breaking [BGMN22], PB-to-CNF translation [GMNO22], et cetera
- ② Concatenate with CDCL solver DRAT proof rewritten in VERIPB format (https://gitlab.com/MIAOresearch/tools-and-utilities/kissat_fork)

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Short dictionary for DRAT-to-VeriPB translations

DRAT	VERIPB
1	x1
-2	$\sim x2$
1 -2 3 0	1 x1 1 $\sim x2$ 1 x3 ≥ 1 ;
1 -2 3 0 is RUP	rup 1 x1 1 $\sim x2$ 1 x3 ≥ 1 ;
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- ③ But LRAT syntactically rewritten for VERIPB should allow way faster proof checking — see latest version of CADICAL [CaD]

VERIPB Documentation

VERIPB tutorial at *CP '22* [BMN22]

- video at youtu.be/s_5BIi4I22w
- updated slides for *IJCAI '23* tutorial [BMN23]



Description of VERIPB and CAKEPB [BMM⁺23] for SAT 2023 competition

- Available at satcompetition.github.io/2023/checkers.html

Specific details on different proof logging techniques covered in research papers [EGMN20, GMN20, GMM⁺20, GN21, BGMN22, GMN22, GMNO22, VDB22, BBN⁺23, MM23]

Lots of concrete example files at gitlab.com/MIA0research/software/VeriPB

Future Research Directions

Performance and reliability of pseudo-Boolean proof logging

- Trim proof while verifying (as in DRAT-TRIM [HHW13a])
- Compress proof file using binary format
- Design formally verified proof checker (*work in progress* [BMM⁺23])

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- Model counting
- Symmetric learning and recycling (substitution) of subproofs
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And more...

- Use proof logs for algorithm analysis or explainability purposes
- Lots of other challenging problems and interesting ideas

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And more...

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- Lots of other challenging problems and interesting ideas
- **We're hiring!** Talk to me to join the pseudo-Boolean proof logging revolution! ☺

Summing up

- Combinatorial solving and optimization is a true success story
- But ensuring correctness is a crucial, and not yet satisfactorily addressed, concern
- Certifying solvers producing machine-verifiable proofs of correctness seems like most promising approach
- Cutting planes reasoning with pseudo-Boolean constraints seems to hit a sweet spot between simplicity and expressivity
- **Action point:** What problems can VERIPB solve for you? ☺



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Thank you for your attention!



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