

What does it mean to be "the hardest problem in NP"?

DEFINITION (NP-HARD) and NP-COMPLETE

The language $L \subseteq \Sigma^*$ is **NP-HARD**, if for every $L' \in NP$ it holds that $L' \leq_p L$ (i.e., there is a polynomial-time-computable function $g: \Sigma^* \rightarrow \Sigma^*$ such that

$$x \in L' \iff g(x) \in L$$

L is **NP-COMPLETE** if in addition $L \in NP$

L is as hard as any problem in NP , since any efficient algorithm for L can be used to decide any language in NP efficiently

LEMMA

- ① If $L_1 \leq_p L_2$ and $L_2 \leq_p L_3$, then $L_1 \leq_p L_3$ (TRANSITIVITY)
- ② If L is NP-hard and $L \in P$, then $P = NP$
- ③ If L is NP-complete, then $L \in P$ iff $P = NP$

Proof: Exercise, or see textbook.

But do NP-complete problems exist? Not clear from the definition. But indeed NP-complete problems are all over the place in computer science, mathematics, physics, chemistry, biology, economics, industry ...

The most important ones (at least historically) are variants of **SATISFIABILITY** (or SAT for short)

CNF formula (conjunctive normal form)

$$(x_1 \vee x_2 \vee x_3) \wedge (\underline{x}_1 \vee \neg x_2 \vee \neg x_3) \\ \wedge (\neg x_1 \vee x_2 \vee \neg x_3) \wedge (\neg \underline{x}_1 \vee \neg x_2 \vee x_3)$$

Variables x_i (or x, y, z) set to true = 1 or false = 0

logical connectives AND \wedge , OR \vee , NOT \neg
(or sometimes write \bar{x} for $\neg x$)

(Disjunctive) clause $C = x_1 \vee \neg x_2 \vee \neg x_3$

satisfied if one literal assigned to true

CNF formula conjunction of clauses

$$F = C_1 \wedge C_2 \wedge \dots \wedge C_m = \bigwedge_{i=1}^m C_i$$

satisfied if all clauses C_i are satisfied

SAT = { $F \mid F$ is a satisfiable CNF formula }

k-CNF formula: Each clause has $\leq k$ literals

k-SAT = { $F \mid F$ is a satisfiable k-CNF formula }

COOK-LEVIN THEOREM (1971 and 1973, respectively)

- ① SAT is an NP-complete problem
- ② 3-SAT is an NP-complete problem

Focus on ① — ② is easy corollary.

Clearly, SAT $\in NP$. A satisfying assignment to the variables is a short witness that is easy to verify.

Need to show: For any $L \in NP$, exists efficient reduction g such that

$$x \in L \iff g(x) \text{ is a satisfiable CNF formula}$$

What can we do to prove this?

- Only thing we know is that exists Turing machine $M_L(x, y)$ such that $x \in L \iff \exists y \text{ of length } \leq p(|x|) \text{ such that } M_L(x, y) = 1$
- Write computation of such TM M_L on x as a CNF formula
- Show that if $x \in L$, then can plug in witness y such that formula describing TM computation is satisfied

Think of alphabet Σ as $\{0, 1\}$ (can always re-code symbols)

Think of input x as given.

Define Boolean function f_x by

$$f_x(y) = \begin{cases} 1 & \text{if } M_L(x, y) = 1 \\ 0 & \text{otherwise} \end{cases}$$

PROPOSITION 4

Any Boolean function $f: \{0,1\}^{\ell} \rightarrow \{0,1\}$
can be expressed as CNF formula
of size $\leq \ell \cdot 2^{\ell}$

(size := total # literals in formula counted with repetitions)

Proof sketch

Consider assignment α s.t. $f(\alpha) = 0$

Write down clause C_{α} falsified exactly by this one assignment

Let the CNF formula be $F = \bigwedge_{\alpha \in f^{-1}(0)} C_{\alpha}$

EXAMPLE PARTY $(x_1, x_2, x_3) = \underline{\text{odd \# variables true}}$

Truth table

x_1	x_2	x_3	PARTY	Clauses
0	0	0	0	$(x_1 \vee x_2 \vee x_3)$
0	0	1	1	
0	1	0	1	
0	1	1	0	$\wedge (x_1 \vee \neg x_2 \vee \neg x_3)$
1	0	0	1	
1	0	1	0	$\wedge (\neg x_1 \vee x_2 \vee \neg x_3)$
1	1	0	0	$\wedge (\neg x_1 \vee \neg x_2 \vee x_3)$
1	1	1	1	

This CNF formula evaluates to true precisely when # true variables odd

Proof attempt 1 for Cook-Levin Theorem:

Given $L \in NP$, verifier $M_L(x, y)$, and input x
 Consider $f_x^L(y)$

Use Proposition 4 to generate CNF formula F_x

F_x is satisfiable \iff exists y s.t. $M_L(x, y) = 1$
 This is our reduction g ! QED \blacksquare

Or is it? What is the size of F_x ?

$p(|x|) \cdot 2^{p(|x|)}$ — exponential!

So g will run in exponential time — too slow!

Use that Turing machine computations are LOCAL
 Only depend on current state and currently read symbols

Simplifying assumptions (but justified):

- ① For any $L \in NP$, fix polynomial p_L such that witnesses should have length exactly $p_L(|x|)$
- ② Turing machines have two tapes, input and work/output
- ③ Turing machine is OBLIVIOUS: head movements do not depend on tape contents, only on input length (which is $|x| + |y|$) $p(|x|)$

Can run M_L on x and $0^{p(|x|)} = 000\dots 0$ to determine head positions at every time step and build table

Almost quadratic loss in running time

(See Arora-Barak Ch 1 for details)

Notation

Q : Turing machine states (= "lines in program")

Σ : Alphabet (containing 0, 1, \sqcup = blank etc)

u : Input x and y concatenated

All symbols in Σ can be encoded in binary

If $|\Sigma| = s$, need $\lceil \log_2 s \rceil$ bits

SNAPSHOT $z = \langle a, b, q \rangle \in \Sigma \times \Sigma \times Q$

captures what determines Turing machine action at given time step

a : symbol read on input tape

b : symbol read on work/output tape

q : current TM state

Since Q is also finite, snapshot z can be encoded as binary string of fixed length, say c bits [choose $c \geq \lceil \log_2 s \rceil$]

Snapshot z_i at time i depends on:

(a) state at time $i-1$

(b) contents at current locations of tapes

Suppose we're given sequence of snapshots $z_1, z_2, z_3, \dots, z_t$ claimed to describe computation by M_x . How to verify?

INSIGHT: We can verify each z_i by only local checks

To check $z_i = \langle a_i, b_i, g_i \rangle$, only need to look at

(1) $z_{i-1} = \langle a_{i-1}, b_{i-1}, g_{i-1} \rangle$ — tells us if jump to state g_i correct

(2) $u_{\text{inputpos}(i)}$ — The input tape head is at position $\text{outputpos}(i)$ at time i (which we have computed in a table), so check $a_{\text{inputpos}(i)} = a_i$.

(3) $z_{\text{prev}(i)}$ — Time $\text{prev}(i)$ is the last time the work tape head was at its current position (which we have also computed in a table), so check that symbol written to output tape at time $\text{prev}(i)$ as specified by $z_{\text{prev}(i)} = \langle a_{\text{prev}(i)}, b_{\text{prev}(i)}, g_{\text{prev}(i)} \rangle$ is the same as b_i .

① - ③ uniquely determine z_i

So there is a Boolean function

$\text{step}_i(z_i, z_{i-1}, u_{\text{inputpos}(i)}, z_{\text{prev}(i)})$

that evaluates to true precisely when transition at time step i correct

Function of $\leq 4c$ bits = constant

Apply Proposition 4 \Rightarrow constant-size formula!

Running time of $M_x(x,y)$ is exactly $g(|x| + |y|)$ for some polynomial g , which is $g^*(|x|)$ for some other polynomial

$$g^*(|x|) = g(|x| + p_2|x|)$$

Let our reduction write down CNF formula F_x as follows

- (1) subformula INPUT(x) saying that first $|x|$ symbols on input tape must match x .
- (2) subformula START(z_1) encoding that the starting position of M_x is correct
- (3) subformulas STEP(i) for $i = 2, 3, \dots, g^*(|x|)$ saying that snapshot z_i is correct given z_{i-1} , $\text{inputs}(i)$, and $\text{prev}(i)$ (where we can look up $\text{inputs}(i)$ and $\text{prev}(i)$ in tables)
- (4) subformula ACCEPT saying that the final state is that of an accepting computation of M_x (e.g., 1 is written in first position of work tape and all other positions blank)

$$F_x = \text{INPUT}(x) \wedge \text{START}(z_1) \wedge \bigvee_{i=2}^{g^*(|x|)} \text{STEP}(i) \wedge \text{ACCEPT}$$

Subformulas ①, ②, ④ easy — just fixing bits to values

$$\text{"}v=w\text{" } (\neg v \vee w) \wedge (v \vee \neg w)$$

Subformula ③

$$\text{size } q^*(1 \times 1) \cdot \underbrace{\text{(exponential in } c\text{)}}_{\text{constant}}$$

Can be computed in polynomial time

- First run $M_L(x, O^{P_L(1 \times 1)})$ to compute tables "inputs" and "prev"
- Then output CNF formula F_x , where subformula ③ is what takes time

F_x is satisfiable if and only if exists y such that $M_L(x, y) = 1$, i.e., if and only if $x \in L$ QED (for real) 

Reducing from SAT to 3-SAT is straightforward exercise (or see textbook)

Two observations

- ① If M_L runs in time $T(1 \times 1)$, formula F_x can be made very small — $O(T \log T)$

- ② From satisfying assignment to F_x , can read off witness y for x

This is called a LEVIN REDUCTION

To prove that a language L is NP-complete, we need to do two things

- Show $L \in \text{NP}$ (usually easy)
- Reduce from SAT or 3-SAT (or from some other already known NP-complete problem) to L

We will now see some such reductions. But first one more definition

DEFINITION 5: COMPLEMENT CLASS

For a language $L \subseteq \sum_1^*$, the COMPLEMENT of L is $\bar{L} = \sum_1^* \setminus L$

DEFINITION 6: coNP

$$\text{coNP} = \{ L \mid \bar{L} \in \text{NP} \}$$

Aside: If strings in L encode objects such as formulas or graphs, then we usually think of \bar{L} as only containing correctly encoded instances.

That is, L and \bar{L} will both be sets of formulas or graphs satisfying or not satisfying some property, respectively, while "syntax error" strings are not contained in either L or \bar{L} .

This is just a technical convention that doesn't really matter much

Note that coNP is not the complement of NP — the intersection is non-empty! (E.g.)

$$P \subseteq NP \cap \text{coNP} \quad (\text{why?})$$

Example

TAUTOLOGY (example ⑦ above)

is in coNP . If F is not a tautology, then this is witnessed by an assignment falsifying the formula

$$\boxed{\text{UNSAT}} = \{ F \mid F \text{ is an unsatisfiable CNF formula} \}$$

is also in coNP

In fact, both of these languages are coNP-complete — any other language $L \in \text{coNP}$ can be efficiently reduced to them

Proof sketch: Given $L \in \text{coNP}$, run Cook-Levin reduction on L

$$x \in L \Leftrightarrow x \notin \overline{L} \Leftrightarrow \overline{F_x} \notin \text{SAT}$$
$$\Leftrightarrow \overline{F_x} \in \text{UNSAT}$$

How is coNP related to NP?

Could there be short certificates for UNSAT that somehow "compress information about exponentially many assignments"?

Most researchers believe $\text{NP} \neq \text{coNP}$
but this is a wide-open question!