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Artix 7 FPGA I/O

XC7A200TFBG484 BANK 34 IO_0_34 IO_L1P_T0_34 T1 U1 adc_d1d0_p adc_d1d0_n IO_L1N_T0_34 U2 adc_d7d6_p IO_L2P_T0_34 adc d7d6 n IO_L2N_T0_34-IO_L3P_T0_DQS_34-R3 adc_d3d2_p R2 adc_d3d2_n IO_L3N_T0_DQS_34 W2 adc_d11d10_p adc_d11d10_n IO_L4P_T0_34 IO_L4N_T0_34 IO_L5P_T0_34 adc_d5d4_p adc_d5d4_n IO_L5N_T0_34 adc_d9d8_p adc_d9d8_n IO_L6P_T0_34 V3 IO_L6N_T0_VREF_34 AA1 IO_L7P_T1_34 AB1 IO_L7N_T1_34 AB3 adc_d15d14_p adc_d15d14_n IO_L8P_T1_34 IO_L8N_T1_34 AB2 Y3 IO_L9P_T1_DQS_34 AA3 IO_L9N_T1_DQS_34 AA5 adc_d13d12_n IO_L10P_T1_34 IO_L10N_T1_34 AB5 IO_L11P_T1_SRCC_34 adc_of_n IO_L11N_T1_SRCC_34 adc_clkout_p V4 IO L12P T1 MRCC 34 adc_clkout_n IO_L12N_T1_MRCC_34 IO_L13P_T2_MRCC_34 IO_L13N_T2_MRCC_34 IO_L14P_T2_SRCC_34 Т5 U5 IO_L14N_T2_SRCC_34 IO_L15P_T2_DQS_34 IO_L15N_T2_DQS_34 IO_L16P_T2_34 Ψ6 V5 IO_L16N_T2_34 IO_L17P_T2_34 T6
IO_L17P_T2_34 Y6
IO_L18P_T2_34 AA6 IO_L18N_T2_34 IO_L19P_T3_34 W7
IO_L19N_T3_VREF_34 AB7 IO_L20P_T3_34 AB6 IO_L20N_T3_34 IO_L21P_T3_DQS_34 V8 IO_L21N_T3_DQS_34 AA8 IO_L22P_T3_34 AB8 IO_L22N_T3_34 Y8 IO L23P T3 34 ¥7 IO_L23N_T3_34 IO_L24P_T3_34 IO_L24N_T3_34-IO_25_34-ช7

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XC7A200TFBG484 BANK 13 IO_0_13 IO_L1N_T0_13 IO_L1B_T0_13 adc_sdo IO_L1P_T0_13 AB17 adc_sdi adc sclk IO_L2N_T0_13
IO_L2P_T0_13
IO_L3N_T0_DQS_13
AB13
IO_L3N_T0_DQS_13
AA13 adc csb IO_L3P_T0_DQS_13 AB15 IO_L4N_TO_13
IO_L4P_TO_13
IO_L5N_TO_13
IO_L5P_TO_13
IO_L5P_TO_13
6N_TO_VREF_13 IO_L6N_T0_VREF_13 W14 10_L6P_T0_13 10_L7N_T1_13 AB12 AB11 IO_L7P_T1_13 IO_L8N_T1_13 AA9 IO_L8P_T1_13 AA11 IO_L9N_T1_DQS_13
IO_L9P_T1_DQS_13
IO_L10N_T1_13
V10
V10 IO_L10P_T1_13 Y12 IO_L12P_T1_MRCC_13 V14 IO_L13N_T2_MRCC_13 V13
IO_L13P_T2_MRCC_13 V15 IO_L14N_T2_SRCC_13 U15 IO_L14P_T2_SRCC_13 IO_L15N_T2_DQS_13 IO_L15P_T2_DQS_13 IO_L15P_T2_DQS_13 W16 LISP_T2_DQS_13 IO_L16N_T2_13 IO_L16P_T2_13 IO_L17N_T2_13 IO_L17P_T2_13 **U55**

XC7A200TFGG484 BANK 16 artix_dbg0 IO_0_16 IO_L1P_T0_16 F13 artix dbg1 F14 artix_dbg2 IO_L1N_T0_16 F16 artix_dbg3 IO_L2P_T0_16 artix dbg4 IO_L2N_T0_16 -IO_L3P_T0_DQS_16 -IO_L3N_T0_DQS_16 -C14 artix dbg5 C15 artix_dbg6 E13 artix_dbg7 IO_L4P_T0_16 E14 artix_dbg8 IO_L4N_T0_16 IO_L5P_T0_16 E16 artix_dbg9 D16 IO_L5N_T0_16 D14 IO_L6P_T0_16 IO_L6N_T0_VREF_16 D15 artix_dbg_led0 artix_dbg_led1 B15 IO_L7P_T1_16 B16 artix_dbg_led2 IO_L7N_T1_16 C13 artix_dbg_led3 IO_L8P_T1_16 IO_L8N_T1_16 B13 A15 IO_L9P_T1_DQS_16 A16 watchdog_pulse IO_L9N_T1_DQS_16 IO_L10P_T1_16 IO_L10N_T1_16 watchdog_clk A14 B17 IO_L11P_T1_SRCC_16 dac d9 IO_L11N_T1_SRCC_16 - IO_L12P_T1_MRCC_16 - IO_L12N_T1_MRCC_16 D17 p2a_sck C17 dac_d5 IO_L13P_T2_MRCC_16 C19 dac d4 IO_L13N_T2_MRCC_16 -IO_L14P_T2_SRCC_16 -IO_L14N_T2_SRCC_16 -E19 dac d6 D19 dac_d1 F18 IO_L15P_T2_DQS_16 -IO_L15N_T2_DQS_16 -IO_L16P_T2_16 -E18 B20 dac_d8 A20 IO_L16N_T2_16 IO_L17P_T2_16 IO_L17N_T2_16 A19 dac_d13 F19 IO_L18P_T2_16 F20 IO_L18N_T2_16 D20 dac_d0 dac_d3 IO_L19P_T3_16 IO_L19N_T3_VREF_16 C20 dac_d2 IO_L20P_T3_16 B22 dac d11 IO_L20N_T3_16 -IO_L21P_T3_DQS_16 -IO_L21N_T3_DQS_16 dac d7 A21 dac clk E22 IO_L22P_T3_16 IO_L22N_T3_16 IO L23P T3 16 E21 p2a_csn D21 p2a_sdo IO_L23N_T3_16 G21 IO_L24P_T3_16 G22 IO_L24N_T3_16 IO_25_16 **U55**

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Artix 7 FPGA I/O

PUDC - Pull High for 3state I/O during configuration **≩ 4.7**K XC7A200TFBG484 BANK 14 IO 0 14 P22 cnfg_d0 IO_L1P_T0_D00_MOSI_14 R22 IO_L1N_T0_D01_DIN_14 cnfa d2 IO_L2P_T0_D02_14 R21 IO L2N TO D03 14 cnfg_d3 U22 IO_L3P_T0_DQS_PUDC_B_14 V22 fault_fpga_7 fault_fpga_9_lat TO_L3N_TO_DQS_EMCCLK_14 T21 T21 T0_L4P_TO_D04_14 U21 T0_L4N_TO_D05_14 P19 fault_fpga_9 fault_fpga_5_lat fault_fpga_7_lat fault_no_clock_lat fault_fpga_10_lat fault_bad_power_lat fault fpga 11 lat 10_L11N_T1_SRCC_14 10_L12P_T1_MRCC_14 10_L12N_T1_MRCC_14 10_L13R_T2_MRCC_14 10_L13R_T2_MRCC_14 fault_fpga_6_lat fault_fpga_2_lat fault_fpga_0_lat fault_fpga_3 fault_fpga_0 fault_fpga_1 fault_no_clock fault_no_trig fault_no_trig_lat fault_fpga_2 fault_fpga_1_lat acis_resetn fault_fpga_3_lat fault_no_pulse_lat IO_L18N_T2_A11_D27_14 P14 IO_L19P_T3_A10_D26_14 R14 fault_no_pulse IO_L19N_T3_A09_D25_VREF_14 IO_L19N_T3_A09_D25_VREF_14
IO_L20P_T3_A08_D24_14
IO_L20P_T3_A07_D23_14
IO_L21P_T3_DQS_14
IO_L21P_T3_DQS_14
IO_L22P_T3_A05_D21_14
IO_L22P_T3_A05_D21_14
IO_L22P_T3_A04_D20_14
IO_L23P_T3_A03_D19_14
IO_L23P_T3_A03_D19_14
IO_L23P_T3_A01_D17_14
IO_L24P_T3_A01_D17_14
IO_L24N_T3_A00_D16_14
IO_L25_14
IO_25_14 R18 beam_pulse_detect_led trigger_pulse_detect_led trigger_pulse_fp

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P3.3V

XC7A200TFBG484 BANK 35 IO_0_35 IO_L1P_T0_AD4P_35 eeprom csn0 A1 eeprom_sck0 IO_L1N_T0_AD4N_35 C2 eeprom_sdi0 IO_L2P_T0_AD12P_35 eeprom holdn0 IO_L2N_T0_AD12N_35 IO L3P T0 DQS AD5P 35 eeprom sdo0 D1 IO_L3N_T0_DQS_AD5N_35 eeprom_csn1 IO_L4P_T0_35 eeprom sck1 IO_L4N_T0_35 IO_L5P_T0_AD13P_35 G1 eeprom_sdi1 eeprom_holdn1 IO_L5N_T0_AD13N_35 eeprom_sdo1 IO_L6P_T0_35 IO L6N TO VREF 35 K1 dac_tp_d8 IO_L7P_T1_AD6P_35 dac_tp_d5 IO_L7N_T1_AD6N_35 dac_tp_d3 IO_L8P_T1_AD14P_35 IO_L8N_T1_AD14N_35 G2 dac_tp_d1 dac_tp_d7 IO_L9P_T1_DQS_AD7P_35 dac_tp_d4 IO_L9N_T1_DQS_AD7N_35 IO_L10P_T1_AD15P_35 IO_L10N_T1_AD15N_35 Н5 н3 dac_tp_d2 IO_L11P_T1_SRCC_35 dac_tp_d0 IO_L11N_T1_SRCC_35 IO L12P T1 MRCC 35 G4 IO_L12N_T1_MRCC_35 IO_L13P_T2_MRCC_35 IO_L13N_T2_MRCC_35 IO_L14P_T2_SRCC_35 т.3 dac tp d10 K3 dac_tp_d6 IO_L14N_T2_SRCC_35 dac_tp_clk IO_L15P_T2_DQS_35 dac_tp_d9 IO_L15N_T2_DQS_35 IO_L16P_T2_35 dac_tp_d11 dac_tp_d12 IO_L16N_T2_35 IO_L17P_T2_35 IO L17N T2 35 J6 T.5 IO_L18P_T2_35 dac_tp_d13 IO_L18N_T2_35 tpdac_sdo IO_L19P_T3_35 IO L19N T3 VREF 35 MЗ tp_sw0 IO_L20P_T3_35 tpdac_ldacn IO_L20N_T3_35 IO_L21P_T3_DQS 35 Р4 tpdac_sclk IO_L21N_T3_DQS_35 tpdac_syncn IO_L22P_T3_35 tp_sw1 IO_L22N_T3_35 IO L23P T3 35 М6 tp_sw2 М5 tp_sw3 IO_L23N_T3_35 IO_L24P_T3_35 tpdac_clrn L6 IO_L24N_T3_35 IO_25_35 **υ**55

XC7A200TFBG484 BANK 15 IO_0_15 IO_L1P_T0_AD0P_15 G1.3 IO_L1N_T0_AD0N_15 G15 a2p_d1_p IO_L2P_T0_AD8P_15 a2p_d1_n IO_L2N_T0_AD8N_15 IO L3P T0 DQS AD1P 15 J14 a2p d2 p H14 a2p_d2_n IO_L3N_T0_DQS_AD1N_15 a2p_d3_p a2p_d3_n IO_L4P_T0_15 G18 IO_L4N_T0_15 IO_L5P_T0_AD9P_15 J15 a2p_d4_p a2p_d4_n IO_L5N_T0_AD9N_15 a2p d5 p a2p d5 n IO_L6P_T0_15 H18 IO L6N TO VREF 15 J22 a2p_d6_p IO_L7P_T1_AD2P_15 a2p_d6_n IO_L7N_T1_AD2N_15 a2p_d0_p a2p_d0_n IO_L8P_T1_AD10P_15 IO_L8N_T1_AD10N_15 G20 K21 a2p_d8_p IO_L9P_T1_DQS_AD3P_15 a2p_d8_n a2p_d9_p IO_L9N_T1_DQS_AD3N_15 IO_L10P_T1_AD11P_15 IO_L10N_T1_AD11N_15 L21 a2p d9 n J20 a2p d10 p IO_L11P_T1_SRCC_15 a2p_d10_n IO_L11N_T1_SRCC_15 J19 a2p d7 p IO_L12P_T1_MRCC_15-IO_L12N_T1_MRCC_15-H19 a2p_d7_n K18 a2p_d11_p IO_L13P_T2_MRCC_15 IO_L13N_T2_MRCC_15 L19
IO_L14P_T2_SRCC_15 L20 a2p d11 n a2p d12 p a2p_d12_n IO_L14N_T2_SRCC_15 a2p_d13_p IO_L15P_T2_DQS_15 a2p d13 n a2p d14 p IO_L15N_T2_DQS_ADV_B_15 IO_L16P_T2_A28_15 м1 я L18 a2p_d14_n IO_L16N_T2_A27_15 a2p_d15_p a2p_d15_n IO_L17P_T2_A26_15 N19 IO L17N T2 A25 15 N20 a2p_sel_p IO_L18P_T2_A24_15 a2p_sel_n IO_L18N_T2_A23_15 a2p enb p IO_L19P_T3_A22_15 IO_L19N_T3_A21_VREF_15 K14 a2p_enb_n M13 IO_L20P_T3_A20_15 IO_L20N_T3_A19_15 IO_L21P_T3_DQS_15-IO_L21N_T3_DQS_A18_15-J17 L14 IO_L22P_T3_A17_15 L15 IO_L22N_T3_A16_15 L16 a2p_clk_p IO L23P T3 FOE B 15 K16 a2p_clk_n IO_L23N_T3_FWE_B_15 IO_L24P_T3_RS1_15 IO_L24N_T3_RS0_15 IO_25_15 **U**55

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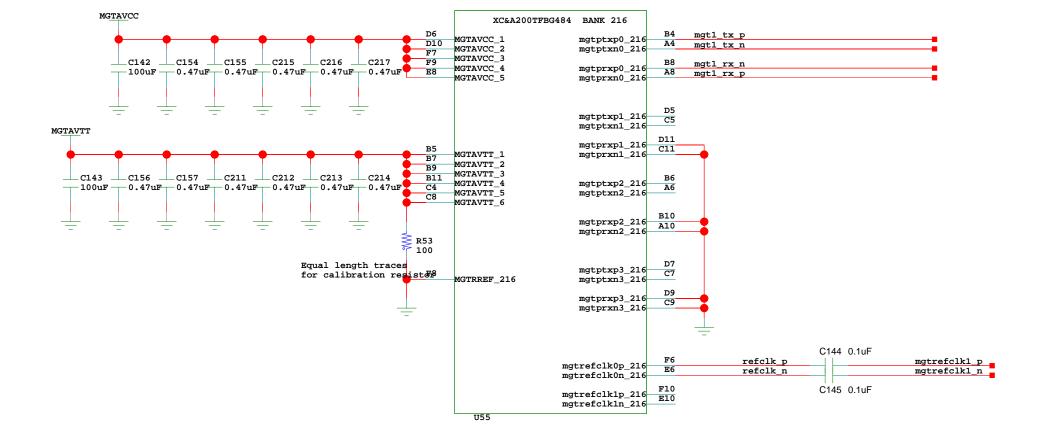
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Artix 7 FPGA MGT



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