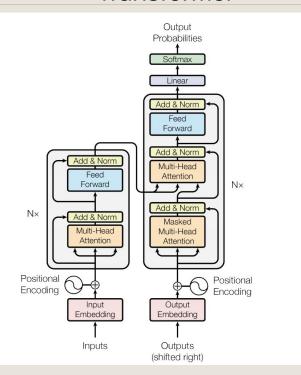
Flash Attention-3: Fast and Accurate Attention with Asynchrony and Low-precision

TIMELINE

Flash Attention 1	Flash Attention 2	Flash Attention 3
•	•	•
2022.10	2023.6	2024.3
Flash Attention:	Flash Attention-2:	Flash Attention-3:
Fast and	Faster Attention	Faster Attention
Memory-Efficient	with Better	with Better
Exact Attention	Parallelism and	Parallelism and
with IO-Awareness	Work Partitioning	Customization

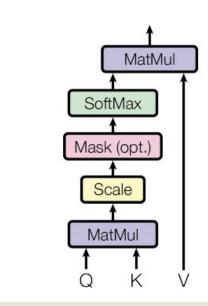
Attention

Transformer



[Image source: Vaswani et al. (2017)]

Scaled Dot-Product Attention

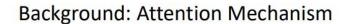


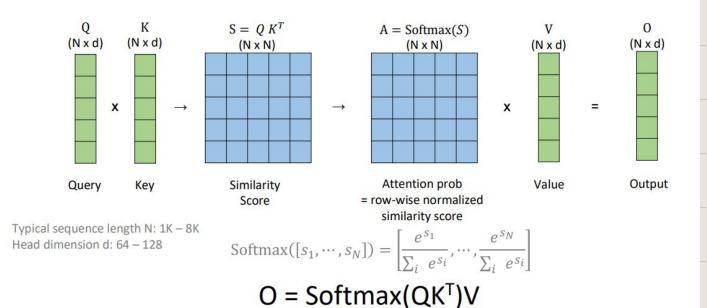
Scaled Dot-Product Attention

$$\operatorname{Attention}(Q,K,V) = \operatorname{softmax}\left(rac{QK^+}{\sqrt{d_k}}
ight)V$$

Attention

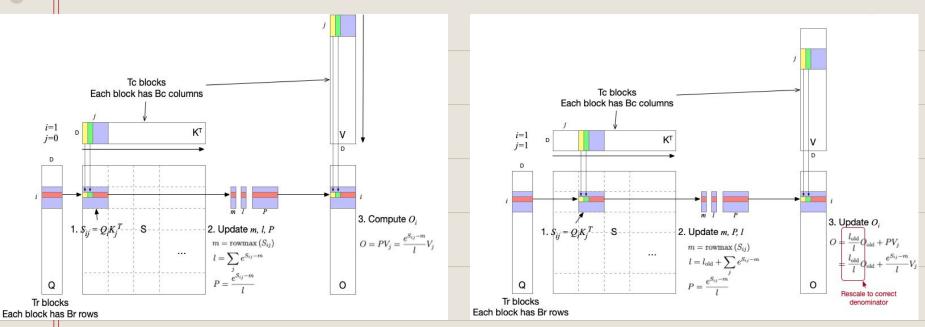
• The entire sequence of length N must be stored for the Softmax





[Image source: 2024 NuerIPS, Flash Attention 3]

- Softmax computed using partial scores S, with the max value
- Recomputed and compensated for later.



[Image source: https://insujang.github.io/2024-01-21/flash-attention/]

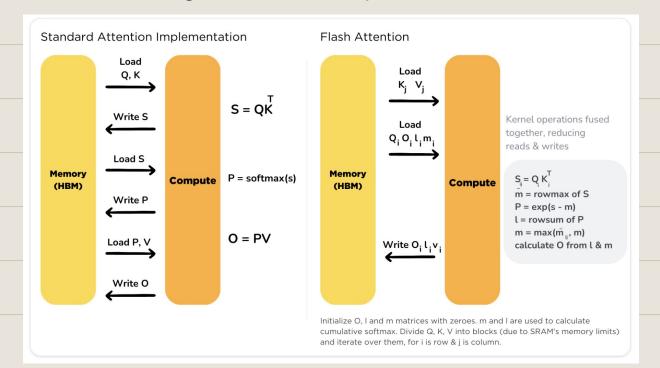
Recompute O1 with new max M1

Loop 0	Loop 1	
$S_0 = QK_0$	$S_1 = QK_1$	
$\mathbf{m}_0 = rowmax(S_0)$	$\mathbf{m}_1 = \max(rowmax(S_0), \mathbf{m}_0)$	
$l_0 = rowsum(e^{s_0 - m_0})$	$l_1 = l_0 * e^{m_0 - m_1} + e^{s_1 - m_1}$	
g		

 $O_1 = O_0 * \frac{l_0}{l_1} + P_1 V_1$

 $O_0 = P_0 V_0$

• Benefits : Reducing Read & Write cycle

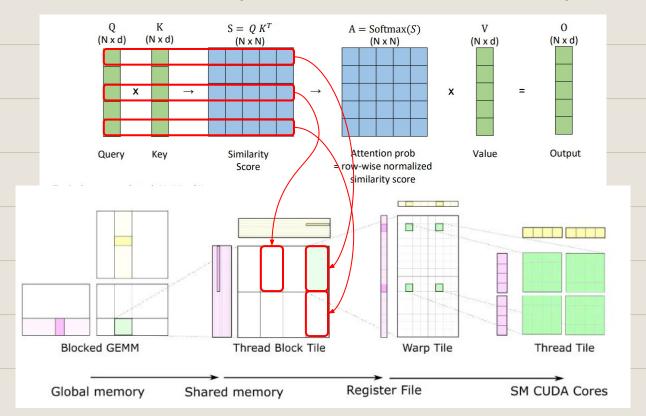


Limitation

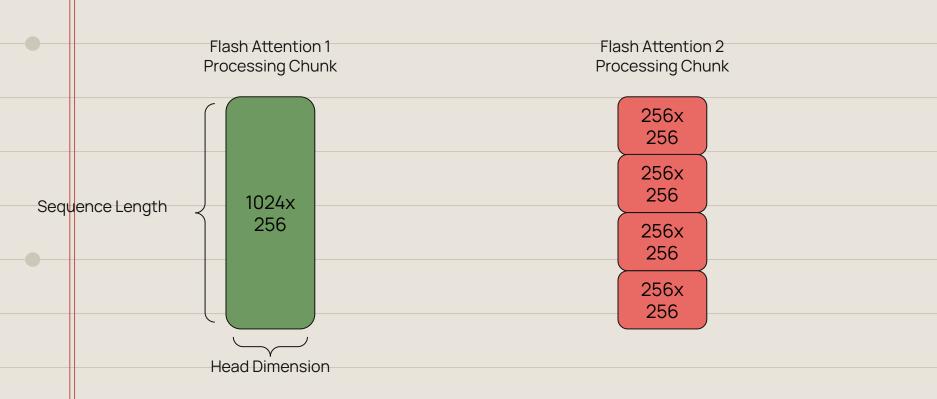
0

- C Limited inter-thread block parallelism: For long sequences, a single CTA (Cooperative Thread Array) handles the computation, which restricts parallelization.
 - Rigid memory layout: The fixed structure limits flexibility and adaptability to different models or hardware.
- O **Performance benefits mainly for long sequences**: Gains are significant only for long sequences; performance is
 - limited for short sequences or small batch sizes.

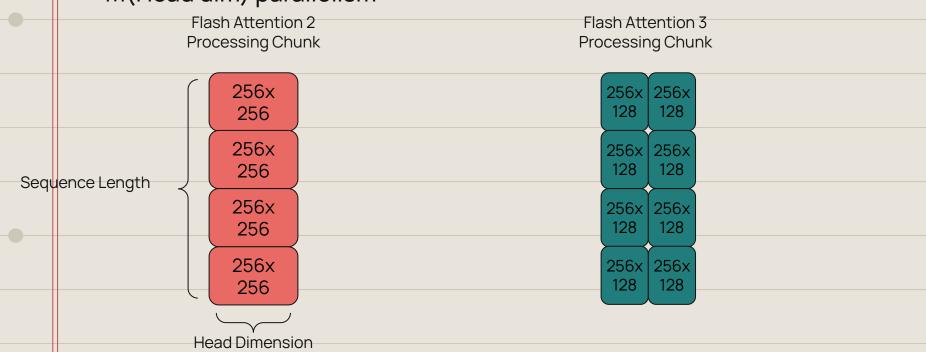
Main idea : Thread Block parallelism (Global max and update later)



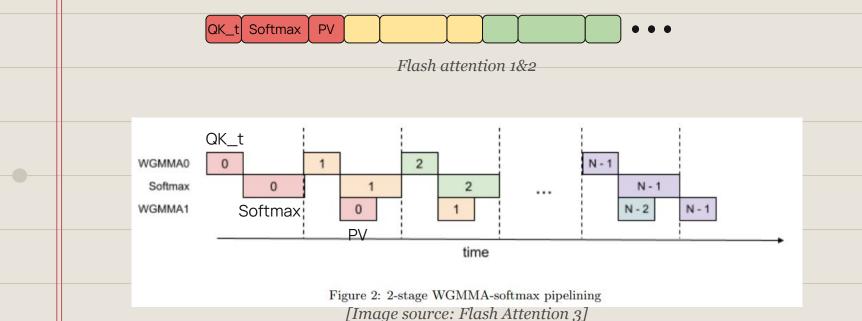
Main idea : Thread Block parallelism (Global max and update later)



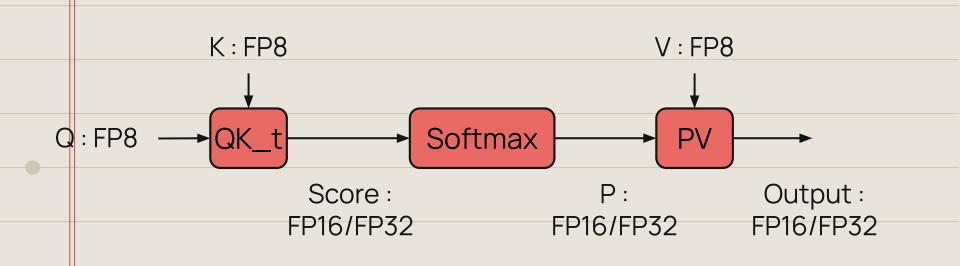
- Main idea: M(Head dim) parallelism + Re-ordering + Block Quantization
- M(Head dim) parallelism



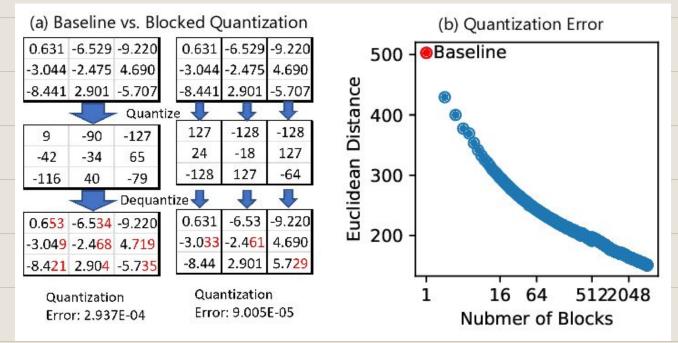
- Main idea : M parallelism + Re-ordering + Block Quantization
- Re-ordering



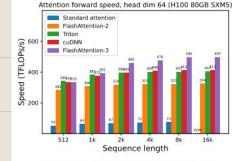
- Main idea : M parallelism + Re-ordering + Block Quantization
- FP8 Block Quantization



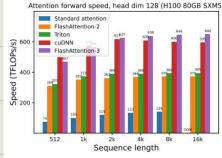
- Main idea : M parallelism + Re-ordering + Block Quantization
- FP8 Block Quantization

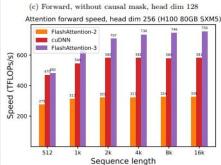


- Validation
- **GPU Optimize library**
 - cuDNN: CUDA Deep Neural Network
 - 0 Triton: GPU Programming for Neural Networks

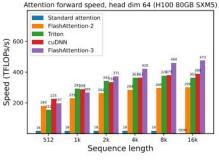


(a) Forward, without causal mask, head dim 64 Attention forward speed, head dim 128 (H100 80GB SXM5)

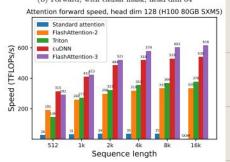




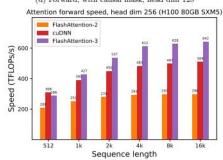
(e) Forward, without causal mask, head dim 256



(b) Forward, with causal mask, head dim 64



(d) Forward, with causal mask, head dim 128



(f) Forward, with causal mask, head dim 256

[Image source: Flash Attention - 3]

Figure 5: Attention forward speed (FP16/BF16) on H100 GPU

THANK YOU!