

JAMES BROADHEAD

Electronic and Computer Engineer

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I am currently looking for a Software Engineering position in Development or QA. I have experience working in industry, academia and open-source. I am efficient and motivated, and perform well in team environments and enjoy unusual and challenging tasks.

Third Level Qualifications:

Baccalaureus in Arte Ingeniaria (B.A.I) Hons.

Bachelor of Computer and Electronic Engineering from
Trinity College Dublin

Experience:

Telecommunications Research Group (TCD): *Postgraduate Researcher* (2007-2010)

Research into Frequency Rendezvous, Dynamic Network
formation and Emergency Communication Networks

Developed Distributed, Multi-Process Environment for
Monte-Carlo Simulations using Python (SciPy & Numpy)

Experience using Source Control and Bug Tracking (svn, TRAC)

Attended International Conferences and Workshops, Involved
in Peer Review of submitted papers

Taught First-Year Engineering class for Digital Design project

Cisco Systems: *Software Development Engineering Intern* (2007)

4-month internship in California in team-based software
development in the Voice Technology Group.

Experience programming C++ using the Windows API and MFC
with industry-standard tools.

Full member of software development team with
responsibilities and deadlines on two projects.

Gentoo Linux: *User / Contributor* (2005-present)

Active member of community; submitting bug reports, patches,
and install scripts. I contribute to documentation and
help in the user support channel.

General system administration experience, debugging failed
builds and failing applications.

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Experience (cont.):

Xilinx: *Customer Applications Engineer Intern (2006)*

3-month internship debugging customer design issues, replicating, triaging and reporting software bugs.

Experience with VLSI languages (VHDL, Verilog), attended digital design courses and “Customer skills / Interpersonal Communications” workshop

Experience dealing with customers over telephone and using remote-desktop

Skills:

Programming:

C, C++, Java, Python, RISC Assembly (M68k), Erlang, PIC Microcontrollers, VLSI Languages for FPGA (VHDL, Verilog) Shell Scripting (bash)

Development Environments:

Developing for Linux and Windows Systems. Some experience using Solaris in the TCD Networking Society

Experience working with Pspice, Matlab, Xilinx ISE, Modelsim, Visual Studio, gcc, C++test, subversion, mercurial.

City and Guilds AutoCAD qualified.

Secondary Education:

St. Andrew's College, Booterstown, Co. Dublin

Leaving Certificate – All subjects at Higher Level (2003)

A2 – Maths, English, Music French B2 – Irish

B3 – Physics, Classical Studies.

Highly Commended in Young Scientist Competition

Other Experience:

Reserve Defence Force: *Infantry Platoon Sergeant (2003-present)*

Part-time training and instruction with the RDF.

I hold the rank of Sergeant, an administration/leadership role at platoon level (30 soldiers).

This has greatly improved my work-discipline, self and situational awareness, public speaking, and structured, solutions-based thinking.

Languages: English, French, Irish

Interests: Music; Grade 8 Oboe, Sailing, Skiing, Hiking, Reading.

References: Andy Francke; andyfra@cisco.com
Yann Clin; yann.clin@xilinx.com