

# **Survey of Software verification tools for real world applications**

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# Contents

<b>1</b>	<b>Introduction</b>	<b>3</b>
<b>2</b>	<b>Software Verification Overview</b>	<b>3</b>
<b>3</b>	<b>Formal Program Verification Tools</b>	<b>3</b>
3.1	Extended Static Checker for Java[5] . . . . .	3
3.2	Static Driver Verifier/SLAM[6] . . . . .	3
3.3	Krakatoa [8], [9] . . . . .	4
<b>4</b>	<b>Verifiable Languages</b>	<b>4</b>
4.1	Spec# . . . . .	4
4.2	Spark Ada . . . . .	4
<b>5</b>	<b>Conclusion</b>	<b>5</b>
	<b>References</b>	<b>6</b>

# 1 Introduction

Generally, software verification is a very interesting topic in research at the moment, however it is currently limited to the field of researchers and it is only really used as a part of demonstration software and only as a part of verifiable languages. Therefore, this paper will look into how the software verification techniques can be applied to applications designed for use in the real world. This will obviously have advantages as it guarantees code free of fatal runtime errors and reduces the likelihood of other errors.

## 2 Software Verification Overview

Software verification can be looked at as the automatic analysis of a program. One commonplace example of program verification is type checking which has been implemented in many programming languages. There are also more complex areas of program verification such as extended static checking and full functional program verification. For the purposes of this report I will be calling Languages which have been designed to be verified as a part of compilation “Verifiable Languages”. There are a number of these languages which I will go on to outline further in the paper. There are also tools which have been developed to verify existing languages

## 3 Formal Program Verification Tools

### 3.1 Extended Static Checker for Java[5]

This is a checker which finds common programming errors, this may be one of the problems with these tools they are labelled as tools which find bugs rather than tools which guarantee code quality if you are a programmer generally you don’t like finding bugs in your code. Programmers need to write annotations for the program. “The checker is powered by verification-condition generation and automatic theorem proving techniques.”[5]. This works on a modular level meaning any method or constructor can be verified.

### 3.2 Static Driver Verifier/SLAM[6]

This was designed to allow Microsoft to verify drivers for their operating systems. The actual tool used to verify drivers was called Static Driver Verifier while slam is the analysis engine it uses. SLAM is a tool which can “check that a C program correctly uses the interface to an external library”[6]. I expect that this is one of the best examples of mainstream software verification uses. This is probably

because the SDV(Static Driver Verifier) tool is included as a part of the driver developer kit for windows this means that developers have to make sure that their driver is verified before they can release it.

### 3.3 Krakatoa [8], [9]

This is a verification tool which runs can be run on programs written in Java. Contracts for the code are written in the comments for a method the come from the Java Modelling Language[10]. This tool is integrated into the Why IDE which was designed to make it easier to verify programs.

## 4 Verifiable Languages

### 4.1 Spec#

This is not a tool as such it is a language which adds various static analysis features which are checked during compilation. This runs on the .NET framework therefore can be used with other .NET languages. Many developers have easy access to the language through visual studio “The Spec# system is fully integrated into the Microsoft Visual Studio environment.”[7]. Barnett et al. also lists several features which Spec# adds to C# such as:

- “type support for distinguishing non-null object references from possibly-null object references”[7]
- “method specifications like pre- and postconditions”[7]
- “support for constraining the data fields of objects”[7]

Because this works on the .NET framework functions can be called from other languages meaning it is possible to call a method with parameters which violate the preconditions in this case Spec# provides the option to specify an exception to be thrown in the case where a precondition is not met.

### 4.2 Spark Ada

Spark is a Verifiable language based on Ada it uses pre and post conditions as contracts for verification. It appears to be used on a number of mission critical pieces of software mainly on embedded systems.

## 5 Conclusion

In conclusion there seems to be many tools and many verifiable languages although there doesn't appear to be any which compile to the JVM which could be the reason why it is not more widespread. I would expect formal verification to grow in the future but as mentioned earlier there several people already predicted this and this obviously has not happened.

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