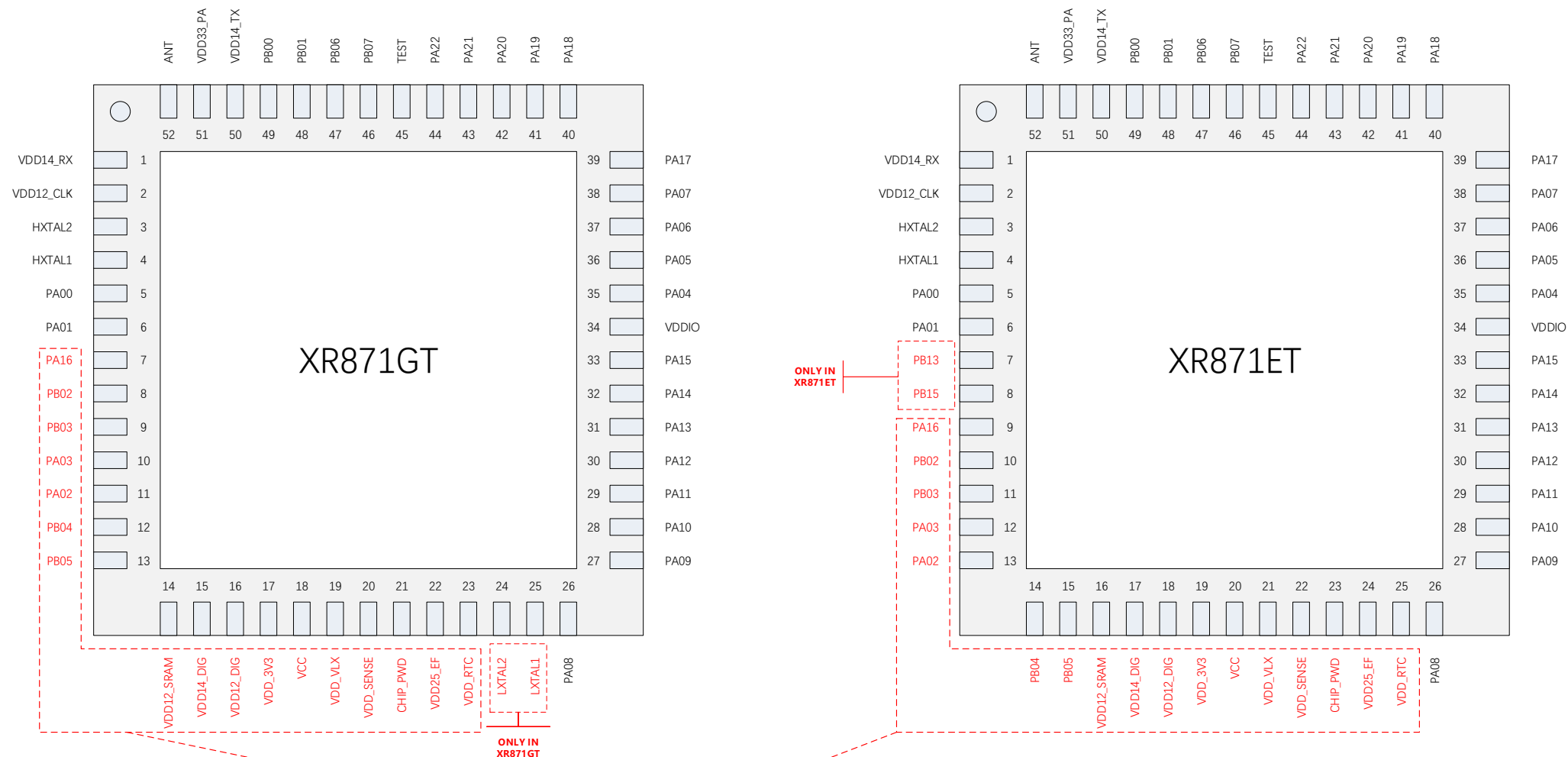


# **XR871GT VS XR871ET**

# Package

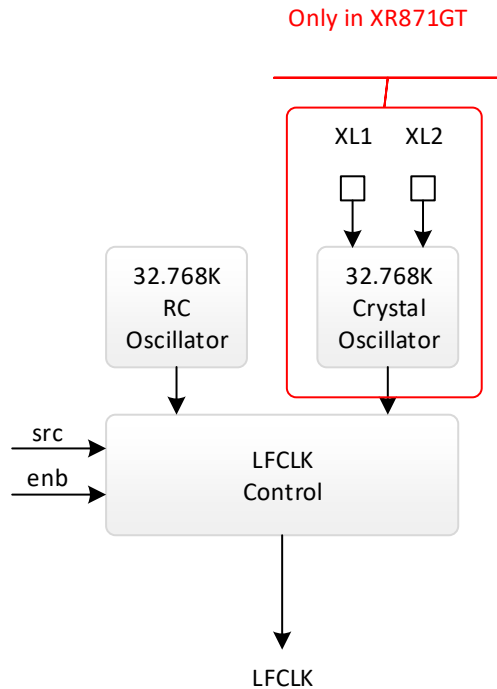


PINs with Different NO.

# Clock & GPIO

Function	Name	Func0	Func1	Func2	Func3	Func4	Func5	Func6	Func7	XR871GT	XR871ET
GPIOA	PA00	I	O	SPI1_MOSI	SD_CMD	UART0_TX	CSI_D0	EINTA0	Hi	1	1
	PA01	I	O	SPI1_MISO	SD_DATA0	UART0_RX	CSI_D1	EINTA1	Hi	1	1
	PA02	I	O	SPI1_CLK	SD_CLK	TWI1_SCL	CSI_D2	EINTA2	Hi	1	1
	PA03	I	O	SPI1_CS0	SD_DATA1	TWI1_SDA	CSI_D3	EINTA3	Hi	1	1
	PA04/WUPIO0	I	O	UART1_CTS	SD_DATA2	TWI0_SCL	CSI_D4	EINTA4	Hi	1	1
	PA05/WUPIO1	I	O	UART1_RTS	SD_DATA3	TWI0_SDA	CSI_D5	EINTA5	Hi	1	1
	PA06/WUPIO2	I	O	UART1_TX	SPI1_CS1	TWI0_SCL	CSI_D6	EINTA6	Hi	1	1
	PA07/WUPIO3	I	O	UART1_RX	SPI1_CS2	TWI0_SDA	CSI_D7	EINTA7	Hi	1	1
	PA08	I	O	ADC_CH0	PWM0/ECT0	TWI1_SCL	CSI_PCLK	EINTA8	Hi	1	1
	PA09	I	O	ADC_CH1	PWM1/ECT1	TWI1_SDA	CSI_MCLK	EINTA9	Hi	1	1
	PA10	I	O	ADC_CH2	PWM2/ECT2	DMIC_CLK	CSI_HSYNC	EINTA10	Hi	1	1
	PA11	I	O	ADC_CH3	PWM3/ECT3	DMIC_DATA	CSI_VSYNC	EINTA11	Hi	1	1
	PA12	I	O	ADC_CH4	PWM4/ECT4	I2S_MCLK	IR_TX	EINTA12	Hi	1	1
	PA13	I	O	ADC_CH5	PWM5/ECT5	I2S_BCLK	32KOSCO	EINTA13	Hi	1	1
	PA14	I	O	ADC_CH6	PWM6/ECT6	I2S_DI	IR_RX	EINTA14	Hi	1	1
	PA15	I	O	ADC_CH7	PWM7/ECT7	I2S_DO	UART1_CTS	EINTA15	Hi	1	1
	PA16	I	O	IR_TX	IR_RX	I2S_LRCLK	UART1_RTS	EINTA16	Hi	1	1
	PA17/WUPIO4	I	O	TWI0_SCL	IR_RX	TWI1_SCL	UART1_TX	EINTA17	Hi	1	1
	PA18/WUPIO5	I	O	TWI0_SDA	IR_TX	TWI1_SDA	UART1_RX	EINTA18	Hi	1	1
	PA19/WUPIO6	I	O	NUART_CTS	NSWD_TMS	PWM0/ECT0	SPI1_MOSI	EINTA19	Hi	1	1
	PA20/WUPIO7	I	O	NUART_RTS	NSWD_TCK	PWM1/ECT1	SPI1_MISO	EINTA20	Hi	1	1
	PA21/WUPIO8	I	O	NUART_TX	DMIC_CLK	PWM2/ECT2	SPI1_CLK	EINTA21	Hi	1	1
	PA22/WUPIO9	I	O	NUART_RX	DMIC_DATA	PWM3/ECT3	SPI1_CS0	EINTA22	Hi	1	1
GPIOB	PB00	I	O	UART0_TX	JTAG_TMS	PWM4/ECT4	SWD_TMS	EINTB0	Hi	1	1
	PB01	I	O	UART0_RX	JTAG_TCK	PWM5/ECT5	SWD_TCK	EINTB1	Hi	1	1
	PB02	I	O	SWD_TMS	JTAG_TDO	PWM6/ECT6	FLASH_WP/IO2	EINTB2	Hi	1	1
	PB03	I	O	SWD_TCK	JTAG_TDI	PWM7/ECT7	FLASH_HOLD/IO3	EINTB3	Hi	1	1
	PB04	I	O	SPI0_MOSI	SD_CMD		FLASH_MOSI/IO0	EINTB4	Hi	1	1
	PB05	I	O	SPI0_MISO	SD_DATA0		FLASH_MISO/IO1	EINTB5	Hi	1	1
	PB06	I	O	SPI0_CS0			FLASH_CS	EINTB6	Hi	1	1
	PB07	I	O	SPI0_CLK	SD_CLK		FLASH_CLK	EINTB7	Hi	1	1
	PB13	I	O					EINTB13	Hi		1
	PB15	I	O					EINTB15	Hi		1
RESET#	RESET#/CHIP_PWD									1	1
CLOCK	HOSCI/HXTAL1									1	1
	HOSCO/HXTAL2									1	1
	LOSCI/LXTAL1									1	
	LOSCO/LXTAL2									1	
ANT	ANT									1	1

# With or Without LOSC



- The 32768 clock of the XR871GT is sourced from an external crystal.
- The 32768 clock of the XR871ET is sourced from an internal RC circuit and is less accurate than the external crystal.
- Compare XR871ET and XR871GT
  - The RTC clock of the XR871ET is less accurate than that of XR871GT and we need to do clock calibration and network time synchronization.
  - In the standby mode with WLAN connection, the power consumption of XR871ET is 20% higher than that of XR871GT.
  - The frequency of the RC clock will vary with the ambient temperature.