

ECE 255 Introduction To Computer Architecture

Lab 1: Using The Eclipse Integrated Development  
Environment

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# Objectives

An introduction to the ARM architecture and assembly language program procedures. The objectives of this lab are:

- Run and debug programs using the Eclipse Integrated Development Environment
- Use single step execution and breakpoints to analyze programs
- Link and assemble ARM architecture assembly programs
- Load programs onto the ARM development board using Eclipse
- Monitor memory locations and registers to see the response of a program

## Introduction

The programs that we assembled and ran for this experiment were obtained from the ECE 255 course website, and then imported into eclipse as pre-existing projects. There were three separate projects that we built, debugged, and ran for this experiment: lab1a, lab1as, and lab1bs. These programs were fairly simple, the first one just adding the contents of two registers (integer addition) and storing them into the contents of a third register. The second program was similar, but initialized variables x, y, and sum, each with a size of four bytes. The contents of x and y were loaded into registers, the sum of x and y was computed, and stored in another register. The third program we analyzed had the same principle, but with arrays. A loop was employed to add every element of the x array with the y array, and be stored into a sum array.

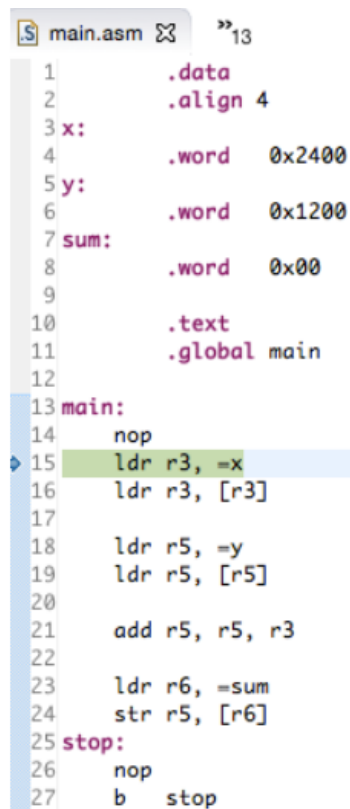
## Discussion

When we executed the first program, register 4 was storing the value 5, and register 5 was storing the value 4. After execution, the contents of register 0 contained the value 9 (the sum of 4 and 5). We changed the operation from add to subtract, and this changed the contents of register 0 after execution of the program. Register 0 contained the integer value 1 (5 - 4, or R4 - R5). This is what we were anticipating before making this change to the program, and we could see this result in the contents of the registers.

```
        . text
        . global main
main:
        nop
        mov     r4, #5
        mov     r5, #4
        add     r0, r4, r5
stop:
        nop
        b       stop
```

**Figure 1: Program 1**

The second program as mentioned added two integer values and stored them in a register, as shown in Figure 2. However, because we were loading a register with the contents of a variable, initialized at a specific memory location, we were able to view those locations in the memory and analyze the content while executing the program step by step. For example, the sum variable was at memory location 0x00, so we were able to see the contents of that location change from 0 to 0x3600 (the sum of x (0x2400) and y(0x1200)). Also, we observed the instructions being transferred into the memory. This program was simple in nature, but the experiment shows clearly how the registers and memory of the ARM architecture respond to a program.



```
1      .data
2      .align 4
3  x:   .word 0x2400
4
5  y:   .word 0x1200
6
7  sum: .word 0x00
8
9
10     .text
11     .global main
12
13 main:
14     nop
15     ldr r3, =x
16     ldr r3, [r3]
17
18     ldr r5, =y
19     ldr r5, [r5]
20
21     add r5, r5, r3
22
23     ldr r6, =sum
24     str r5, [r6]
25 stop:
26     nop
27     b    stop
```

**Figure 2: Program 2**

The third and final program (as mentioned) initialized three arrays (x\_array, y\_array, and sum\_array). The assembly code for this program is shown in Figure 3. The x and y arrays had 8 values each, and were defined as a word, so each element in the arrays had four bytes of storage. The sum array was not initialized, but space was accounted for in the memory. First this program would store the memory location of the beginning of x\_array into register 0, the memory location of y\_array into register 1, and sum\_array into register 2. These instructions are illustrated in Figure 4. Also, the integer value 0 was moved into register 3. Next in the program, a loop was employed to iterate addition through both arrays. First we check if the contents of

register 3 are greater than or equal to the integer value 8, and if it is, we break out of the loop. Register 3 holds our loop index for this program, we add the value 1 each time the loop is complete to keep track. The number 8 is used as comparison because each array has 8 elements, so we only want to execute this loop 8 times. Next this program will load the contents from the memory addresses stored in R0 and R1 into R4 and R5 respectively. Note that R0 and R1 currently contain the memory addresses of the first element in x\_array and y\_array. So now we have the first element of each array stored in two registers, we add them together, and store this value at memory location held in register 2 (the start of the sum array). Next this program iterates the values of R0, R1, and R2 by four (advance by 4 bytes due to 32bit word), so that they now contain the memory addresses of the next element in each array. Our loop index is iterated by 1, and the process repeats 8 times. After execution of this program, the sum array is now full of values that are the sum of x and y, as shown in Figure 5. Program listing is shown in Figure 4.

```

1      .data
2      .align 4
3 x_array:  .word  0x24,0x12,0x05,0x66,0x12,0x01,0x08,0x14
4 y_array:  .word  0x12,0x33,0x21,0x0A,0x15,0x11,0x25,0x99
5 sum_array: .space 32
6
7      .text
8      .global main
9
10 main:    nop
11          ldr    r0, =x_array      @ Load register r0 with the value 5
12          ldr    r1, =y_array
13          ldr    r2, =sum_array
14          mov    r3, #0
15 loop:
16          cmp    r3, #8
17          bge    stop
18          ldr    r4, [r0]          @load content in r0
19          ldr    r5, [r1]          @load content in r1
20          add    r4, r4, r5
21          str    r4, [r2]
22
23          add    r0, r0, #4
24          add    r1, r1, #4
25          add    r2, r2, #4
26          add    r3, r3, #1
27          b      loop
28 stop:
29          nop
30          b      stop

```

**Figure 3: Program 3**



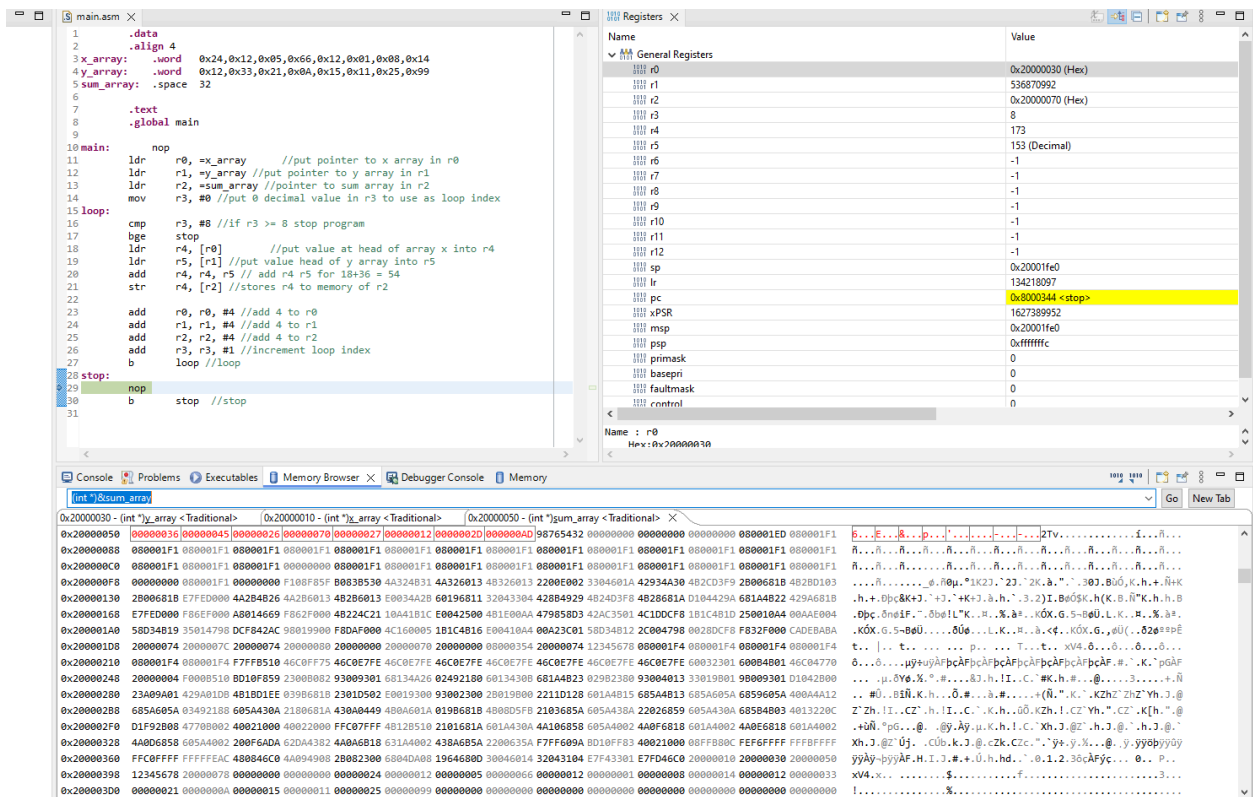


Figure 6: Memory Contents

## Limitations in the Code

From Figure 3 and the relevant discussion behind the functionality of the code, it is clear that that version can only operate on 8 elements of an array. Once the loop index reaches 8, the program reaches the stop point and no more operations are performed. In terms of scalability this program with the current code cannot be scaled to do any operations on the arrays beyond 8 elements. Fortunately the changes one would need to make in order to scale this program beyond the 8 elements (perhaps to 32 as proposed in the lab manual) are very small changes, which can be seen in Figure 6. If you had an array with 32 elements you would only need to change the compare expression, the size of the sum array, and of course make sure to give your `x` and `y` arrays 32 elements. Simply change the number 8 in the `cmp` statement to 32, accordingly set the `sum` array to  $32 \times 4 = 128$  bytes, and set both `x_array` and `y_array` to have 32 elements. These changes are shown below in Figure 7.

```

1      .data
2      .align 4
3  x_array: .word    0x24,0x12,0x05,0x66,0x12,0x01,0x08,0x14
4           .word    0x01,0x02,0x03,0x04,0x05,0x06,0x07,0x08
5           .word    0x01,0x02,0x03,0x04,0x05,0x06,0x07,0x08
6           .word    0x01,0x02,0x03,0x04,0x05,0x06,0x07,0x08      @ x_array now has 32 elements
7
8  y_array: .word    0x12,0x33,0x21,0x0A,0x15,0x11,0x25,0x99
9           .word    0x08,0x07,0x06,0x05,0x04,0x03,0x02,0x01
10          .word    0x08,0x07,0x06,0x05,0x04,0x03,0x02,0x01
11          .word    0x08,0x07,0x06,0x05,0x04,0x03,0x02,0x01      @ y_array now has 32 elements
12
13  sum_array: .space 128 @ changed sum_array space from 32 bytes to 128 bytes to hold 32 elements
14
15      .text
16      .global main
17
18  main:    nop
19          ldr     r0, =x_array      @ Load register r0 with the value 5
20          ldr     r1, =y_array
21          ldr     r2, =sum_array
22          mov     r3, #0
23
24  loop:    cmp     r3, #32 @changed 8 in loop to 32, will now loop until the index is equal or greater than 32
25          bge     stop
26          ldr     r4, [r0]          @load content in r0
27          ldr     r5, [r1]          @load content in r1
28          add     r4, r4, r5
29          str     r4, [r2]
30
31          add     r0, r0, #4
32          add     r1, r1, #4
33          add     r2, r2, #4
34          add     r3, r3, #1
35          b       loop
36
37  stop:    nop
38          b       stop
39

```

**Figure 7: Program 3 Modified for 32 Elements**

## Conclusion

This lab demonstrated the understanding of cross-assemblers and the usage of the Eclipse IDE debugging and memory monitoring tools. Assembly code was translated by Eclipse IDE into machine code for the STM32F051R8T6 microcontroller. Single step execution and breakpoints were used alongside the memory and registry monitors to analyze the execution and response of the program. By stepping into operations one at a time the program's loading of values from memory into the registry were able to be recorded. Then the addition of these values within the loop was able to be tracked one at a time. Finally the storage of these values into a different location in the memory was seen. These lab exercises displayed the foundational principles behind embedded systems.