

# Jameson Thies

jamesonthies@gmail.com

Davis, CA

## EDUCATION

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### **Master of Science in Electrical and Computer Engineering**

September 2018 - Present

University of California, Davis

### **Bachelor of Science in Computer Engineering**

August 2014 - May 2018

San Diego State University

*Cum Laude*

## EXPERIENCE

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### **San Diego State University Research Foundation**

*Undergraduate Research Assistant*

June 2017 – October 2018

*Supervisor – Dr. Amirhossein Alimohammad*

- Researched into efficient FPGA and ASIC data processing applications including brain-computer interfaces through funding from the Center for Sensorimotor Neural Engineering and the SDSU Summer Undergraduate Research Program. Primary focuses include Autoencoder based neural spike compression, and Shallow Neural Network based neural spike classification.

### **Cognuse Inc.**

*Software Engineering Intern*

May 2016 – September 2016

- Developed React/Redux JavaScript applications that adapt to a range of REST API responses creating modular, adaptable applications which allow for accessible data presentation and responsive user interactions.

## SKILLS & INTEREST

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**Skills:** C, C++, Python, MATLAB, Verilog, JavaScript, Node.js, C#, R

**Interest:** Embedded Systems Programming, Parallel Computing, API Integration, Data Science, Digital Circuit Design

## PROJECTS

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### **Reconfigurable FPGA Based Discrete Cosine Transform Architectures**

- Created a fully reconfigurable RTL design for the high throughput computation of Fast DCT algorithms of various lengths
- MATLAB fixed-point simulations and Verilog hardware description
- Poster presentation at 2018 SDSU Research Symposium

### **FPGA MIDI Synthesizer**

- Worked as Project Manager with team of electrical and computer engineering students in the design and implementation of a MIDI Synthesizer based on a Spartan 6 FPGA with a mobile Bluetooth interface
- Presented at San Diego State University Senior Design Day Fall 2017