APB2 peripheral reset register (RCC_APB2RSTR)

Address offset: 0x0C

Reset value: 0x00000 0000

Access: no wait state, word, half-word and byte access

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
Reserved										TIM11 RST	TIM10 RST	TIM9 RST	Reserved		
										rw	rw	rw			

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
ADC3 RST	USART1 RST	TIM8 RST	SPI1 RST	TIM1 RST	ADC2 RST	ADC1 RST	IOPG RST	IOPF RST	IOPE RST	IOPD RST	IOPC RST	IOPB RST	IOPA RST	Res.	AFIO RST
rw	rw	rw	rw	rw	rw	rw	rw	rw	rw	rw	rw	rw	rw	Res.	rw

Bits 31:22 Reserved, must be kept at reset value.

Bit 21 **TIM11RST**: TIM11 timer reset
Set and cleared by software.
0: No effect
1: Reset TIM11 timer

Bit 20 **TIM10RST**: TIM10 timer reset
Set and cleared by software.
0: No effect
1: Reset TIM10 timer

Bit 19 **TIM9RST**: TIM9 timer reset
Set and cleared by software.
0: No effect
1: Reset TIM9 timer

Bits 18:16 Reserved, always read as 0.

Bit 15 **ADC3RST**: ADC3 interface reset
Set and cleared by software.
0: No effect
1: Reset ADC3 interface

Bit 14 **USART1RST**: USART1 reset
Set and cleared by software.
0: No effect
1: Reset USART1

Bit 13 **TIM8RST**: TIM8 timer reset
Set and cleared by software.
0: No effect
1: Reset TIM8 timer

Bit 12 **SPI1RST**: SPI1 reset
Set and cleared by software.
0: No effect
1: Reset SPI1

Bit 11 **TIM1RST**: TIM1 timer reset
Set and cleared by software.
0: No effect
1: Reset TIM1 timer

Bit 10 **ADC2RST**: ADC 2 interface reset
Set and cleared by software.
0: No effect
1: Reset ADC 2 interface

Bit 9 **ADC1RST**: ADC 1 interface reset
Set and cleared by software.
0: No effect
1: Reset ADC 1 interface

Bit 8 **IOPGRST**: IO port G reset
Set and cleared by software.
0: No effect
1: Reset IO port G

Bit 7 **IOPFRST**: IO port F reset
Set and cleared by software.
0: No effect
1: Reset IO port F

Bit 6 **IOPERST**: IO port E reset
Set and cleared by software.
0: No effect
1: Reset IO port E

Bit 5 **IOPDRST**: IO port D reset
Set and cleared by software.
0: No effect
1: Reset IO port D

Bit 4 **IOPCRST**: IO port C reset
Set and cleared by software.
0: No effect
1: Reset IO port C

Bit 3 **IOPBRST**: IO port B reset
Set and cleared by software.
0: No effect
1: Reset IO port B

Bit 2 **IOPARST**: IO port A reset
Set and cleared by software.
0: No effect
1: Reset IO port A

Bit 1 Reserved, must be kept at reset value.

Bit 0 **AFIORST**: Alternate function IO reset
Set and cleared by software.
0: No effect
1: Reset Alternate Function