# NATIONAL UNIVERSITY OF SINGAPORE SCHOOL OF COMPUTING

**MIDTERM TEST - ANSWERS** 

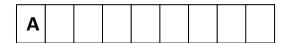
AY2019/20 Semester 1

### **CS2106 – INTRODUCTION TO OPERATING SYSTEMS**

October 2019 Time Allowed: **1 hour** 

### **INSTRUCTIONS**

- 1. This question paper contains **SEVENTEEN (17)** questions and comprises **SIXTEEN (16)** printed pages.
- 2. Maximum score is **40 marks** and counts towards 20% of CS2106 grade.
- 3. Write legibly with a pen or pencil. MCQ form should be filled out using pencil.
- 4. This is a **CLOSED BOOK** test. However, a single-sheet double-sided A4 reference sheet is allowed.
- 5. Write your **STUDENT NUMBER** below with a pen.



Questions in Part B	Marks
13	/3
14	/3
15	/7
16	/5
17	/5
Total	/23

## Part A: MCQ questions.

Questions 1-12 from this section should be answered using the MCQ bubble form provided. Answers given in the midterm paper will not be considered for grading.

- 1. [1 mark] What are the advantages of microkernel design over monolithic design in Operating Systems?
  - i. Kernel is generally more robust.
  - ii. Better performance.
  - iii. Kernel is extensible.
  - iv. Better isolation and protection between kernel and process management services
  - A. i. and ii. only.
  - B. i. and ii. and iii. only.
  - C. ii. and iii. only.
  - D. i., iii. and iv. only.
  - E. iii. and iv. only.
- 2. [1 mark] Ubuntu 16.04 (on our lab machines) is using a microkernel.
  - A. True.
  - B. False.
  - C. It has elements of both microkernel and monolithic kernel.
  - D. I am not sure.
  - E. I forgot.
- 3. [1 mark] The modern operating system serves as an abstraction that:
  - i. Allows the user of a computer to directly interact with the hardware
  - ii. Manages and allocates hardware resources to ensure efficient and fair usage
  - iii. Controls the execution of processes
  - iv. Provides security and protection for different executing programs
  - A. ii. and iii. only.
  - B. i. only.
  - C. iii. and iv. only.
  - D. ii., iii. and iv. only.
  - E. None of the above. only.
- 4. [1 mark] A context switch is performed because process A was "picked" by the OS scheduler for execution. Considering the generic five-states process model, process A transits between the following states:
  - A. Blocked -> Ready
  - B. Blocked -> Running
  - C. Ready -> Running
  - D. Running -> Ready
  - E. None of the above

- 5. [1 mark] Process A performs the exit() system call. Considering the generic five-states process model, process A transits between the following states:
  - A. Running -> Blocked
  - B. Running -> Ready
  - C. Running -> Running
  - D. Ready -> Ready
  - E. None of the above
- 6. [1 mark] Assume a single processor (core) system and no simultaneous multi-threading (ignore this term if you do not know what it is). Consider the generic five-states process model. How many processes can be in READY state at the same time? How many processes can be in RUNNING state at the same time?
  - A. Exactly one process in READY state and one process in RUNNING state.
  - B. Any number of processes can be in READY state, but at most one process in RUNNING state.
  - C. The operating system decides on the fly how many processes are in READY and RUNNING state.
  - D. Exactly one process is in RUNNING state, while there might be no process in READY state.
  - E. Multiple processes can be in READY state and multiple processes can be in RUNNING state.
- 7. [2 marks] Consider the following situations for using the stack pointer and the frame pointer.

Assume that the stack frame layout is the same as the one given in the lecture. Assume there is an instruction named: return <offset>(register) (e.g. return -4(\$fp)) that returns to the address at offset + <value in register>.

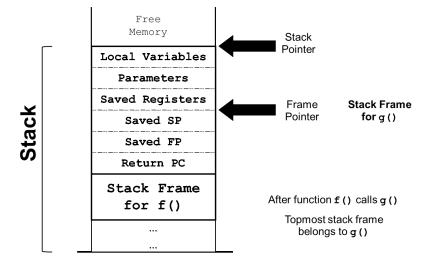


Figure 1: Stack frame layout from lecture notes.

i. Allocating space for local variables in the middle of a function, e.g.:

- ii. Accessing the arguments passed to the current function
- iii. Returning to the caller function after the stack and frame pointer is restored to the caller's original values.

In what situations would we use the stack pointer instead of the frame pointer?

- A. i. only.
- B. ii. and iii. only.
- C. i. and iii. only.
- D. All of the above.
- E. None of the above.
- 8. [2 marks] C has a feature called variable-length arrays (VLAs). It allows programmers to write code as follows:

```
Line#
      Code Snippet
1
      int f(int input) {
2
           int arr[input];
3
           int x = 5; // Point A
4
           return arr[0];
5
6
      int main(void) {
7
           int input;
8
           scanf("%d", &input);
9
           int retval = f(input);
10
           printf("f returned %d\n", retval);
11
```

The highlighted code (line 2) is an array that has space allocated based on user input at runtime. In gcc, arr would be allocated **on the stack**.

Assume the stack frame of f (at Point A in line 3) looks like the image below given any value of input, and the stack grows upwards (towards the top of this page). Also assume that the stack pointer is currently right above x, and the frame pointer is currently at input.

```
x
```

```
arr[0]
arr[1]
... rest of arr ...
arr[input - 2]
arr[input - 1]
input
<saved fp, sp, return address, etc>
<main's stack frame>
```

Figure 2: Stack frame at Point A in line 3.

#### Consider the following statements for Point A in line 3:

- i. f can reference the variable x using a constant offset from the frame pointer, e.g. offset (fp).
- ii. f can reference the value in arr[0] using a constant offset from the stack
  pointer, e.g. offset (\$sp).
- iii. f can reference the variable x using a constant offset from the stack pointer, e.g. offset (sp).

#### Which statements are true at Point A in line 3?

- A. i. only
- B. ii. and iii. only
- C. i. and iii. only
- D. All of the above
- E. None of the above
- 9. [2 marks] The creator of the Linux kernel (Linus Torvalds) has expressed strong displeasure about using VLAs in kernel code. What are some valid criticisms of VLAs based on your knowledge of VLAs from Question 8?
  - i. VLAs are more inconvenient for C programmers to use compared to using malloc for the same purpose.
  - ii. Using VLAs requires more code to calculate the addresses of locations on the stack frame than a fixed-size array declared at compile time.
  - iii. The size of the stack frame changes based on the size of the VLA, which makes it impossible to locate certain variables.
  - A. i. only.
  - B. ii. only.
  - C. ii. and iii. only.
  - D. All of the above.
  - E. None of the above.

## 10. [2 marks] Consider two threads executing once the following C code, and accessing shared variables a, b and c:

Line#	Code Snippets	
1	Initialization	
2	int a = 4;	
3	int b = 0;	
4	int c = 0;	
5	Thread 1	
6	b = 10;	
7	a = -3;	
8	8 Thread 2	
9	if (a < 0) {	
10	c = b - a;	
11	} else {	
12	c = b + a;	
13	}	

You can assume that each statement can be written using store and load operations (and these operations are not interrupted while running; they are atomic). Furthermore, the order of execution of statements within each thread is preserved by the C compiler and the hardware so it matches the code above.

What are the possible values for variable **c** after both threads complete?

- A. -4, 0, 4.
- B. -3, -4, 7, 13, 14.
- C. **3**, 4, **7**, 13, 14.
- D. 4, 7, 13, 14.
- E. None of the above.
- 11. [1 mark] In Question 10, **c** might have multiple values when both threads complete because the code has:
  - A. Critical section.
  - B. Mutual exclusion.
  - C. Deadlock.
  - D. Race condition.
  - E. Exception.

12. [2 marks] In cooperating concurrent tasks, sometimes we need to ensure that all N tasks have reached a certain point in the code before proceeding. This specific synchronization mechanism is commonly known as a **barrier**. Assume that these tasks are executing using threads. Consider the barrier we implemented in Tutorial 5:

```
Line#
      Code Snippets
1
      Initialization:
2
           int arrived = 0;
                                   //shared variable
3
           Semaphore mutex = 1;
4
           Semaphore waitQ = 0;
5
      Barrier(N) {
6
           wait(mutex);
7
           arrived ++;
8
           signal(mutex);
9
           if (arrived == N)
10
11
                 signal(waitQ);
12
           wait(waitQ);
13
           signal(waitQ);
14
```

Choose the statement that is **false** about this barrier:

- A. waitQ will always have a value of 1 at the end of the execution of function Barrier (line 13).
- B. This barrier cannot be reused to synchronize a set of N threads because waitQ will not be 0 at the end of the execution.
- C. This barrier cannot be reused to synchronize a set of N threads because arrived will not be 0 at the end of the execution.
- D. We have a race condition because arrived is accessed outside the critical section.
- E. The barrier implementation is correct, and no changes are needed.

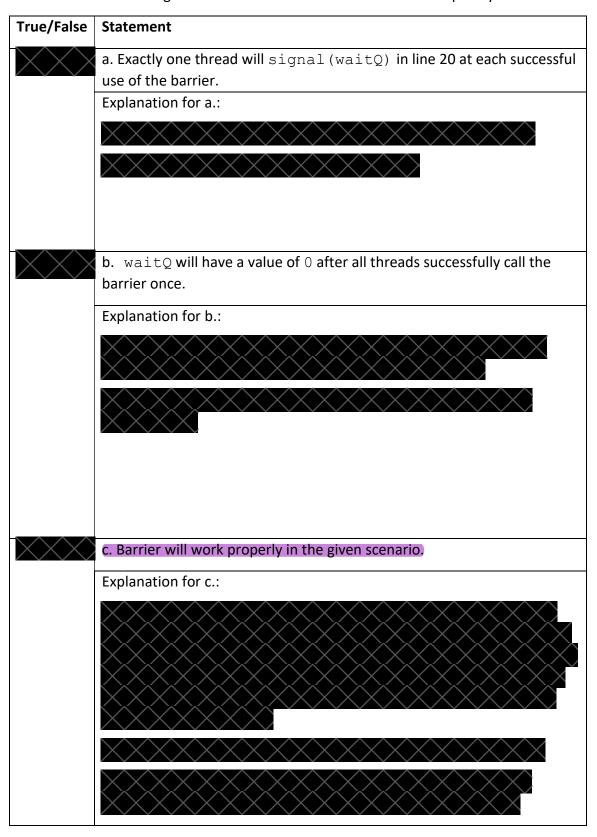


## Part B. Short Questions

13. [3 mark] Assume you want to write a reusable barrier: N threads can use the same function Reusable\_barrier() to block multiple times during their execution and wait for all the other threads to reach the same point. You may assume that all threads use this barrier synchronization in for loop. The code executed by each thread, and the attempt to implement the barrier follow:

```
Line#
     Code Snippets
1
     A thread executes the following statements:
2
     while (true) {
3
           Compute();
4
           Reusable barrier(N);
5
           Critical code after barrier();
6
     Initialization for barrier:
           int arrived = 0; //shared variable
8
9
           Semaphore mutex = 1;
10
           Semaphore waitQ = 1; //NOTE the change
11
12
     Reusable barrier(N) {
13
           wait(mutex);
14
           if (arrived == 0) {
15
                wait(waitQ);
16
17
           arrived++;
18
           if (arrived == N) {
19
                arrived = 0;
20
                signal(waitQ);
21
22
           signal(mutex);
23
24
           wait(waitQ);
25
           signal(waitQ);
26
```

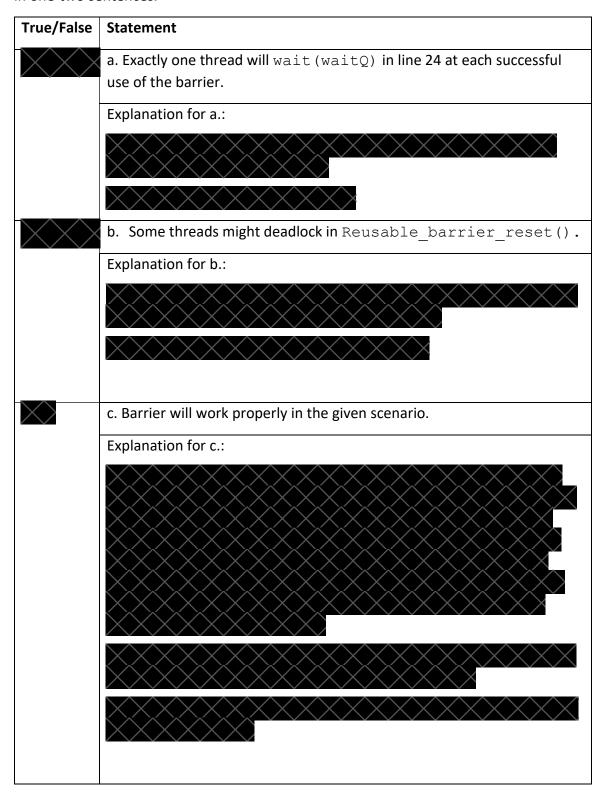
For each of the following statements state if it is true or false and explain your decision.



14. [3 mark] Assume a similar scenario to Question 13 (but with a different implementation below). The following code has been written to implement the reusable barrier. N threads can use the same function Reusable\_barrier\_block() to block multiple times during their execution and wait for all the other threads to reach the same point. After using Reusable\_barrier\_block, the critical code can be executed, followed by a call to Reusable\_barrier\_reset().

```
Line#
     Code Snippets
     A thread executes the following statements:
1
     while (true) {
2
           Compute();
3
           Reusable barrier block (N);
           Critical code after barrier();
4
5
           Reusable barrier reset (N);
6
7
     Initialization:
8
           int arrived = 0;
9
           Semaphore mutex = 1;
10
           Semaphore waitQ = 0;
     Reusable barrier block (N) {
11
12
           wait(mutex);
13
           arrived ++;
14
           if (arrived == N)
15
                signal(waitQ);
16
           signal(mutex);
17
           wait(waitQ);
18
           signal(waitQ);
19
20
     Reusable barrier reset(N) {
21
           wait (mutex);
22
           arrived --;
23
           if (arrived == 0)
                wait (waitQ);
24
25
           signal(mutex);
26
```

For each of the following statements state if it is true or false and explain your decision in one-two sentences.



15. Answer each of the follo	owing questions briefly (in one or two sentences). State your
assumptions, if any.	

a. [1 mark] Under what conditions does FIFO scheduling result in the shortest possib	le
average response time?	



b. [1 mark] Under what conditions does round robin scheduling behave identically to FIFO?



c. [1 mark] Under what conditions does round robin scheduling perform poorly compared to FIFO?





d. [1 mark] In the situation you described in part c. above, does reducing the time slice for round-robin scheduling help or hurt its performance relative to FIFO? Why?



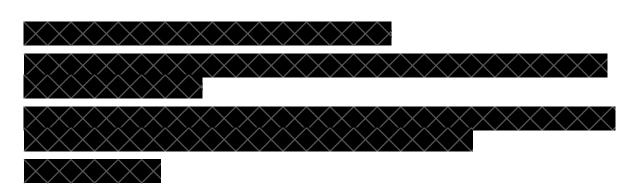


e. [1 mark] Which scheduling algorithm gives a higher priority to I/O-bound processes for the CPU?



f. [2 marks] Do you think that a CPU-bound (CPU intensive) process should be given a higher priority for I/O than an I/O-bound process? Justify your answer.





16. [5 marks] You are required to implement an intra-process mutual exclusion mechanism (a lock) **using Unix pipes**. Your implementation **should not use mutex** (pthread\_mutex) or semaphore (sem), or any other synchronization construct.

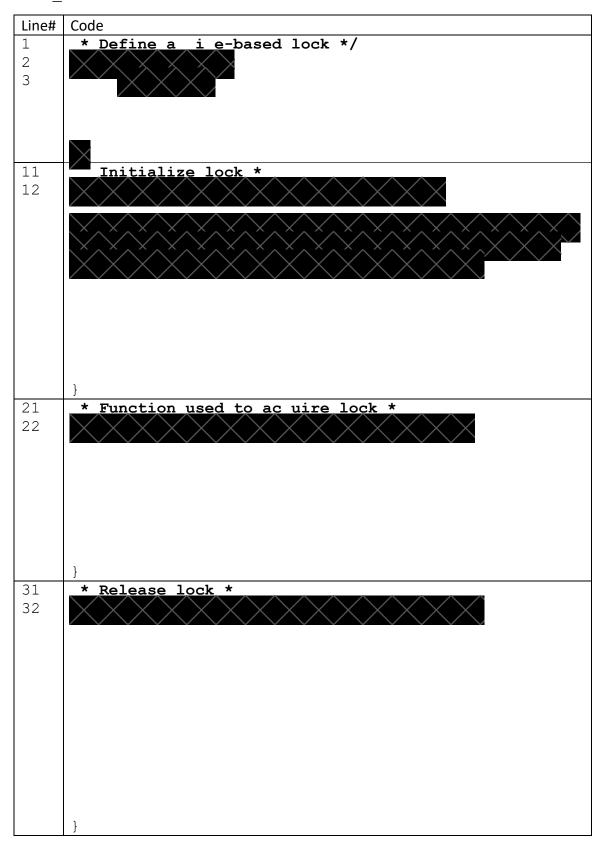
Information to refresh your memory:

- In multithreaded processes, file descriptors are shared between all threads in a process. If multiple threads simultaneously call read () on a file descriptor, only one thread will be successful in reading any available data up to the buffer size provided, the others will remain blocked until more data is available to be read.
- The read end of a pipe is at index 0, the write end at index 1.
- System calls signatures for read, write, open, close, pipe (some might not be needed):

```
int pipe(int pipefd[2]);
int open(const char *pathname, int flags, mode_t mode);
int close(int fd);
ssize_t read(int fd, void *buf, size_t count);
ssize_t write(int fd, const void *buf, size_t count);
```

• Marks will not be deducted for minor syntax errors.

Write your lock implementation below in the spaces provided. Definition of the pipe-based lock (struct pipelock) should be complete, but feel free to add any other elements you might need. You need to write code for lock\_init, lock\_acquire, and lock release.



17. Consider the following C code. The executables programA and programB are located in the system path (they are called successfully from the program), and they simply print a message indicating the pid of the process executing them and then terminate. Assume appropriate wait() calls are used at line 22.

```
Line#
      Code Snippets
1
      int main() {
2
            char* argv1[] = {"programA", NULL};
3
            char* argv2[] = {"programB", NULL};
4
            char* env[] = {NULL};
5
            printf("%d is entering fork.\n", getpid());
6
            int index = 0;
7
8
            int pid = fork();
9
10
           pid = fork();
11
12
            if (pid == 0 && index == 0) {
13
                  index++;
14
                 pid = fork();
15
                  if (pid == 0) {
16
                       execve("programA", NULL, NULL);
17
                  }
18
19
            if (pid != 0 && index == 0) {
20
                 pid = fork();
                  execve("programB", NULL, NULL);
21
22
23
            . . .// wait processes
24
            printf("%d is exiting from fork.\n", getpid());
25
            return 0;
26
```

a) [2 marks] Which program is the original process executing when that process terminates?



#### b) [3 marks] How many processes terminate in each of the relevant programs?

Program	Number of processes that terminate there
Current program exiting at line 24.	
programA	
programB	