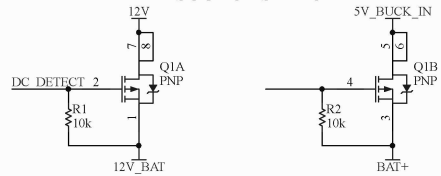
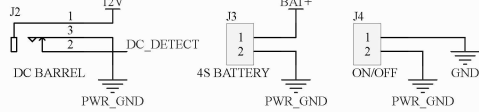


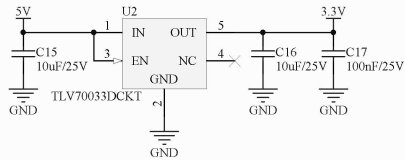
## SOURCE SELECT



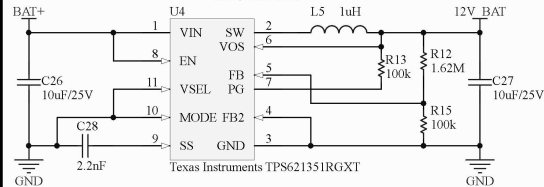
## POWER INPUT



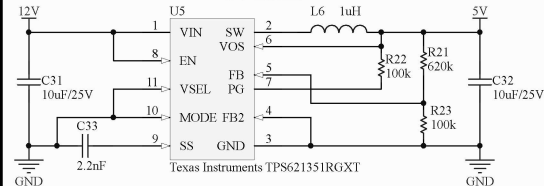
## 3.3V RAIL



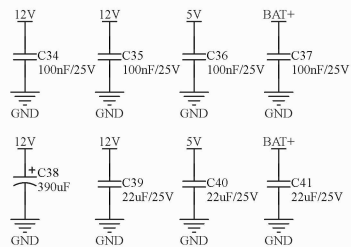
## 12V RAIL



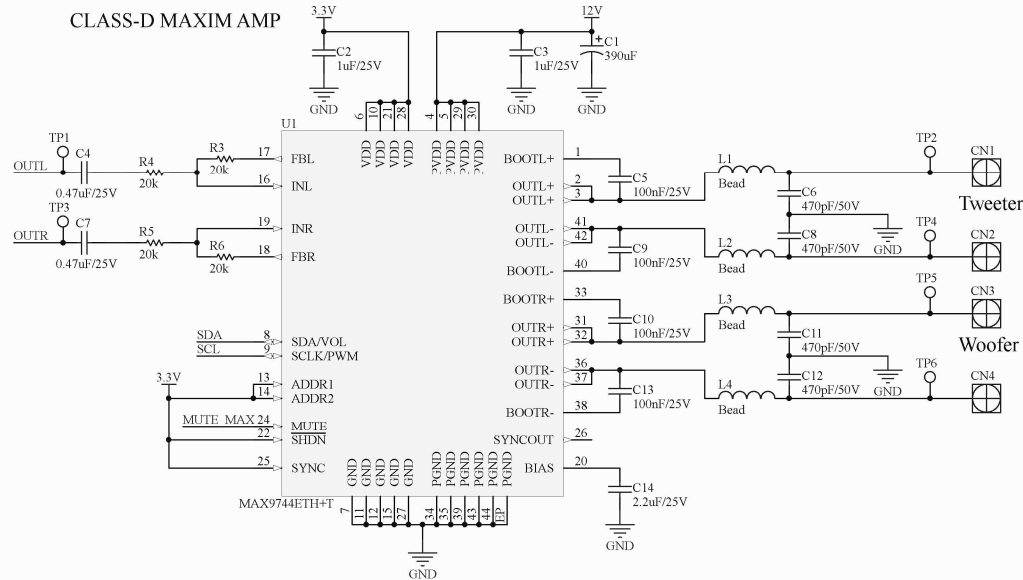
## 5V RAIL



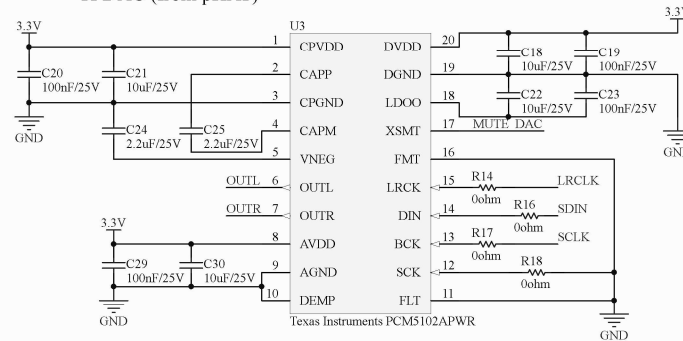
## BULK CAPACITANCE/DECOUPLING



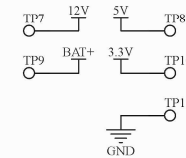
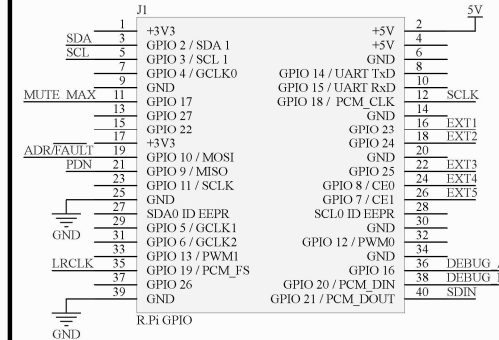
## CLASS-D MAXIM AMP



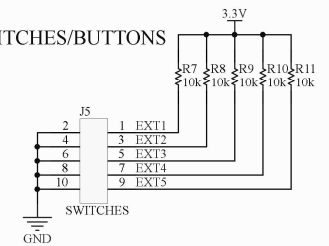
## TI DAC (from pHAT)



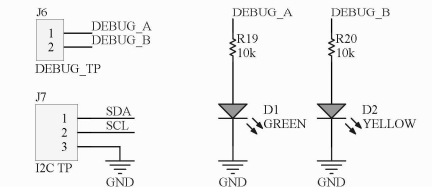
## RASPBERRY PI ZERO W HEADER



## EXTERNAL SWITCHES/BUTTONS



## DEBUG PINS



Title		
Size	Number	Revision
A3		
Date:	16/04/2019	Sheet of
File:	Max_Main.SchDoc	Drawn By: