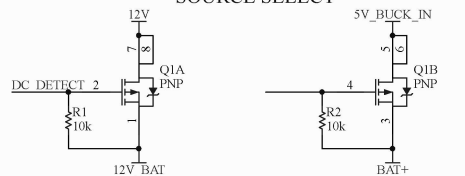
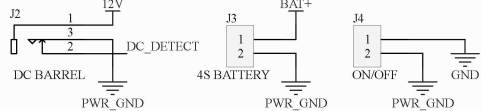


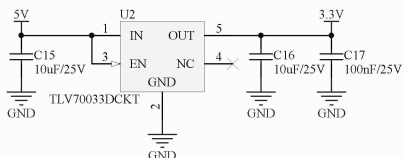
SOURCE SELECT



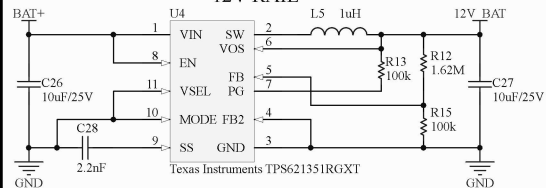
POWER INPUT



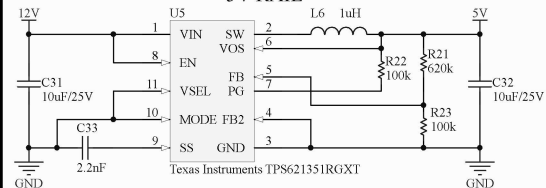
3.3V RAIL



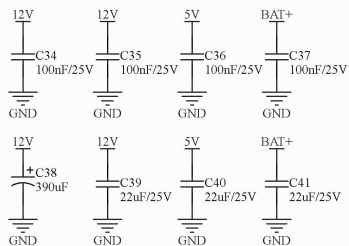
12V RAIL



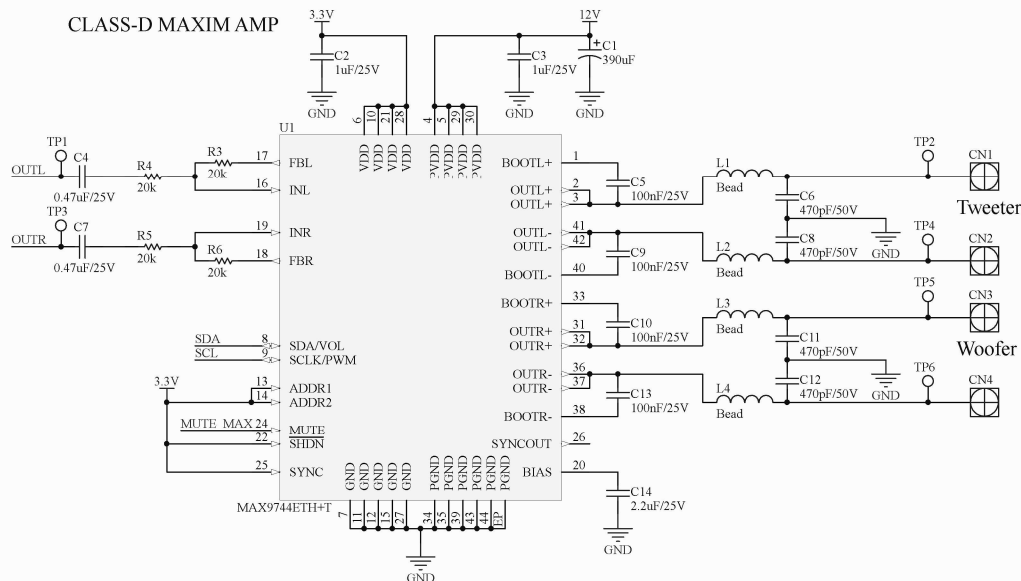
5V RAIL



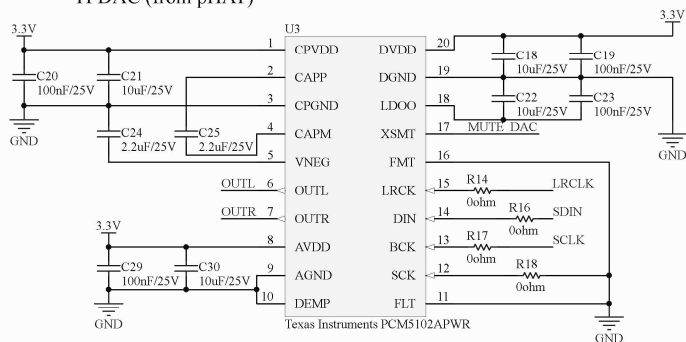
BULK CAPACITANCE/DECOUPLING



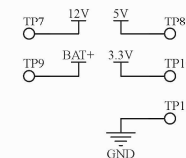
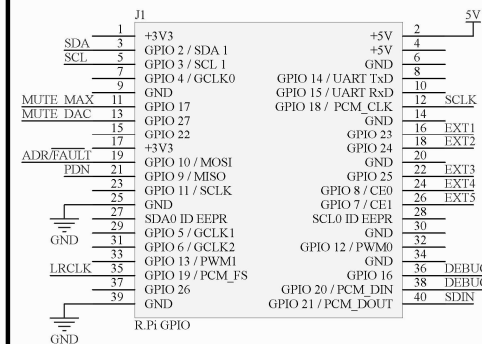
CLASS-D MAXIM AMP



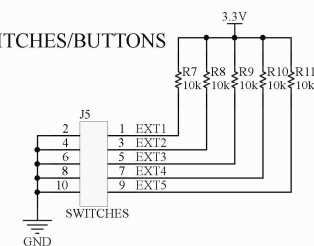
TI DAC (from pHAT)



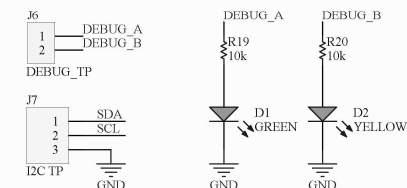
RASPBERRY PI ZERO W HEADER



EXTERNAL SWITCHES/BUTTONS



DEBUG PINS



Title			
Size A3	Number		Revision
Date:	17/05/2019	Sheet	of
File:	Max_Muin.SchDoe	Drawn By:	