VEERA JANARDHANA ACHARI DURUGADDA

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- indian
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- 📅 31-12-1999
- in https://www.linkedin.com/in/veera-janardhana-achari-6666081a6/

🎓 Education

B.Tech, Electronics and Communication Engineering, KL UNIVERSITY

2018 – 2022 | GUNTUR, INDIA

- CGPA-8.52

INTERMEDIATE, NARAYANA JR COLLEGE

2016 – 2018 | KURNOOL, INDIA

- PERCENTAGE - 95.2%

SSC, GOOD SHEPHERD EM SCHOOL

2015 – 2016 | KURNOOL, INDIA

- GPA - 9

🎗 INTERNSHIP

VI SOLUTIONS,INTERN

06-2020 – 07-2020 | BANGLORE, INDIA

- Gained knowledge on NI Labview software

📜 certifications

VLSI Design, Embedded IOT Programming

VLSI CAD Part II: Layout

NDG LINUX UNHACTED
LEVEL 1

GIT

🤝 Interpersonal skills

- Positive Attitude
- Hard Working
- Flexible

🎸 Hobbies

- Playing Handball
- Listening to music

🏅 career objective

- I will be able to analyze the things and enhances the knowledge for the unknown skills in solving problems.

🧠 Skills

C, PYTHON, JAVA, MATLAB, LABVIEW, GIT, VERILOG, VLSI, EMBEBBED SYSTEMS

🎲 Projects

a comparison study on low power adders using micro wind EDA

Design a low drop out voltage regulator for a microprocessor

Examination Data Management using C Programming

Smart blind stick in all direction detection using arduino

🗄 MOOCS

Hardware Description Languages for FPGA Design

Introduction to FPGA Design for Embedded Systems

🧩 Co-Curricular Activities

- Interview skill and time management Organized by: multi arts
- Ethical hacking Organized by: #include by KL university

✍ Declaration

I hereby declare that the above sited information is true to the best of my knowledge and belief.

D VEERA JANARDHANA ACHARI
KURNOOL