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Analog Integrated Circuit Design - Why?

analog signal • Many electronic systems, particularly those dealing with low signal amplitudes or very high frequency required analog approach • Lots of most challenging design problems are analog • Good analog circuit designers are scarce (very well compensated, gain lots of respect, regarded as "artists"

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to the design of a comparator. Random circuit noise can cause the output to change from one logic level to the other, even when the comparator input is held constant. Hence, in order to measure the input offset ... Analog Integrated Circuit Design 2nd Edition ...

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Integrated circuit design, or IC design, is a subset of electronics engineering, encompassing the particular logic and circuit design techniques required to design integrated circuits, or ICs. ICs consist of miniaturized electronic components built into an electrical network on a monolithic semiconductor substrate by photolithography.

Integrated circuit design - Wikipedia

Yes – your interpretation is consistent with equation (1.121). This definition is particularly suitable for CMOS digital logic circuits where the MOSFETs act as switches that are OFF when V GS = 0. In analog circuits, sometimes the source (and even the body terminal) may not be at ground.

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