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to the design of a comparator. Random circuit noise can cause the output to change from one logic level to the other, even when the comparator input is held constant. Hence, in order to measure the input offset ... Analog Integrated Circuit Design 2nd Edition ...

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He is an appointed member of the Administrative Committee of the IEEE Solid-State Circuits Society, a member and past chair of the Analog Signal Processing Technical Committee for the IEEE Circuits and Systems Society, and a past member and chair of the Wireline Communications subcommittee of the Custom Integrated Circuits Conference.

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Errata | Analog Integrated Circuit Design

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Integrated circuit - Wikipedia

EECE488: Analog CMOS Integrated Circuit Design Introduction and Background Shahriar Mirabbasi Department of Electrical and Computer Engineering University of British Columbia shahriar@ece.ubc.ca Technical contributions of Pedram Lajevardi in revising the slides is greatly acknowledged. SM 2 EECE 488 - Set 1: Introduction and Background Marking

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