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42. In 8085, the pins for SID and SOD are. A. 4 and 5 respectively B. 5 and 4 respectively C. 3 and 4 respectively D. 4 and 3 respectively. Answer: B. 43. Assertion (A): Each memory chip has its own address latch. Reason (R): ALE signal comes out of microprocessor 8085 and goes to memory chip. A. Both A and R are correct and R is correct explanation of A B.

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Paper : Objective Questions for 8085 Microprocessor. 1. In Synchronous data Transfer type both Transmitter and Receiver will operate in a) Same Clock pulse b) Different Clock pulse c) None of the above 2. The term PSW Program Status word refers a) Accumulator & Flag register b) H and L register c) Accumulator & Instruction register d) B and C register 3.

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20. BHE of 8086 microprocessor signal is used to interface the a) Even bank memory b) Odd bank memory c) I/O d) DMA 21. In 8086 microprocessor the following has the highest priority among all type interrupts. a) NMI b) DIV 0 c) TYPE 255 d) OVER FLOW 22. In 8086 microprocessor one of the following statements is not true.

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8085 8086 MICROPROCESSOR VIVA QUESTIONS AND ANSWERS

28. List all the interrupt signals of 8085 microprocessor. 29. Write short note on evolution of microprocessors. 30. Explain the functions of the ALE and IO/M signals of the 8085 microprocessor. 31. List the sequence of events that occurs when the 8085 MPU reads from memory. 32.

Microprocessor Qbank | Central Processing Unit | Input/Output

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