
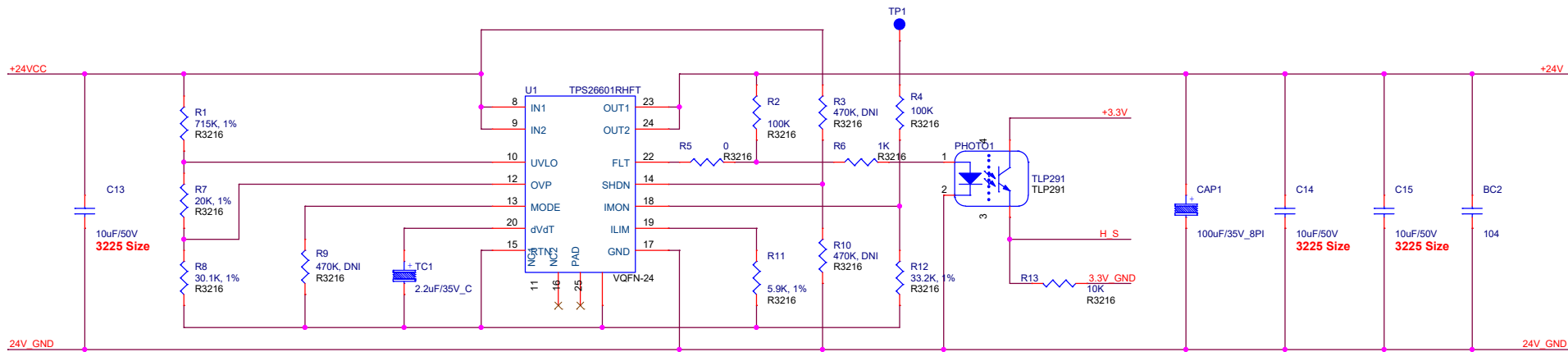
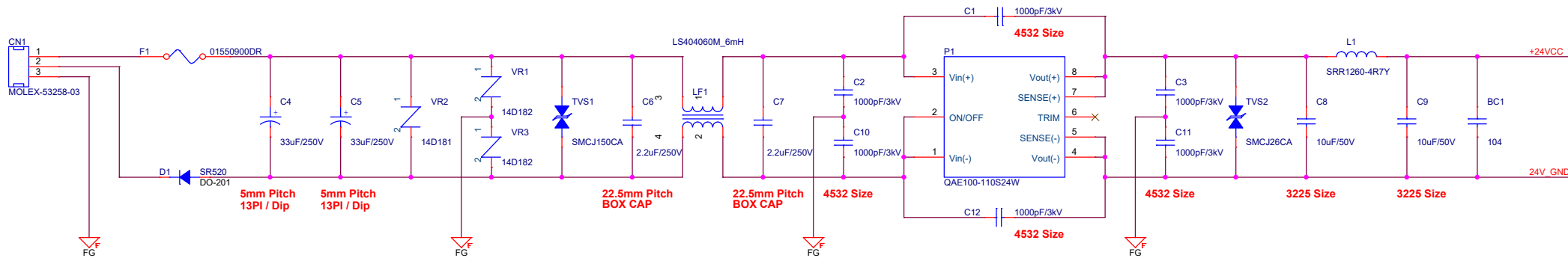
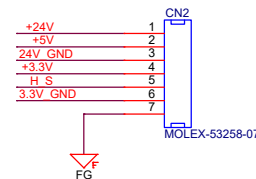
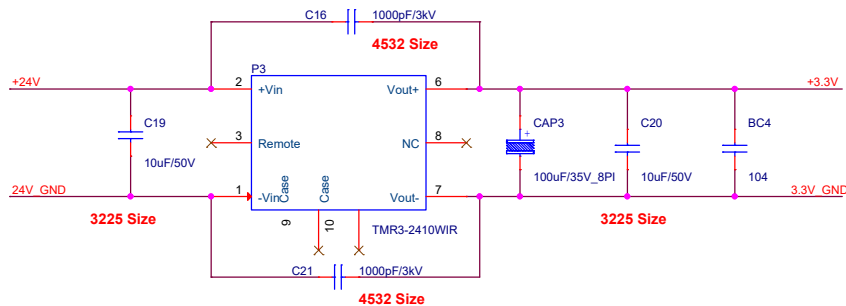
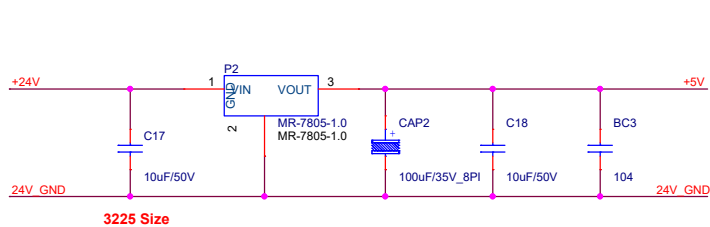


회로도 Version	PCB Version	변경일	변경내역
V1.0	V1.0	2024년 10월 21일	Initial Release for Rev 1.0

Approved	xx. XXX		Title SL-DEOD/DEPC Power Board					
Checked	-		Sub Title REVISION HISTORY					
Designed	xx. XXXX							
Doc. No.			Size	A3	Version	1.0	Page No.	1 of 2
Reference Date			<div> शलوم انجنيئرنگ كو. لٹڊ. SHALOM ENGINEERING CO., LTD.</div>					



* Note : Current Limit = 2A



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Checked	-	Sub Title	POWER			
Designed						
Doc. No.		Size	A3	Version	1.0	Page No. 2 of 2
Reference Date						