

Heaven's Light is Our Guide
Rajshahi University of Engineering and Technology



Course Code

ECE 4128

Course Title

VLSI Design Sessional

Lab Reports

Submitted to

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Exp No.	Experiment Name	Date
1	Implementation of CMOS Inverter.	July 4, 2025
2	Implementation of NMOS Ratio-less Inverter.	July 4, 2025
3	Implementation NAND Gate and NOR Gate Using CMOS	August 8, 2025
4	Design and Observe the Characteristics Curve of CMOS Circuit	September 9, 2025
5	Study and Simulation of 1-Bit SRAM Using Microwind	October 12, 2025
6	Implementation of Half Adder Circuit Using CMOS in Microwind	October 20, 2025