Design of Cascaded H-bridge Multi-level Covnerter for Power Coolant Pump

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Abstract— This paper discussed the analysis and simulation of cascaded H-bridge (CHB) multi-level converter for power coolant pump with demand on high reliability. Multi-level inverters are designed to control medium-voltage motors, energy conversion, active power filtering, and many other applications. Compared to two-level converters, they achieve less voltage distortion (smoother and less distorted AC output voltage) and higher reliability. First, the function of the CHB converter and the most suitable control method is described in detail. In the last part, the 13-level converter is simulated in the normal operation state and the fault operation state. In the operation under normal conditions, phase voltage, line-to-line voltages, and degree of voltage distortion are displayed. In the operation under fault condition, where some of the converter cells are bypassed, line-to-line voltages and torque are displayed.

Keywords—Cascaded H-bridge (CHB) converters, power coolant pump, PS-PWM method, fault operation, simulation

I. INTRODUCTION

A technology enabling massive development of mediumvoltage high-power applications is based on multilevel converters, which have a wide range of usage [1]-[3]. In transport, multi-level converters are used as traction drives for trains, electric cars, and ship propulsion [4]-[5]. An interesting field of application is also the energy conversion and treatment including interfacing units between grid and renewable energy from solar or wind plants, reactive power compensation, and active power filtering [6]-[10]. Another application may be the power interface between two grids with other frequencies, such as powering a 16.7 Hz singlephase grid (for railway) by a 50 Hz three-phase grid [11]. The last applications can be found in the petrochemical industry (compressors and pumps), the mining industry (conveyors for ore transportation), the cement industry (power fans) and the metal industry (rolling mills) [4], [12]-[13].

Multi-level converter technology provides more than two voltage levels of output voltage, which means it is a sum of voltage levels with less distortion, less switching frequency, and higher efficiency. The most widespread topology of the power converter is neutral point clamped (NPC) most often in the form with three-level output. This topology reduces the number of power devices and volume of the converter, but the main drawbacks are more complicated control (especially at five and more levels inverters) and reduced reliability. A very similar topology is a flying capacitor (FC) converter. The major difference is that a flying capacitor is used instead of a clamping diode. The advantage of this topology is the possibility to increase the number of voltage levels, which causes greater stability and redundancy. Perhaps the main drawback is the large and expensive flying capacitors. Other topology of multilevel converters represents cascade H-bridges converters with a serious

connection of H-bridges. They are characterized by great reliability and redundancy, but with increasing volume and cost of the converter. Moreover, each H-bridge requires its own insulated DC source [1], [14]-[15]. Modification of CHB topology is modular multilevel converters (M2C) [16]. This is a very similar concept to that of CHB but with the difference that H-bridges are replaced by half-bridge with a capacitor.

This paper investigates the conception of multilevel medium-voltage converter for power coolant pump with demand on high reliability. Failure could cause serious material damage or death. The main attention is dedicated to the analysis of the CHB converter which is the most suitable variant for this application. Some characteristics of CHB converter were simulated for motor in open-loop control such as line-to-line voltages, phase voltage and spectrum of harmonics. The last part is devoted to the simulation converter under fault condition, where all line-to-line voltages, torque, and more are shown.

II. REVIEW OF CHB CONVERTERS

Series connection of two or more one phase H-bridges provides CHB multilevel converter. Example of one phase CHB converter is illustrated in Fig. 1. Single H-bridge can reach three different voltage levels and four possible switching states. For example, in the H-bridge A, the positive output voltage of DC link is achieved by switch on S_{A1} and S_{A4} , the negative output voltage is achieved by switch on S_{A1} and S_{A4} and the last one, zero voltage can be generated by switch on S_{A1} and S_{A2} or S_{A3} and S_{A4} . To avoid DC-link short-circuit it is forbidden to switch on two semiconductors above at the same time. The inverter H-bridges are connected in series and that will make possible to generate different levels of output voltage when total inverter voltage output is increasing. Count of output voltage levels CHB inverters with k H-bridges is 2k+1.

Each single H-bridge has four possible switching states, where two of them have redundant output. In the case series connection of two H-bridges, sixteen possible switching states arise and the number of redundancies increases.

The main advantage of this topology of the multilevel converter is its high degree of redundancy. As already discussed when the number of H-bridges grows, the redundancy rate is also increasing (enable fault-tolerant operation). Another advantage is the fact that the total output voltage of the converter can be many times greater than the blocking voltage on the semiconductors. Hence, semiconductors have to block only the voltage of the insulated DC source, not the total DC-link voltage of the converter. A major drawback is the need for each H-bridge to provide its own insulated voltage source. A transformer with several secondary windings (according to the number of H-bridges) is most often used.

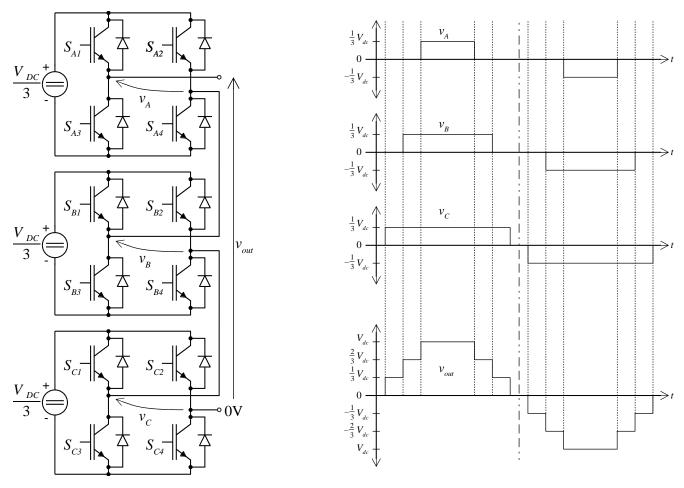


Fig. 1: Seven-level cascaded H-bridge converter: a) power circuit and b) single phase output.

Each secondary winding requires its own rectifier since the volume and cost of the converter increases.

In this application reliability is the main aspect therefore this high-voltage converter topology appears to be the most appropriate.

III. PHASE-SHIFTED PWM MODULATION METHOD

The most optimal method for control CHB inverters is phase-shifted PWM (PS-PWM) techniques. Other control methods for CHB converters are described in [17]. One cell of CHB converter represents a three-level converter that can be controlled by traditional bipolar and unipolar PWM techniques. Each cell of CHB converter can be modulated independently of the others, but using the same carrier triangle signal for all cells. A phase shift is applied to triangle carrier signal for each cell. For better understanding. the implementation diagram of the PS-PWM method for the seven-level CHB is illustrated in Fig. 2. It has been shown that the lowest distortion of the output voltage is achieved by having a phase shift of 180° or 360° / x, where x is the number of cells in the converter. In this way, the output voltage will have a stepped waveform with corresponding voltage levels. An example principle of generation output voltage for seven-level CHB is shown in Fig. 3. Note that all voltages are in per units.

The power is equally distributed over each cell because the cells are controlled by the same control signal and a carrier triangle signal at the same frequency, which is a great advantage. Another advantage is a fact, that phase shift of

triangle carrier signal for each cell cause *x* times higher frequencies of PWM than the carrier triangle signal alone. This is the reason, why it is possible to achieve low THD and low switching losses of switch device with relatively small frequencies of carrier triangle signal.

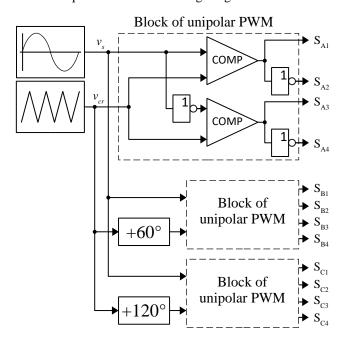


Fig. 2: Three-cell PS-PWM modulation method diagram.

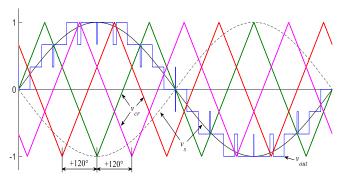


Fig. 3: Three-cell (seven-level) PS-PWM output waveform.

IV. FAUL-TOLERANT OPERATION

A very interesting feature of the CHB multilevel converter is the ability to operate even in case of any power cell failure. The main reason is that the CHB converters have a modular and easily extendable structure and also have several redundant switching states. This allows reducing the machine stop to a minimum.

In general, several types of faults can occur in power converters. Firstly, a failure in dc-link (dc-link capacitor short-circuit, dc-link short-circuit to ground), line-to-line short-circuit or short-circuit to ground at the converter output, or failure of the switching device (short-circuit or open circuit).

Fault-tolerant operation requires to diagnostic the type of fault and where it has occurred as soon as possible. The most appropriate way how to detect faults is to monitor voltage and current across all the components (dc-link, switching device, load). The main drawback is the high cost of the diagnostic equipment and a large number of components. A more efficient way of detecting faults is by measuring voltage and current on the load. The advantage is lower cost and number of measuring components, but the main drawback is the detection dynamics which depend on the type of load.

Fault detection methods are analysed in [18]. The auxiliary switch is connected to each H-bridge (Fig. 4) and can bypass the power cell in case of failure. This will reduce the

One Cell of CHB Converter

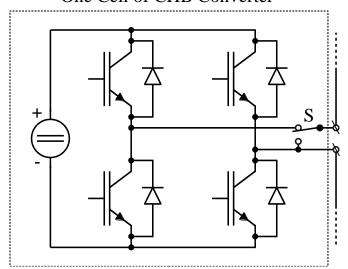


Fig. 4: One cell of CHB converter with auxiliary switch.

output voltage in the failed phase and the total output three-phase voltage will become unbalanced. However, even in this condition, the inverter can operate. An unbalanced voltage can be corrected in two ways. First, the converter bypassing the same number of power cells in the other phases. The three-phase output voltage becomes balanced again, but this will reduce the total power delivered by the converter. The second method is to change the angle of the control sinewave signal of each phase from the reference. Detailed analysis fault-tolerant operation of CHB converters is in [19].

V. CONDITION OF TEST SYSTEM

A CHB converter with six H-bridges per phase was chosen to powered medium-voltage motor for coolant pump. The converter can generate 13-level 6 kV line-to-line voltage with first harmonic frequency 300 Hz, which is required by the application. The modulation method for switching devices of the converter is based on phase-shifted of carrier signals for each cell. The most optional is variant with a 60° offset ($360^{\circ}/6 = 60^{\circ}$), which achieves the lowest distortion of the output voltage. The frequency of the triangle carrier signal is 1200 Hz.

The converter was simulated for operation under normal and fault conditions with a constant speed of the coolant pump motor, which is replaced by an RL circuit with a resistance of 0.5Ω and an inductance of 1 mH.

VI. SIMULATION UNDER NORMAL CONDITIONS

Fig. 5 shows voltage in one phase and Fig. 6 show all line-to-line voltage in kV. These figures demonstrate all the benefits of using a CHB multilevel converter against common two-level converters. In the Fig. 5, there is the phase voltage of the five-level converter, it is apparent that the output voltage waveform is closed to sinusoidal waveform. It can be observed that there will be less distortion of voltage, an improvement in THD and reduced dv/dt. This reduces the stress on the motor bearings and windings and reduces pulsation on the motor shaft. Total harmonics distortion (THD) reaches a value of 0.9984. In comparison with the typical two-level converters, there is significantly smaller THD. Typical THD value for a classic two-level converter reaches a value of 0.882.

In Fig. 7 a spectrum of all harmonics is shown. The first harmonic is dominant and the other harmonics are very

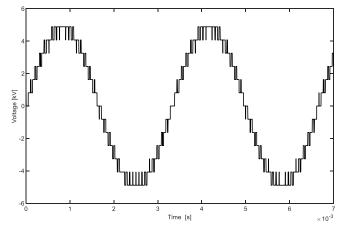


Fig. 5: Phase voltage of six cell (13-level) CHB converter.

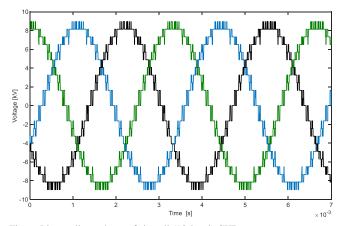


Fig. 6: Line-to-line voltage of six cell (13-level) CHB converter.

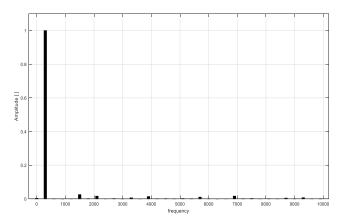


Fig. 7: Harmonic spectrum analysis of output voltage.

small, which is a great advantage of CHB multilevel converters. Other harmonics than the first do not transmit power, they only warm the windings and cause torque ripple.

VII. SIMULATION UNDER FAULT CONDITIONS

To demonstrate the great benefit of a cascaded H-bridge multilevel converter, it is simulated the operation with some faulty power cells. Fig. 8 illustrates the waveform of the inverter voltage wherein phase *A* two power cells are in a fault condition and phase *B* one cell is in a fault condition. Phase *C* is in normal operation without failure of any cells. Cell disorders are resolved by bypassing, producing

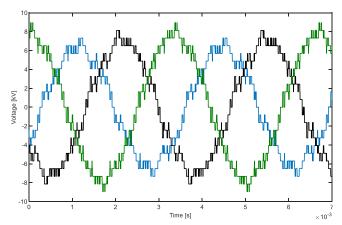


Fig. 9: Line-to-line voltage of six cell (13-level) CHB converter operation under fault condition.

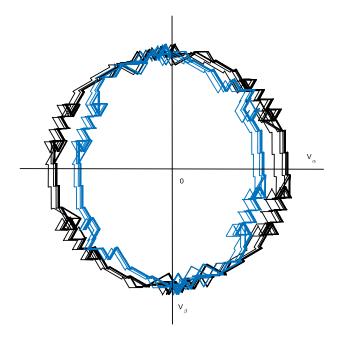


Fig. 8: Components of stator voltage V_s in the stationary $\alpha\beta\theta$ reference frame.

unbalanced line-to-line voltages.

The failure has an interesting impact on the V_{α} and V_{β} components of the motor stator voltage V_s , which is illustrated in Fig. 9. The black spline is the voltage V_s under normal condition and the blue spline is the voltage V_s changes most, where the length is reduced. The resulting spline is no longer a circle but an ellipse with a slight left turn. However, this change in V_{α} voltage has little impact on open-loop motor control.

Fig. 10 compares the moments under normal condition (black line) and fault condition (blue line). It can be seen from the figure that the motor torque drops slightly when the pump is still able to operate.

As mentioned above, even if more than one power cell fails, the converter can still be operated, which is a great advantage of the selected converter topology.

VIII. CONCLUSION

This paper has presented a multi-level converter with cascaded H-bridge to control a power cooling pump.

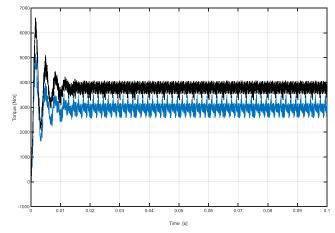


Fig. 10: Torque of motor.

CHB multilevel converters are used for example to control medium-voltage motors energy, conversion, and active power filtering. For the simulation, a 13-level CHB converter was selected in operation under normal conditions and in a fault condition. The motor was controlled in an open-loop. In normal operation, the advantages of using multi-level converters, in general, are evident. In normal operation, the advantages of using multi-level converters, in general, are seen. The output voltage waveform is closed to the sinusoidal waveform resulting in a significant improvement in the motor voltage distortion. A great advantage is also the ability to operate during a fault condition, whereby a faulty power cell can be replaced during a fault which reduces stop times of the machine. A simulation in this state showed that the converter is able to operate even during the failure of several power cells. There is only a slight decrease in line-to-line voltage and machine torque.

ACKNOWLEDGMENT

REFERENCES

- [1] J. Rodriguez et al., "Multilevel Converters: An Enabling Technology for High-Power Applications," in Proceedings of the IEEE, vol. 97, no. 11, pp. 1786-1817, Nov. 2009.
- [2] F. Z. Peng, W. Qian and D. Cao, "Recent advances in multilevel converter/inverter topologies and applications," The 2010 International Power Electronics Conference - ECCE ASIA -, Sapporo, 2010, pp. 492-501.
- [3] S. Kouro et al., "Recent Advances and Industrial Applications of Multilevel Converters," in IEEE Transactions on Industrial Electronics, vol. 57, no. 8, pp. 2553-2580, Aug. 2010.
- [4] S. Bernet, "Recent developments of high power converters for industry and traction applications," in IEEE Transactions on Power Electronics, vol. 15, no. 6, pp. 1102-1117, Nov. 2000.

- [5] H. Stemmler, "Power electronics in electric traction applications," Proceedings of IECON '93 - 19th Annual Conference of IEEE Industrial Electronics, Maui, HI, USA, 1993, pp. 707-713 vol.2.
- [6] Rekha Agrawal, Shailendra Jain, "Multilevel inverter for interfacing renewable energy sources with low/medium- and high-voltage grids", Renewable Power Generation IET, vol. 11, no. 14, pp. 1822-1831, 2017
- [7] Da-liang Yang, Zi-guang Lu, Nai-shan Hang, "Development of ±50kvar DSP-Controlled PWM VSC-Based DSTATCOM with Direct Current Control", Power and Energy Engineering Conference (APPEEC) 2011 Asia-Pacific, pp. 1-5, 2011.
- [8] I. Sanz, M. Moranchel, E. J. Bueno and F. J. Rodriguez, "Analysis of medium voltage modular multilevel converters for FACTS applications," IECON 2016 - 42nd Annual Conference of the IEEE Industrial Electronics Society, Florence, 2016, pp. 6459-6464.
- [9] G. P. Adam, K. H. Ahmed, S. J. Finney and B. W. Williams, "Modular multilevel converter for medium-voltage applications," 2011 IEEE International Electric Machines & Drives Conference (IEMDC), Niagara Falls, ON, 2011, pp. 1013-1018.
- [10] R. C. Portillo et al., "Modeling Strategy for Back-to-Back Three-Level Converters Applied to High-Power Wind Turbines," in IEEE Transactions on Industrial Electronics, vol. 53, no. 5, pp. 1483-1491, Oct 2006
- [11] Jin Wang and F. Z. Peng, "Unified power flow controller using the cascade multilevel inverter," in IEEE Transactions on Power Electronics, vol. 19, no. 4, pp. 1077-1084, July 2004.
- [12] H. Akagi, "Large static converters for industry and utility applications," in Proceedings of the IEEE, vol. 89, no. 6, pp. 976-983, June 2001.
- [13] T. A. Meynard, H. Foch, P. Thomas, J. Courault, R. Jakob and M. Nahrstaedt, "Multicell converters: basic concepts and industry applications," in IEEE Transactions on Industrial Electronics, vol. 49, no. 5, pp. 955-964, Oct. 2002.
- [14] Krishna Kumar Gupta, Shailendra Jain, "Multilevel inverter topology based on series connected switched sources", Power Electronics IET, vol. 6, no. 1, pp. 164-174, 2013.
- [15] Krishna Kumar Gupta, Shailendra Jain, "Comprehensive review of a recently proposed multilevel inverter", Power Electronics IET, vol. 7, no. 3, pp. 467-479, 2014.
- [16] Suman Debnath, Jiangchao Qin, Behrooz Bahrani, Maryam Saeedifard, Peter Barbosa, "Operation Control and Applications of the Modular Multilevel Converter: A Review", Power Electronics IEEE Transactions on, vol. 30, pp. 37-53, 2015.
- [17] G. Carrara, S. Gardella, M. Marchesoni, R. Salutari, G. Sciutto, "A new multilevel PWM method: A theoretical analysis", IEEE Trans. Power Electron., vol. 7, pp. 497-505, Jul. 1992.
- [18] H. Son, T. Kim, D. Kang, D. Hyun, "Fault diagnosis and neutral point voltage control when the 3-level inverter faults occur", Rec. IEEE Power Electron. Spec. Conf. (PESC'04), vol. 6, pp. 4558-4563, 2004-Jun.-2025.
- [19] J. Rodriguez, P. Hammond, J. Pontt, R. Musalem, P. Lezana, M. Escobar, "Operation of a medium-voltage drive under faulty conditions", IEEE Trans. Ind. Electron., vol. 52, pp. 1080-1085, Aug. 2005