# Abcd Efgh

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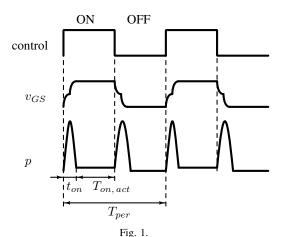
Abstract—Aaa. Bbb. Ccc.

Index Terms-

I. INTRODUCTION

II. METHODOLOGY

III. MEASUREMENT AND INTERPRETATION OF TEST RESULTS



### IV. CONCLUSION

#### ACKNOWLEDGMENT

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#### REFERENCES

- [1] C. Hu and M. J. Model, "A model of power transistor turn-off dynamics," 1980 IEEE Power Electronics Specialists Conference, Atlanta, Georgia, USA, 1980, pp. 91-96.
- [2] B. J. Baliga, Fundamentals of Power Semiconductor Devices, New York: Springer. 2008.

- [3] R. F. Pierret, Semiconductor Device Fundamentals, Addison-Wesley Publishing Comany, 1996, ISBN 0-201-54393-1
- [4] H. K. Gummel, H. K., "A Charge Control Relation for Bipolar Transistors", Bell Syst. Tech. J., vol. 49, 1970
- [5] S. M. Sze and M. K. Lee, Semiconductor Devices: Physics and Technology, 3rd ed., New York: John Wiley & Sons, Inc. 2012.
- [6] J. Miklas, Power Switching Transistors, Brno: Brno University of Technology, Faculty of Electrical Engineering and Communication. 2016. Head of Diploma Thesis doc. Dr. Ing. Miroslav Patocka
- [7] P. Prochazka, J. Miklas, I. Pazdera, M. Patocka, J. Knobloch, R. Cipin, "Measurement of Power Transistors Dynamic Parameters", Mechatronics 2017, pp.571-577, ISBN 978-3-319-65959-6