



Poisson & Drift-Diffusion Equations Solver for Semiconductor Device Modelling

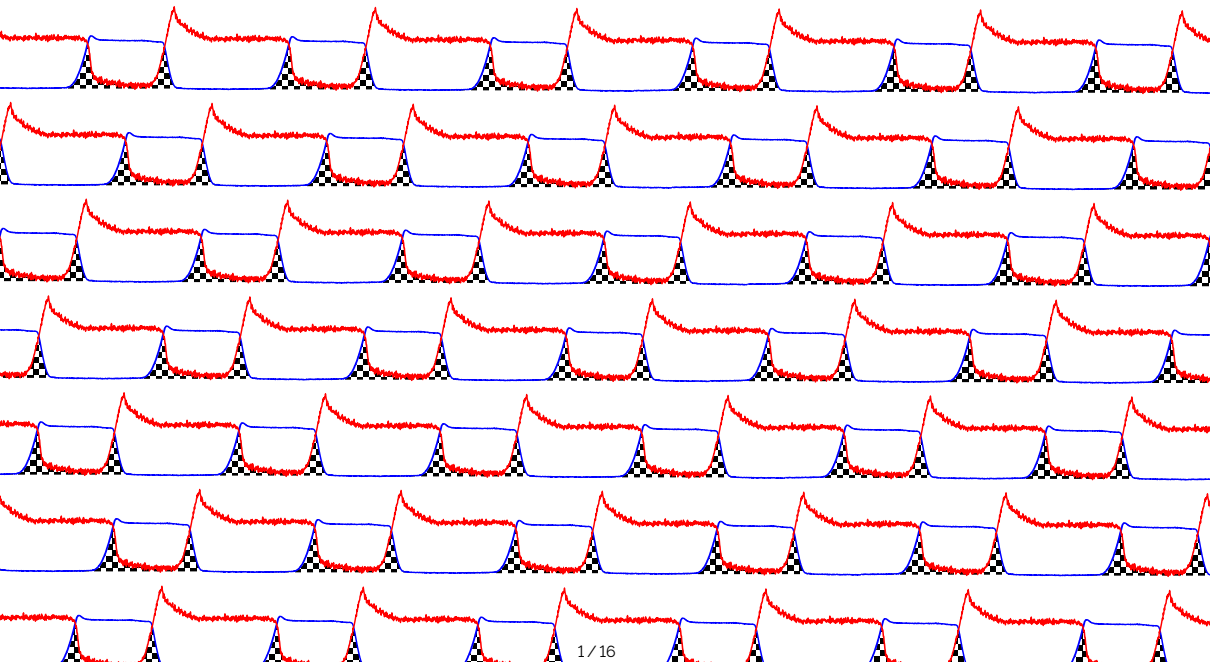
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Brno University of Technology , Czech Republic

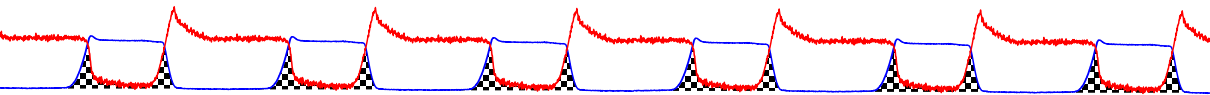
FEniCS 2023, June 14-16



Motivation

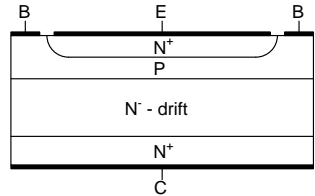
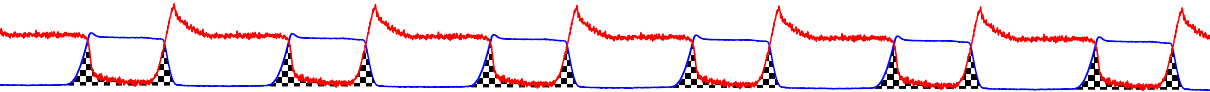


Motivation



Switching loss analysis of power devices

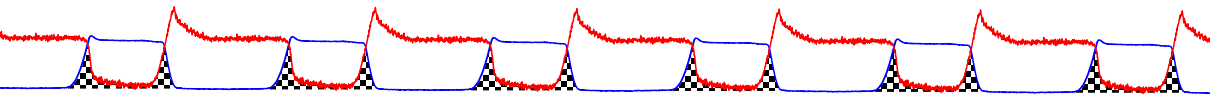
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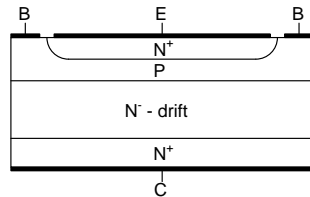
- High voltage, high current

Motivation

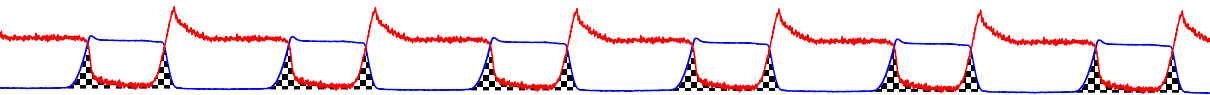


Switching loss analysis of power devices

- High voltage, high current
- Excess charge storage in bipolar transistors (BJT, IGBT) during on-state

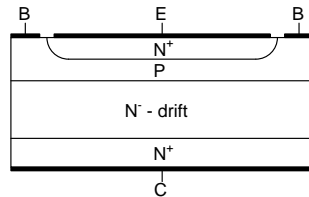


Motivation



Switching loss analysis of power devices

- High voltage, high current
- Excess charge storage in bipolar transistors (BJT, IGBT) during on-state
 - 1-D lumped charge models (possibly transient?)



-emiconductor Equations - Full Drift-Diffusion Model

Electrostatics (*Gauss's Law*): Poisson's Equation:

$$\nabla \cdot (\varepsilon \nabla \psi) = -q(p - n + N_D - N_A) \quad (1)$$

Carrier transport: drift-diffusion equations:

$$\mathbf{J}_p = \overbrace{qp\mu_p\mathbf{E}}^{\text{drift}} - \overbrace{qD_p\nabla p}^{\text{diffusion}} \quad (2)$$

$$\mathbf{J}_n = qn\mu_n\mathbf{E} + qD_n\nabla n \quad (3)$$

Continuity equations

$$\frac{\partial p}{\partial t} = -\frac{1}{q}\nabla \cdot \mathbf{J}_p - R \quad (4)$$

$$\frac{\partial n}{\partial t} = \frac{1}{q}\nabla \cdot \mathbf{J}_n - R \quad (5)$$

Shockley-Read-Hall (SRH) recombination

$$R = \frac{n \cdot p - n_i^2}{\tau_p(n + n_0) + \tau_n(p + p_0)} \quad (6)$$

Unknown variables ($\psi, p, n, \mathbf{J}_p, \mathbf{J}_n$)

Notation:

ε	Permittivity of material (silicon)
ψ	Electric potential
\mathbf{E}	Electric field intensity $\mathbf{E} = -\nabla\psi$
q	Elementary charge
p, n	Holes and electrons concentration
$\mathbf{J}_p, \mathbf{J}_n$	Hole and electron current density
N_A, N_D	Acceptors and donors concentration
μ_p, μ_n	hole and electron mobility
D_p, D_n	Diffusion constants (Fick's Law)
R	Recombination-generation rate
n_i	Intrinsic carrier concentration
p_0, n_0	thermal equilibrium concentrations
τ_p, τ_n	hole and electron recombination life-time

Semiconductor Equations - 3 equations system

- 3 independent variables (ψ, p, n)

$$\lambda_0 \nabla \cdot (\nabla \psi) = -(p - n + N_D - N_A) \quad (7)$$

$$\frac{1}{\lambda_1} \frac{\partial p}{\partial t} = 0 = \nabla \cdot (\mu_p p \nabla \psi + D_p \nabla p) - R(p, n) \quad (8)$$

$$\frac{1}{\lambda_1} \frac{\partial n}{\partial t} = 0 = \nabla \cdot \underbrace{(-\mu_n n \nabla \psi)}_{\text{Drift}} + \underbrace{D_n \nabla n}_{\text{Diffusion}} - \underbrace{R(p, n)}_{\text{Recombination}} \quad (9)$$

Semiconductor Equations - 3 equations system

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- Scaled
- Coupled
- Nonlinear

Steady State Problem Statement

$$\lambda_0 \nabla \cdot (\nabla \psi) = -(p - n + N_D - N_A) \quad \text{in } \Omega \quad (10)$$

$$0 = \nabla \cdot (p \mu_p \nabla \psi + D_p \nabla p) - R \quad \text{in } \Omega \quad (11)$$

$$0 = \nabla \cdot (-n \mu_n \nabla \psi + D_n \nabla n) - R \quad \text{in } \Omega \quad (12)$$

with "ohmic" boundary conditions

$$\psi = \psi_{\text{BC}} \quad \text{on } \Gamma_{\text{D0}} \quad (13)$$

$$p = p_{\text{BC}} \quad \text{on } \Gamma_{\text{D1}} \quad (14)$$

$$n = n_{\text{BC}} \quad \text{on } \Gamma_{\text{D2}} \quad (15)$$

$$\mathbf{n} \cdot \nabla \psi = g \quad \text{on } \Gamma_{\text{N0}} \quad (16)$$

Variational Formulation

$$\lambda_0 \nabla \cdot (\nabla \psi) = -(p - n + N_D - N_A) \quad \text{in } \Omega \quad (10)$$

$$0 = \nabla \cdot (p \mu_p \nabla \psi + D_p \nabla p) - R \quad \text{in } \Omega \quad (11)$$

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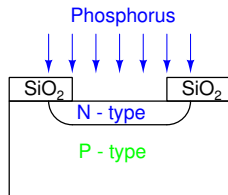
Find $(\psi, p, n) \in V_0 \times V_1 \times V_2$ such that

$$F((\psi, p, n); (v_0, v_1, v_2)) = 0 \quad (17)$$

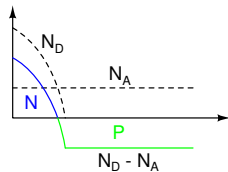
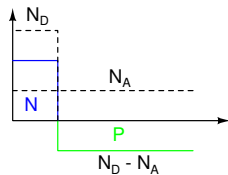
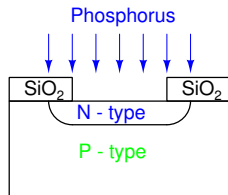
for all $(v_0, v_1, v_2) \in \hat{V}_0 \times \hat{V}_1 \times \hat{V}_2$; with F given by:

$$\begin{aligned} F = & - \int_{\Omega} \lambda_0 \nabla \psi \cdot \nabla v_0 \, dx + \int_{\Omega} (p - n + N_D - N_A) v_0 \, dx + \int_{\Gamma_{NO}} g v_0 \, ds \\ & - \int_{\Omega} D_p \nabla p \cdot \nabla v_1 \, dx - \int_{\Omega} \mu_p p \nabla \psi \cdot \nabla v_1 \, dx - R v_1 \, dx \\ & - \int_{\Omega} D_n \nabla n \cdot \nabla v_2 \, dx + \int_{\Omega} \mu_n n \nabla \psi \cdot \nabla v_2 \, dx - R v_2 \, dx \end{aligned} \quad (18)$$

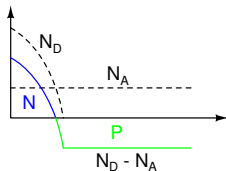
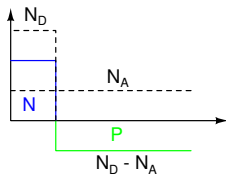
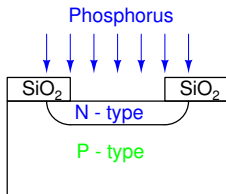
Domain, Doping profiles



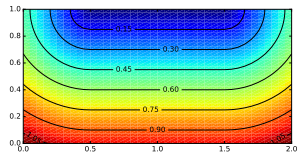
Domain, Doping profiles



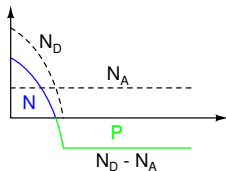
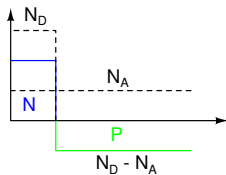
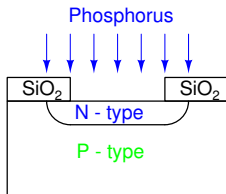
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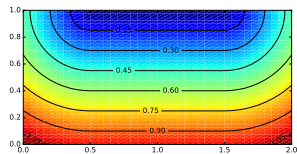
```
def distance(x): ....
```



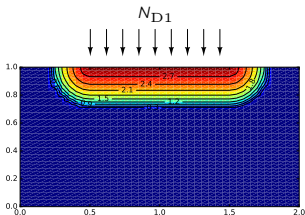
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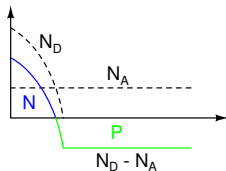
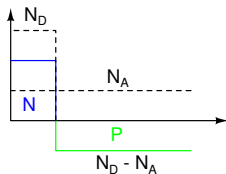
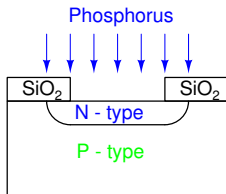
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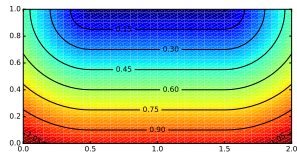
```
Nd1 = Function(Q)  
Nd1.interpolate(doping_expr(distance(x)))
```



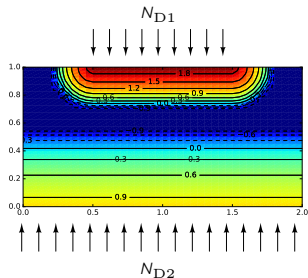
Domain, Doping profiles



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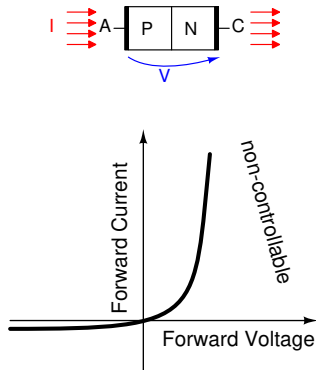


```
N = Nd1 + Nd2 - Na
```


Demonstration of Results

PN Junction Diode

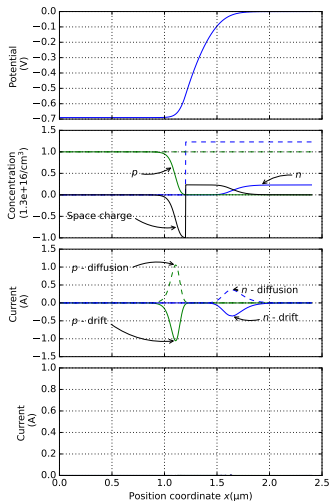
Typical Electrical Output Characteristics



PN Junction Diode

Simulation Results - Step junction

Equilibrium State



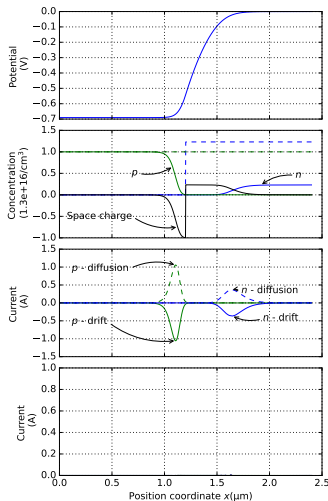
Reverse / Zero Bias:

- Depletion region formed
- Zero total current
- Drift compensated by diffusion

PN Junction Diode

Simulation Results - Step junction

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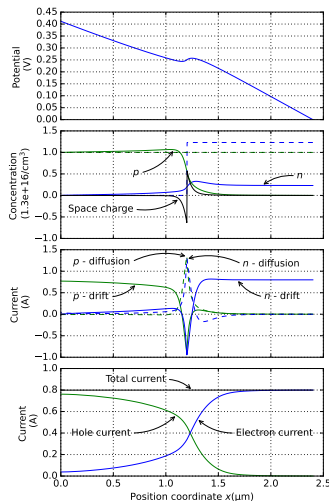
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Forward Bias:

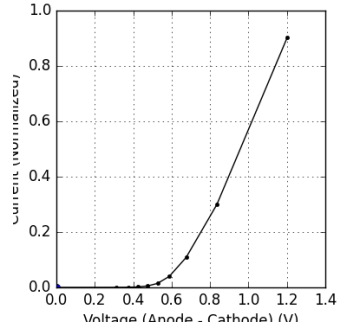
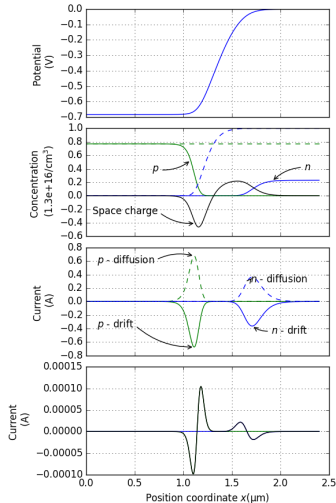
- Depletion region reduced
- Constant current across device
- minority carriers injected
- majority carriers exceeding doping concentration

Forward Bias



PN Junction Diode

Simulation Results - Gaussian Doping Profile



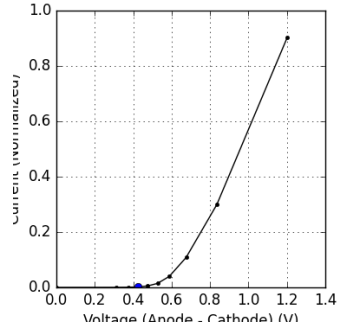
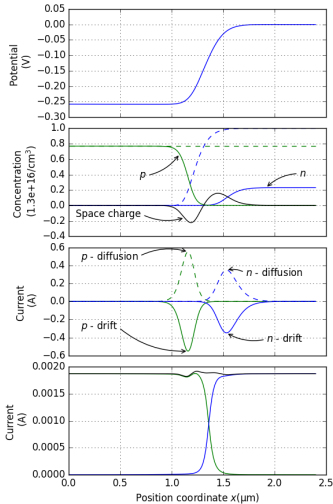
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$$0 = \nabla \cdot (p \mu_p \nabla \psi + D_p \nabla p) - R$$

$$0 = \nabla \cdot (-n \mu_n \nabla \psi + D_n \nabla n) - R$$

PN Junction Diode

Simulation Results - Gaussian Doping Profile



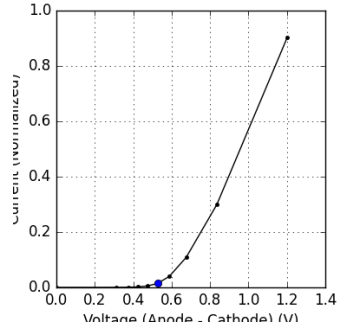
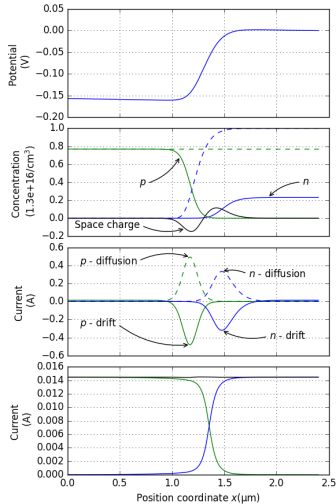
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PN Junction Diode

Simulation Results - Gaussian Doping Profile



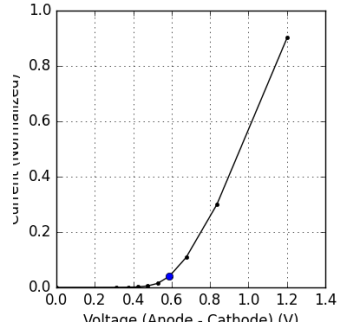
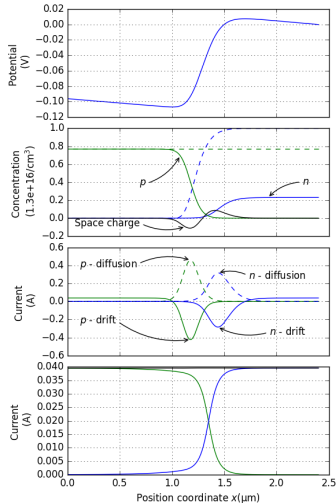
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PN Junction Diode

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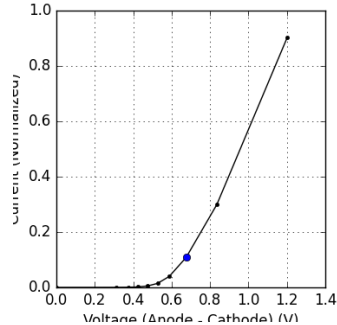
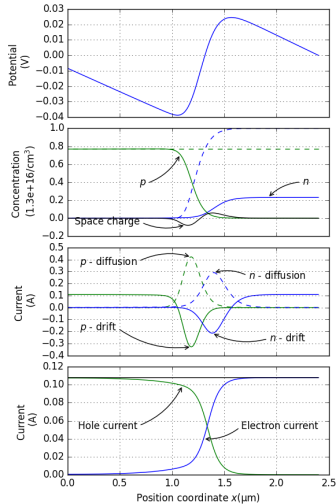
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PN Junction Diode

Simulation Results - Gaussian Doping Profile



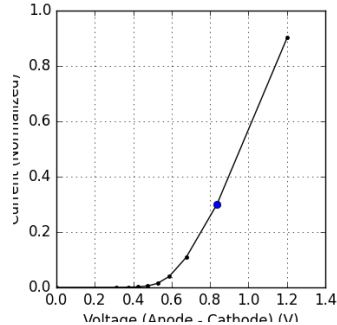
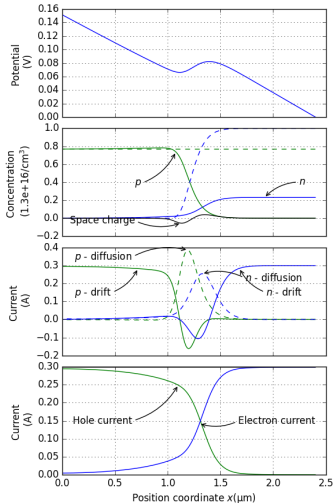
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PN Junction Diode

Simulation Results - Gaussian Doping Profile



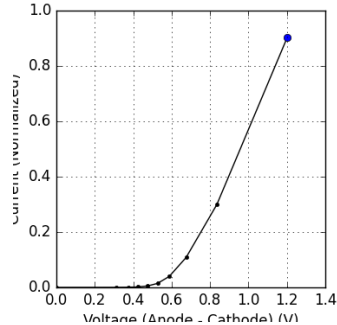
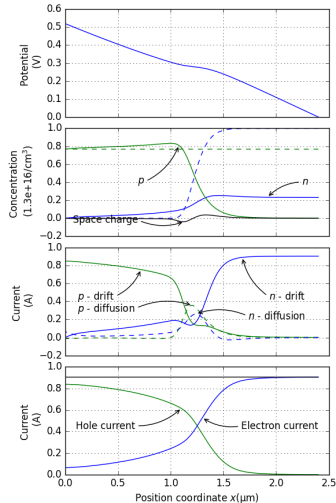
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PN Junction Diode

Simulation Results - Gaussian Doping Profile



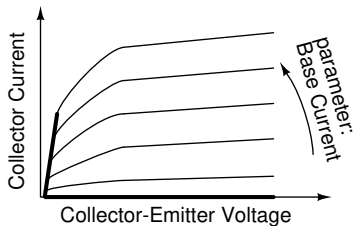
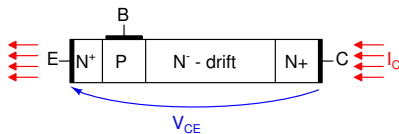
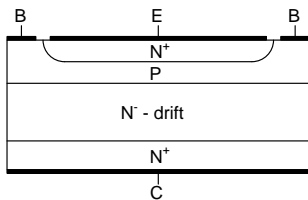
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NP_vN Power Transistor

Typical Electrical Output Characteristics



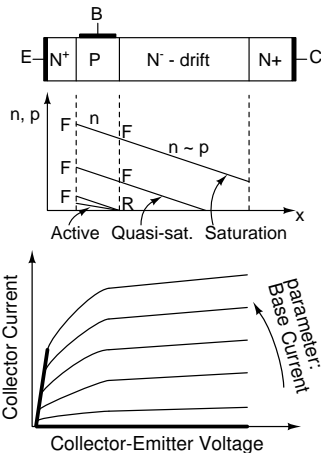
NP_vN Power Transistor

1-D Lumped Charge Model

- Charge control + conductivity modulation
- "Rules":
 - **Zero mobile carriers** at reverse biased junction
 - **Some mobile charge** at forward biased junction
 - ▶ defined by applied voltage and doping concentrations
 - Diffusion current: defined by **concentration gradient**
 - Conductivity: defined by amount of mobile carriers

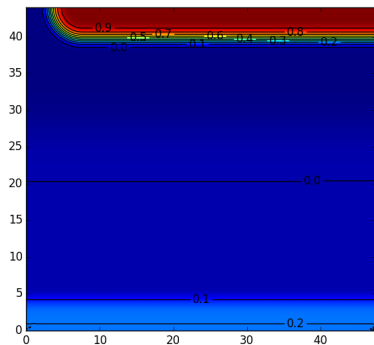
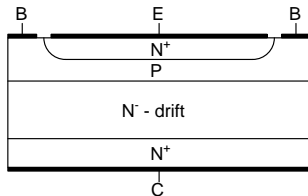
Equations (2),(3)

$$J_p = \overbrace{qp\mu_p \mathbf{E}}^{\text{drift}} - \overbrace{qD_p \nabla p}^{\text{diffusion}}$$
$$J_n = qn\mu_n \mathbf{E} + qD_n \nabla n$$



NP_vN Power Transistor

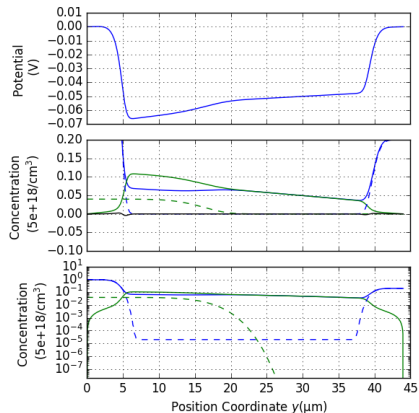
Simulation Domain, BC, Doping



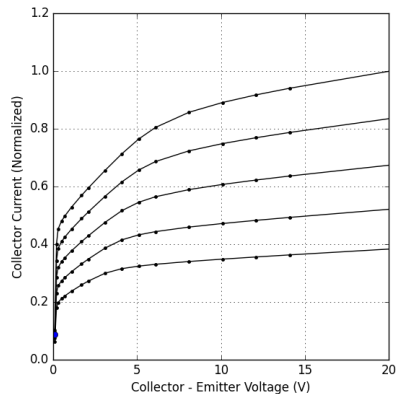
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Saturation



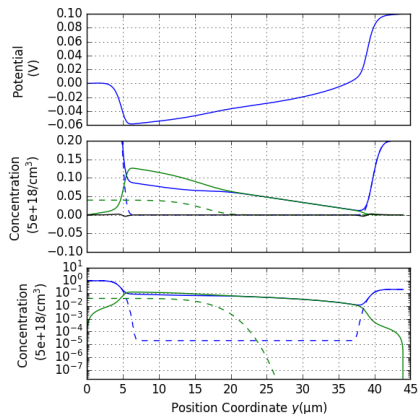
Saturation



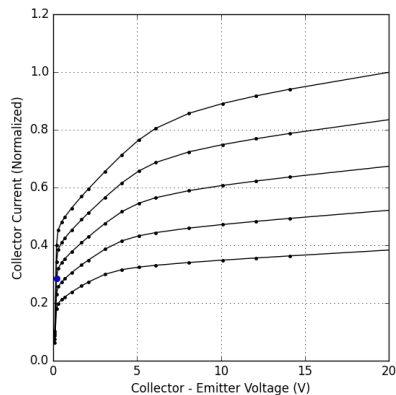
NP ν N Power Transistor

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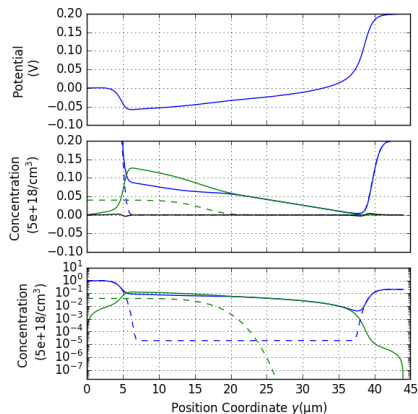
Saturation



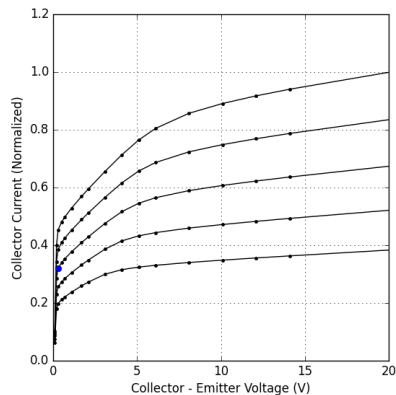
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
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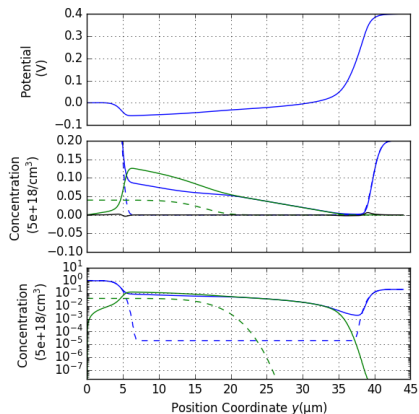
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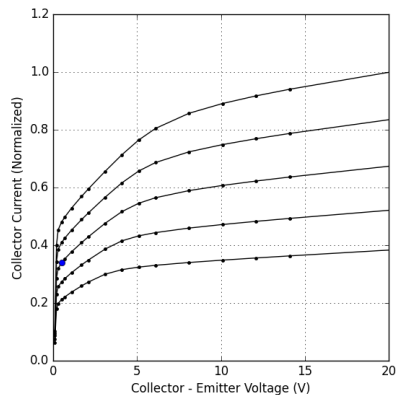
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Quasi-Saturation



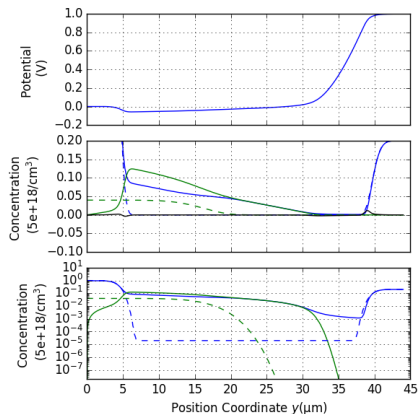
Quasi-Saturation



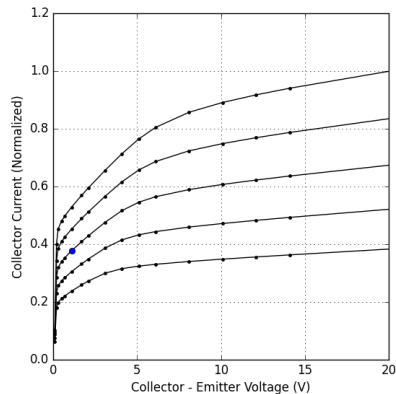
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Quasi-Saturation



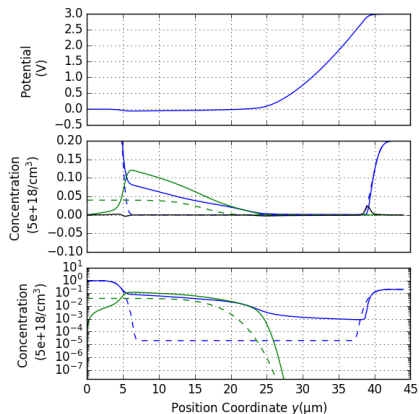
Quasi-Saturation



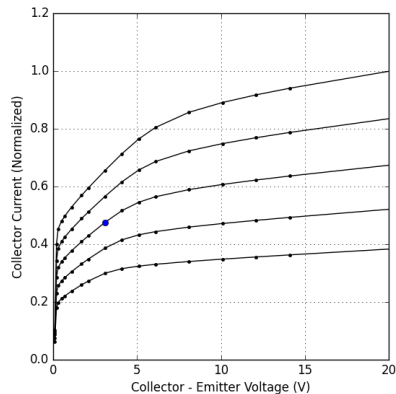
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Quasi-Saturation



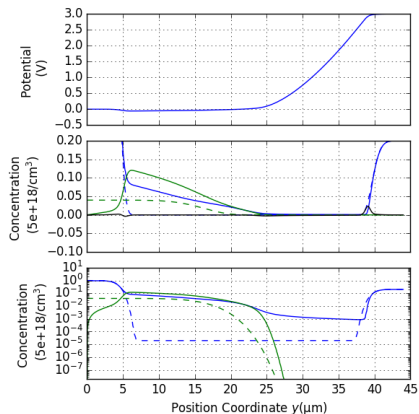
Quasi-Saturation



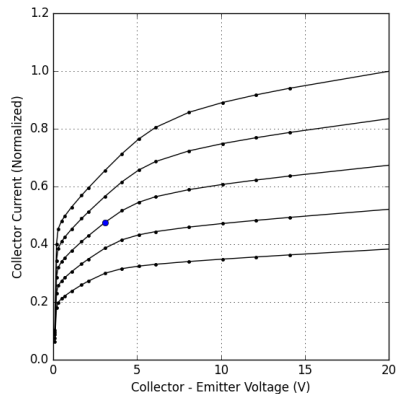
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Quasi-Saturation



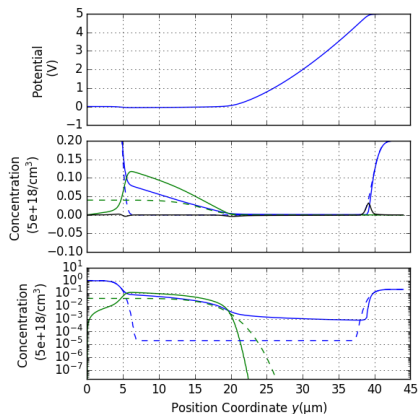
Quasi-Saturation



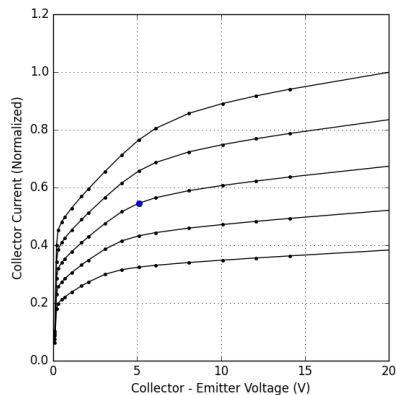
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Active Mode



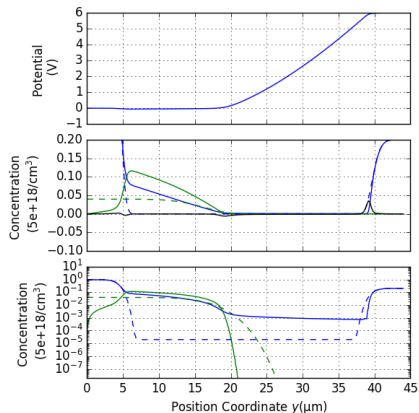
Active Mode



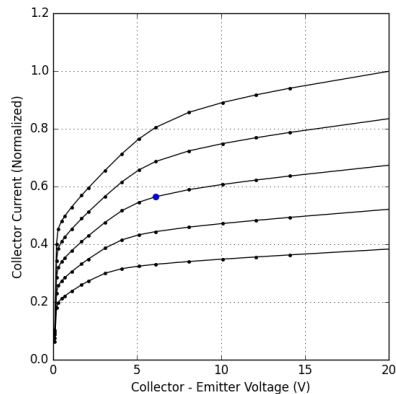
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Active Mode



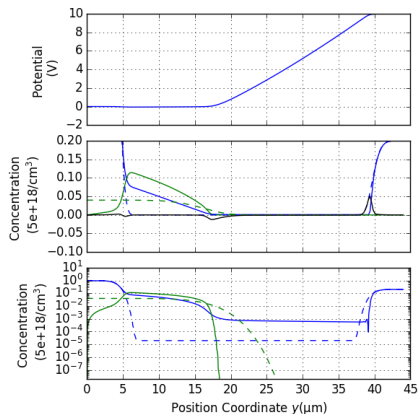
Active Mode



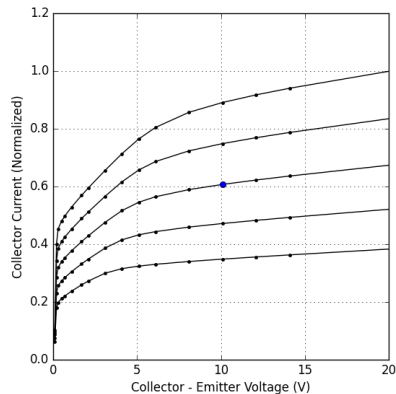
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Active Mode



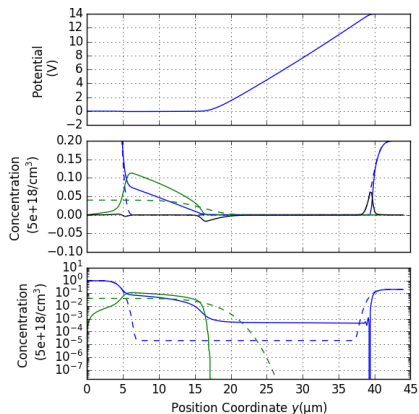
Active Mode



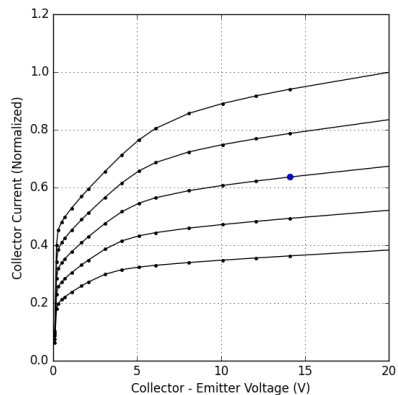
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Active Mode



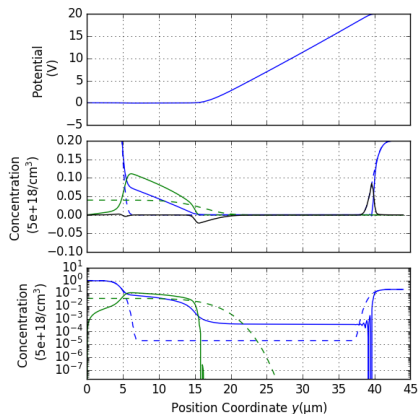
Active Mode



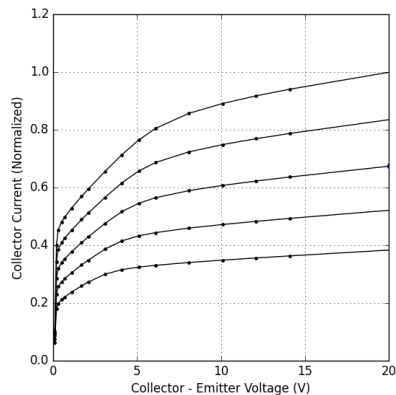
NP ν N Power Transistor

Simulation Results - Operational Modes

Collector voltage sweep
Active Mode



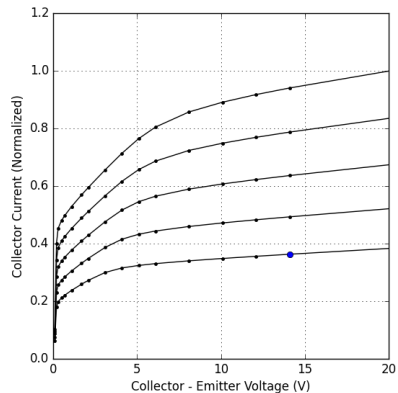
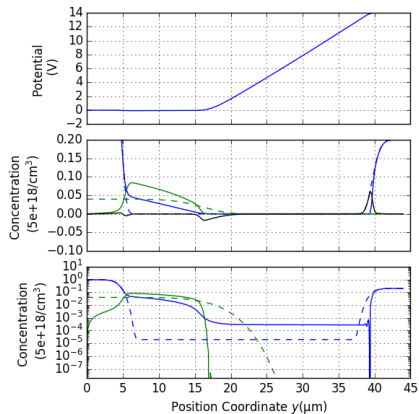
Active Mode



NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)

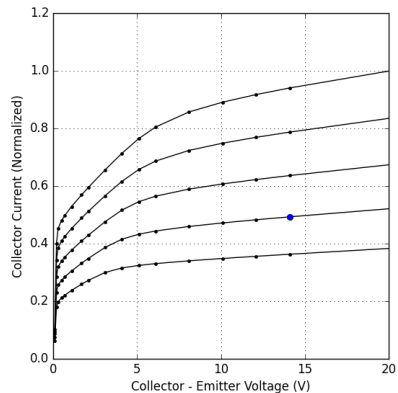
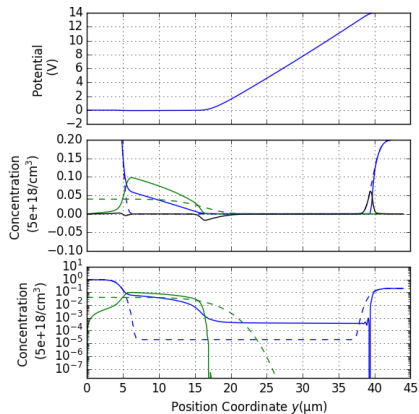
Base drive sweep



NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)

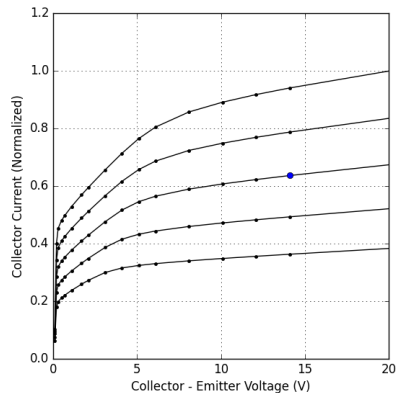
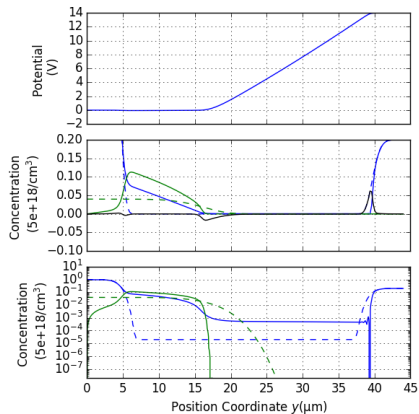
Base drive sweep



NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)

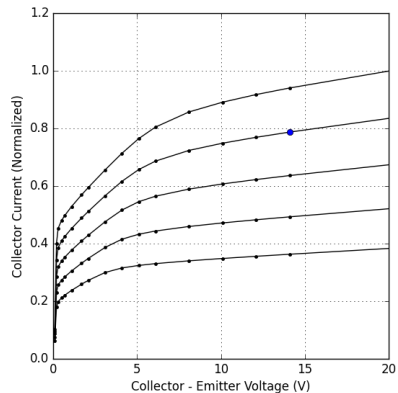
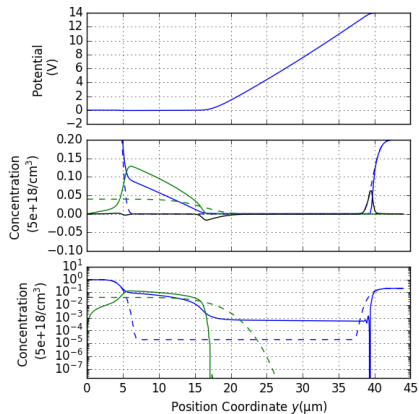
Base drive sweep



NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)

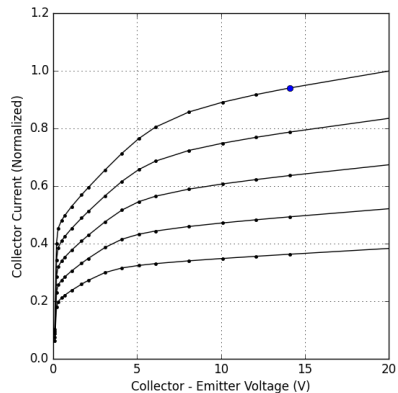
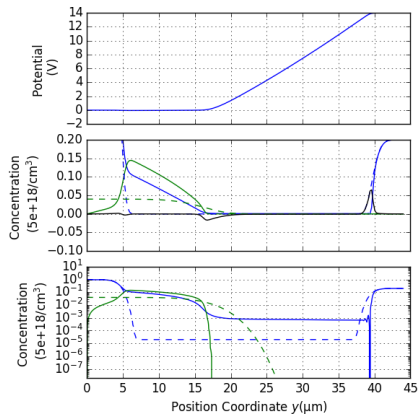
Base drive sweep



NP ν N Power Transistor

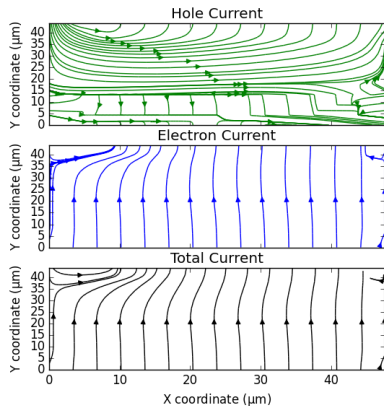
Simulation Results - Transistor Effect (Current Gain)

Base drive sweep



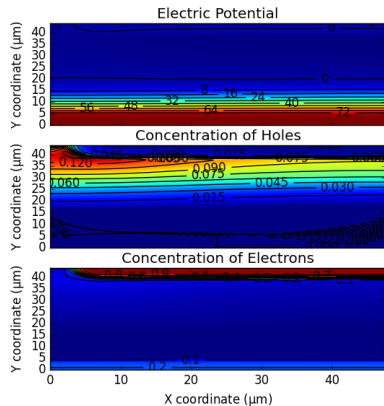
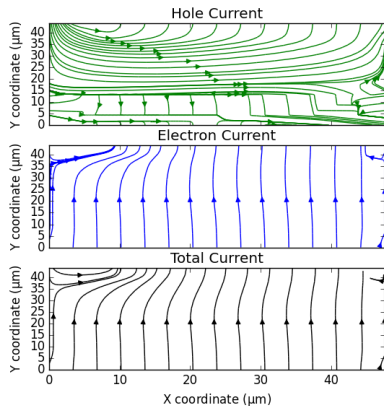
NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)



NP ν N Power Transistor

Simulation Results - Transistor Effect (Current Gain)



Summary

- Numerical approach **validated and demonstrated** on basic structures
 - The equations and FEniCS seem to love each other
- Currently in “demo version” - qualitative explanation of device physics
- Good **validation tool** for analytical assumptions in simplified power BJT model

Outlook

Space for improvements:

- Time dependent problem
- Initial guess accuracy, BC for any bias
- **Nonlinear Solver**, Convergence, Stability

<https://github.com/janmiklas/fenicsx-semiconductor-eq>

Thank you for your kind attention!