Embedded Systems I
Lab 2: VHDL

Description:

In this lab you will learn how to simulate real hardware circuits with help of GHDL and GtkWave.

Task:

1. Install the required Software (see slides) and download the .vhdl files and the make script.

2. Compile and simulate the respective VHDL testbench with the make file.
   ```
   make I=and_or/andor.vhdl T=and_or/andor_tb.vhdl E=andor_tb S_TIME=50ns gtkwave andor_tb.vcd
   ```

3. Use GtkWave to show the results of the simulation.