

JanskyProcessor Architecture

Ian Duncan

March 20, 2015

Contents

1	Data Storage	2
1.1	RAM	2

Introduction

This document aims to (eventually) be a complete reference to the JanskyProcessor architecture, instruction set, and computer. I know the name is horrible, but otherwise the project would be nameless. Also, please pronounce the name "yahn-skee" (janski in IPA).

1 Data Storage

The JanskyProcessor computer has a Von Neumann architecture. Program and data memory are both stored in RAM. There also exists thirteen registers, some of which are used for special purposes such as the stack and instruction pointers.

1.1 RAM