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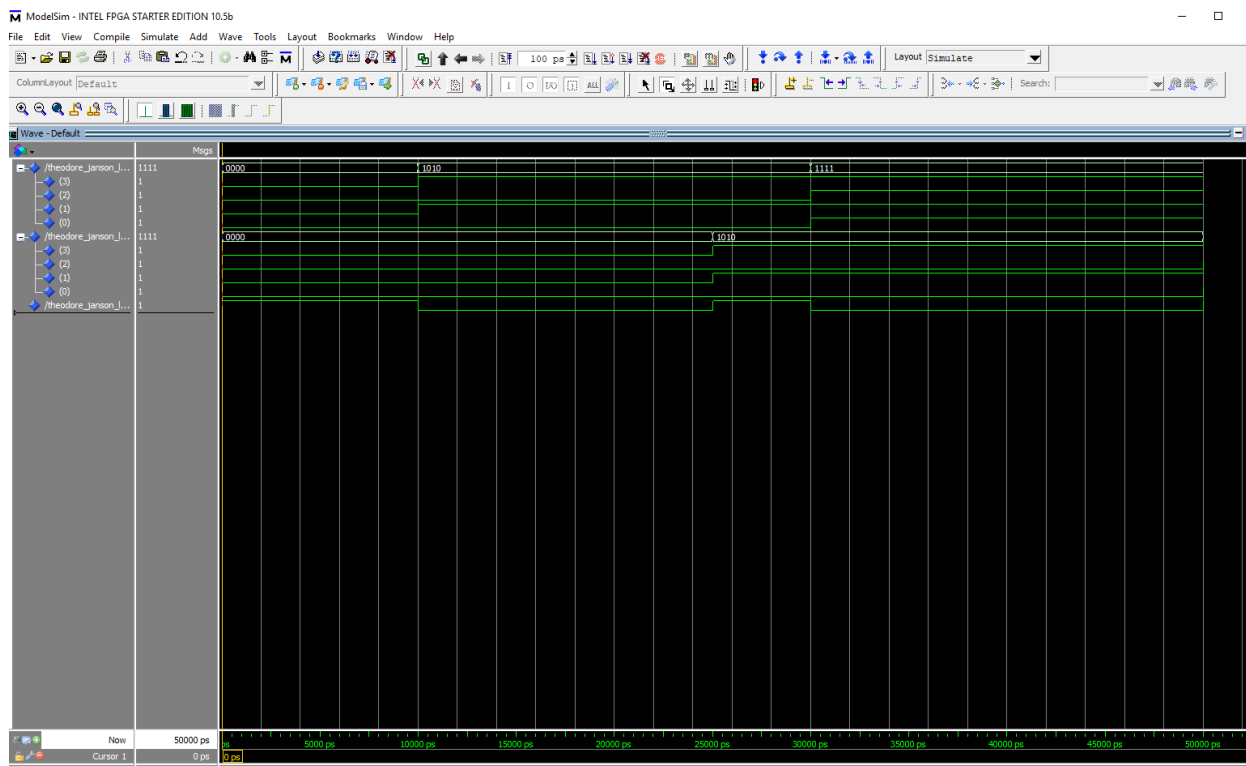
Question 1 (1 mark)

Report the number of pins and logic modules used to fit your design on the FPGA board? (1 mark)

- Logic Utilization (in ALMs): 2
- Total pins: 9

Question 2 (1 mark)

Show a representative simulation plot for the introductory testing example. You can simply include below a snapshot from the waveform that you obtained from ModelSim. In order to fully capture all the signals from the waveform, you can adjust the display range using the magnifier icons.



Question 3 (1 mark)

Show representative simulation plots for the exhaustive test. Note that you can simply include below snapshots from the waveform that you obtained from ModelSim. In order to fully capture all the signals from the waveform, you can adjust the display range using the magnifier icons.

