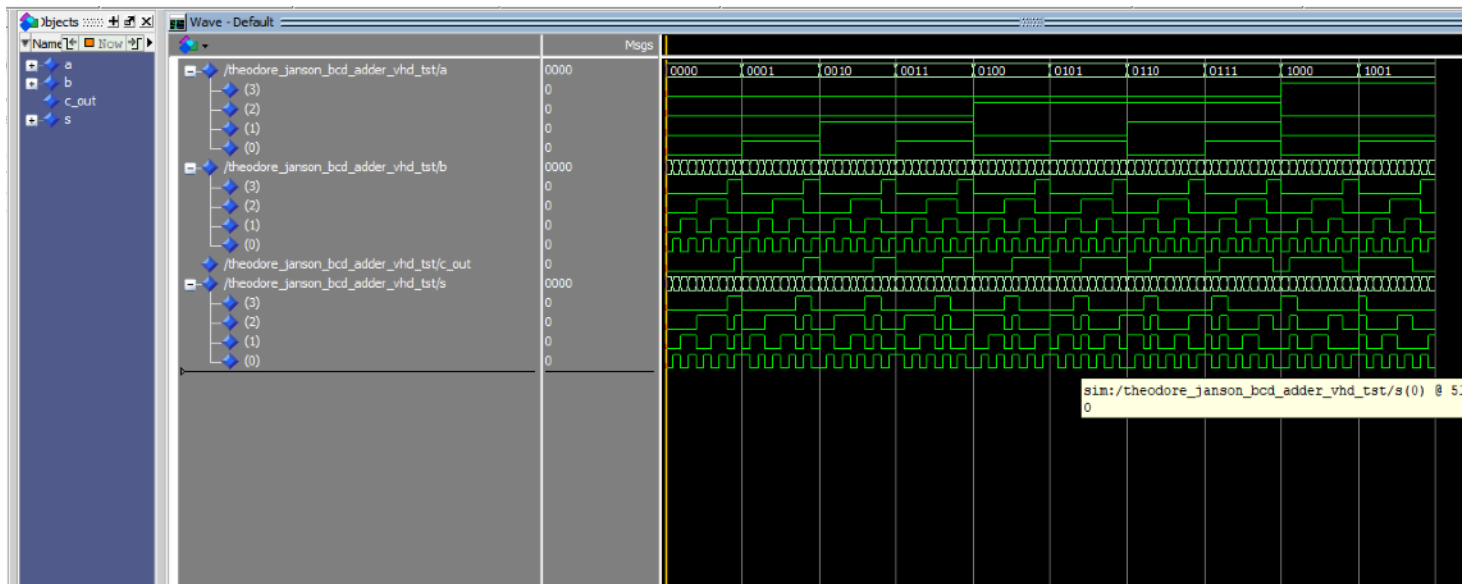


NAME	Theodore Janson
ID	260868223

*Question 1 (2 marks)*

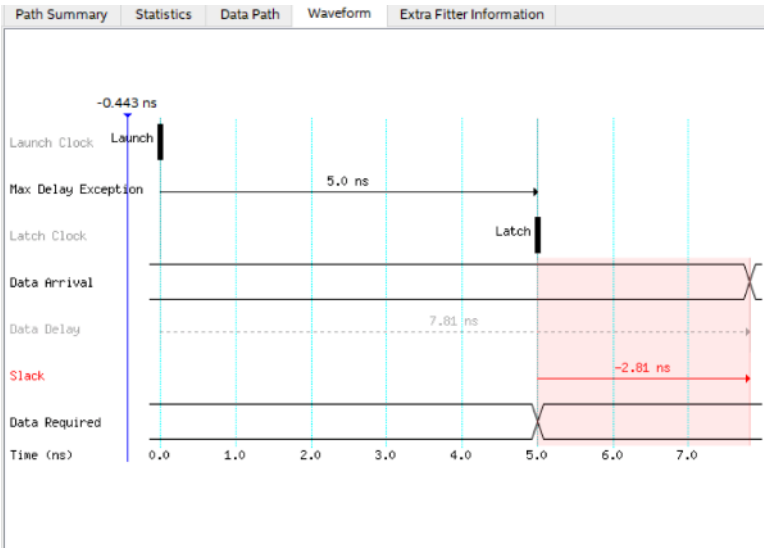
Show representative simulation plots of the one-digit BCD adder circuit for all the possible input values.

*Question 2 (2 marks)*

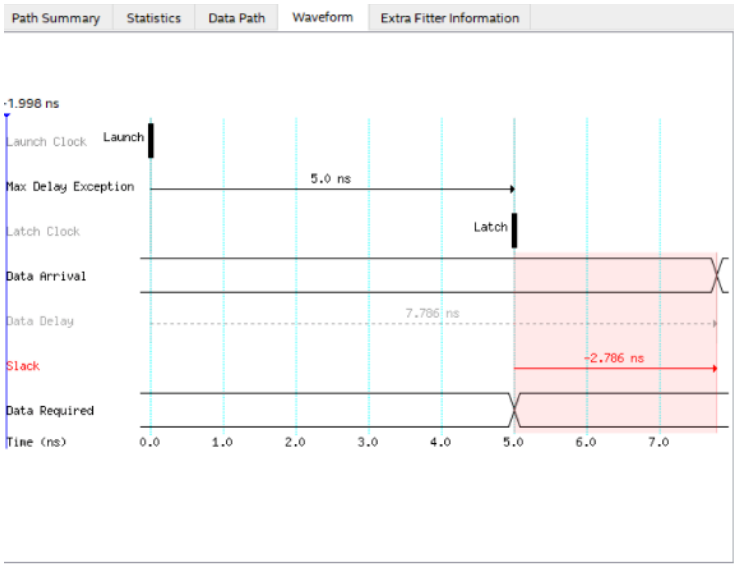
Perform timing analysis and find the critical path(s) of the one-digit BCD adder circuit for the Fast 1100mV 85C Model. Show the obtained timing waveform(s) of the critical path(s) that you found.

Top Failing Paths <a href="#">[hide details]</a>				
Slack	From	To	Recommendations	
1 -2.810	A[0]	S[3]	<a href="#">Report recommendations for this path</a>	
2 -2.786	A[2]	S[3]	<a href="#">Report recommendations for this path</a>	
3 -2.781	A[2]	S[2]	<a href="#">Report recommendations for this path</a>	
4 -2.773	A[0]	S[2]	<a href="#">Report recommendations for this path</a>	
5 -2.760	A[0]	S[1]	<a href="#">Report recommendations for this path</a>	

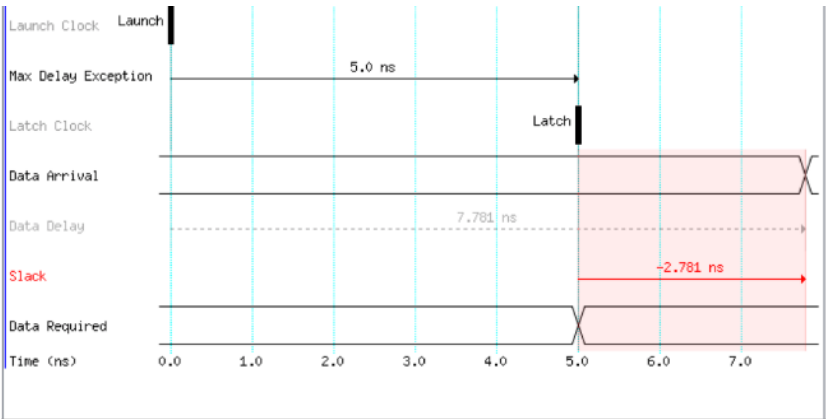
A[0] to S[3]



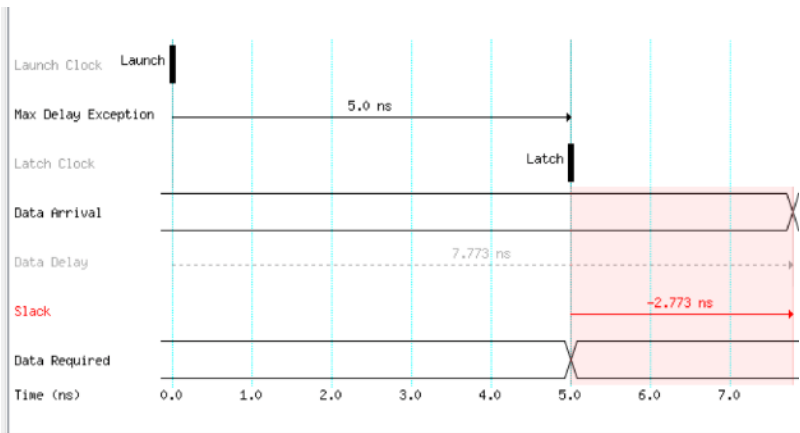
A[2] to S[3]



A[2] to S[2]



A[0] to S[2]



S[0] to S[1]

