

Table 1: ECE 230L Laboratory 7 Grading Rubric

Criteria	Points Possible
Diode OR Gate	16
Circuit Diagram	3
Truth Table Verified	3
Variable diode drop values given for Si Diode	3
Variable diode drop values given for Ge Diode	3
Justification of whether circuit is “better”	4
Diode AND Gate	9
Circuit Diagram	3
Truth Table Verified	3
Diode Drop Values Noted	3
Discrete MOS Inverter Circuit	44
Circuit Diagram	3
Truth Table Verified	3
Voltage Lost Across Circuit	3
Image of Pixelpulse 2 when triangle wave is applied	5
V_{OL} from Pixelpulse 2 graph	3
V_{OH} from Pixelpulse 2 graph	3
Image of Pixelpulse 2 when square wave is applied	5
High-to-Low Transition Time (t_{p-HL})	5
Low-to-high transition time (t_{p-LH})	5
Degraded image of Pixelpulse 2 when square wave is applied	5
Degradation frequency with explanation	4
Exploration: Discrete NAND Gate w/ Applications	6
Discrete NAND gate circuit diagram	3
NAND Gate Truth Table Verified	3
Questions	25
Question 1	5
Question 2	5
Question 3	5
Question 4	5
Question 5	5
Total	100