# Janus Bo Andersen

LTVP-Plus holder eligible to work on LOC. Singaporean spouse. Software and embedded developer with strong interest in computer vision, digital signal processing and SoCs. Solid quantitative finance background.

### **DETAILS**

Residence

Living in Sg with Singaporean spouse.

Holding LTVP-Plus valid through 2025 and can work on LOC.

Pursuing PR.

Danish/EU citizen.

## CONTACT

Mobile

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LinkedIn

<u>linkedin.com/in/janusboandersen</u>

Github

github.com/janusboandersen

#### LANGUAGES

Danish native
English fluent
German advanced (B2/C1)
Mandarin basic

## **PROFILE**

Aiming to transition into a developer or engineer role in the tech startup sector in Singapore and looking for internship role in an exciting company working on hard problems. I have a proven track record in finance, and I decided to pursue my passion for tech and for building things by now completing a degree in software and embedded systems. I particularly enjoy systems engineering, computer vision systems, implementing and optimising algorithms, making them run fast on hardware, working with large data sets, statistics and machine learning.

## **EDUCATION**

## **B.Eng. Electronic Engineering: Software and Embedded**

2018 – 2021 | Aarhus University, Denmark Top of the year with current GPA 11.9 (12-point scale). Selected recent projects:

- Computer vision, 3D reconstruction and image depth-segmentation using multiple views.
- Hardware accelerated machine learning on Xilinx SoC for fast, lowpower edge-processing.
- Quadrotor control system development.
- Lab equipment-as-a-service over MQTT with Dockerized microservices backend stack Django/Postgres/Nginx, responsive frontend.
- Smart energy metering with WM-Bus and Cloud.
- Many other projects within Cloud, embedded, DSP, etc.

## M.Sc. Finance

2009 | Aarhus University, Denmark Top of the year with GPA 11.5 (12-point scale).

- Master's thesis: Commercialization of Microfinance, portfolio view.
- Focus on corporate finance, financial engineering, quantitative methods, statistics, econometrics.
- Selected for Doing Business in China case competition with BCG, Novozymes.

## **B.Sc. Economics and Business**

2007 | Aarhus University, Denmark Highest GPA of the year of 10.6 (old scale).

- Bachelor's thesis: Interest-rate term structure models.
- McKinsey Award and Scholarship 2007: First prize for best result of the B.Sc. education.
- Exchange at Singapore Management University.

#### SFI FCTFD WORK EXPERIENCE

## **Independent developer and consultant**

2016 - 2017 | Remote, international

- Developed a communications and productivity tool for an industry-leading pharma company. Still in use today.
- Visual identity design, user-tested UI/UX, system integration to backend infrastructure for fast data retrieval.

## Novo Nordisk Philippines | Head of Finance, Legal, Logistics & IT

2014 – 2016 | Manila, Philippines

- Drove turn-around in affiliate with new management team.
- Planned and ensured significant investments for three-year period, enabling launch of new pharmaceutical products, headcount increases and investments in the market.
- Responsible for finances and budgets, ensuring that ambitious growth targets for sales and profits were reached two years in a row, and maintaining affiliate funding and cash position.
- Built and led department of 10 employees, with focus on employee performance and development.
- Implemented significant step-up in professionalism by projects in accounting, planning, reporting and logistics processes, as well as training and anchoring business ethics.
- Instituted improvements in employee conditions and company image, among other by driving office relocation project and deployment of a car fleet for the sales force and management.

#### Novo Nordisk A/S | Financial Risk Manager

2011 - 2014 | Corporate Treasury, Denmark

- Drove capital structure management through recommendations to the executive management and board of directors, and subsequent implementation in the market via share buybacks, stock splits, etc.
- Drove currency hedging with OTC traded derivatives, responsible for strategy, planning and daily execution in response to changes in company exposure.
- Project responsible to implement a quantitative currency hedging and risk mgmt. model with McKinsey and PwC.
- Recurring and ad hoc analyses of political and market developments, communication to senior management.
- Liaising with core banks for operations and tenders.
- Daily leadership of department graduate.

## Novo Nordisk A/S | Global Finance Graduate

2009-2011 | Denmark, India, China

- The program is a 2-year international talent program with rotations in four areas of the global finance org.
- Project mgmt. within "internal management consulting". Finance Projects, Denmark.
- Controlling, reporting and distributor management. Beijing, China.
- Off-shoring of financial processes from HQ and affiliates, change management. Bangalore, India.
- Measurement and reporting of enterprise risks vs. budgetary and strategic goals. Novo Nordisk Risk Office, Corporate Financial Planning, Denmark.

#### Jyske Bank A/S | Quant. Analyst

2007-2009 | Quantitative Research, Denmark

Development, implementation and testing of credit risk models on bank's loan portfolio.

## **TECHNICAL SKILLS**

Software dev, languages, build, test systems:

C, C++11/14/17, Python 3, Bash/sh, VBA • CMake 3.x • GoogleTest, Catch2 (C++) • Unittest, PyTest (Py)

Cloud, Linux, DevOps, Databases, Web

AWS • Oracle Linux 7, CentOS 7, Ubuntu 18.04, 20.04 • Git, Github • Docker, Compose • PostgreSQL, MongoDB • Django, Flask (Py), Nginx, HTML5/CSS/Bootstrap 4 • MQTT, TCP/IP/Sockets

Analysis, Machine Learning, DSP, Computer Vision

Matlab: Filter Design, DSP, Image Proc., Computer Vision, Control System • Python: NumPy, SciPy, Pandas, Scikit-learn, Keras, Matplotlib, Seaborn • OpenCV • SAS: BASE, STAT, IML • SAP: FI/CO, BW

#### Embedded

MCU, DSP: FreeRTOS • Freescale (Arm Cortex-M0+), Beagle (Cortex-A8) • TI C5535 DSP • CAN, I<sup>2</sup>C, SPI, UART • SoC, FPGA: VHDL, HLS/SDS • Xilinx Vivado, SDK, SDSoC, Vitis • Xilinx Zynq-7000 SoC, Artix-7 FPGA