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# Languages and Machines

## L7: CFLs and Pushdown Machines

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Regular  $\leftrightarrow$  Finite State Machines (FSMs)

**Context-free**  $\leftrightarrow$  **Pushdown Machines**

Context-sensitive  $\leftrightarrow$  Linearly-bounded Machines

Decidable  $\leftrightarrow$  Always-terminating Turing Machines

Semi-decidable  $\leftrightarrow$  Turing Machines

## Context-Free Grammars

## Simple Pushdown Machines Examples

## Variants of PDMs

## CFGs and PDMs

From CFGs to PDMs

From PDMs to CFGs

## Closure Properties for CFLs



A context-free grammar is

$$G = (V, \Sigma, P, S)$$

where:

- $V$  is a set of non-terminals
- $\Sigma$  is a set of terminals
- $P$  is a set of production rules (e.g.  $A \rightarrow abAb$ )
- $S \in V$  is the starting symbol

# Balanced Parentheses



An archetypical example of a context-free language:  
the set of balanced strings of parentheses '[' and ']' .

A string of parenthesis is **balanced** if:

1. Each left parenthesis has a matching right parenthesis.
2. Matched pairs are well nested.

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For instance, ‘[ [ ] [ ] ]’ is balanced but ‘] [ ’ and ‘[ [ ] [ [ ] ]’ are not.

It is generated by the following grammar:

$$S \rightarrow [S] \mid SS \mid \varepsilon$$



Given a string of parentheses  $x$ , let us write  $L(x)$  and  $R(x)$  to denote the number of left and right parentheses in  $x$ .

Formally, a string of parentheses  $x$  is **balanced** if and only if

- (i)  $L(x) = R(x)$
- (ii) for all prefixes  $y$  of  $x$ ,  $L(y) \geq R(y)$





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(ii) for all prefixes  $y$  of  $x$ ,  $L(y) \geq R(y)$

Conditions (i) and (ii) are both necessary and sufficient for a formal definition of balanced parentheses.

Example: ']' satisfies (i) but not (ii).



Consider conditions (i) and (ii) in the previous slide. We have:

## Theorem

*Let  $G$  be the CFG*

$$S \rightarrow [S] \mid SS \mid \varepsilon$$

*Then*

$$L(G) = \{x \in \{[, ]\}^* \mid x \text{ satisfies conditions (i) and (ii)}\}$$

As usual, the proof proceeds by showing two directions:

1. If  $S \Rightarrow^* x$  then  $x$  satisfies (i) and (ii)
2. If  $x$  is balanced then  $S \Rightarrow^* x$

## Direction 1: Proof Sketch



Induction on the **length of the derivation**  $S \Rightarrow_G^* \alpha$ .

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E.g.  $[[S]]$  is balanced,  $[[S[$  is not.

**Base case.** The “empty” derivation  $S \Rightarrow_G^* S$ . Erasing non-terminals we get an empty string, which is balanced.

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$$S \Rightarrow^n \beta \Rightarrow \alpha$$

By IH,  $\beta$  is balanced.

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Then the number/order of parentheses doesn't change, and the thesis holds easily

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$$S \Rightarrow_G^n \beta_1 S \beta_2 \Rightarrow_G \beta_1 [S] \beta_2$$

Condition (i) follows from the IH.

To show (ii), one checks prefixes  $\gamma$  of  $\alpha$ . There are three cases:  
 $\gamma$  is a prefix of (a)  $\beta_1$ , (b)  $\beta_1[S]$ , (c)  $\beta_1[S]\delta$  (where  $\delta$  is prefix of  $\beta_2$ ).

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- **Base case:**

If  $|x| = 0$  then  $x = \varepsilon$ . The only possible production rule is  $S \rightarrow \varepsilon$ .

- **Inductive case:**

We split the argument into two cases:

- a. There is a **proper prefix**  $y$  of  $x$  (i.e.,  $y \neq \varepsilon, y \neq x$ ) that enjoys (i,ii)
- b. Such a proper prefix doesn't exist

*Intuition:*

If such a prefix  $y$  exists then we can deduce that we can derive  $x$  starting with the production  $S \rightarrow S S$ .

Otherwise,  $x$  is of the form  $[z]$ , for some  $z$  that enjoys (i,ii).

We can derive  $x$  starting with the production  $S \rightarrow [S]$ .



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Simple Pushdown Machines  
Examples

Variants of PDMs

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# Simple Pushdown Machines



A **pushdown machine** is a tuple  $M = (Q, \Sigma, \Gamma, \delta, q_0, F)$  where

- $Q$  is a finite set (of states)
- $\Sigma$  is the input alphabet
- $q_0$  is a start state
- $F \subseteq Q$  is a set of accepting/final states
- $\Gamma$  is the alphabet for the **stack**, a last in / first out structure.
- $\delta$  is the transition function:

$$\delta : Q \times (\Sigma \cup \{\varepsilon\}) \times (\Gamma \cup \{\varepsilon\}) \rightarrow \mathcal{P}(Q \times (\Gamma \cup \{\varepsilon\}))$$

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$$\delta : \underbrace{Q}_{\text{state}} \times \underbrace{(\Sigma \cup \{\varepsilon\})}_{\text{input symbol}} \times \underbrace{(\Gamma \cup \{\varepsilon\})}_{\text{symbol to pop off}} \rightarrow \mathcal{P}(\underbrace{Q}_{\text{new state}} \times \underbrace{(\Gamma \cup \{\varepsilon\})}_{\text{symbol to push}})$$

## Intuition:

For every triple  $(q, a, X)$ ,  $\delta$  defines a set of pairs  $(r, Y)$

- In state  $q$ , symbol  $a$  can be read **if**  $X$  is at the top of the stack
- A transition replaces  $X$  with  $Y$ , and the machine moves to  $r$

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Acceptance:

Scan full input, halt with empty stack **and** in a final state.



## Example 1



We have  $M = (Q, \Sigma, \Gamma, \delta, q_0, F)$ , where:

- $Q = \{q_0, q_1\}$
- $\Sigma = \{a, b\}$
- $\Gamma = \{A\}$
- $F = \{q_1\}$
- The transition function  $\delta$ :

$$\delta(q_0, a, \varepsilon) = \{(q_0, A)\}$$

Add an  $A$  to the stack

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Non-deterministically move to  $q_1$

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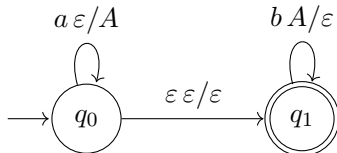
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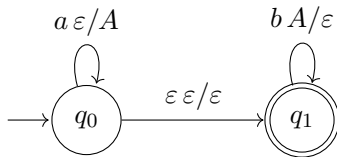
More conveniently:



## Example 1 (continued)



Accepting  $L_1 = \{a^n b^n \mid n \geq 0\}$ :



Key idea: Use stack symbol  $A$  to encode  $n$ .

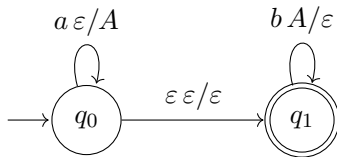
Example:

- Input:  $aaabbb$
- Stack:  $[\varepsilon]$

## Example 1 (continued)



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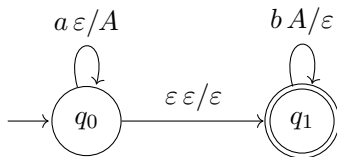
Example:

- Input:  $a \parallel aabbb$
- Stack:  $[A]$

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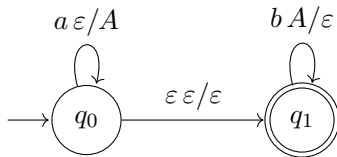
Example:

- Input:  $aa \parallel abbb$
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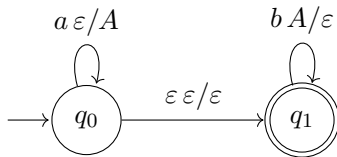
Example:

- Input:  $aaa \parallel bbb$
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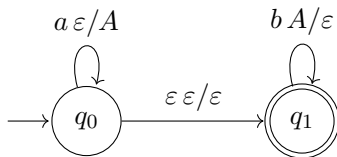
Example:

- Input:  $aaab \parallel bb$
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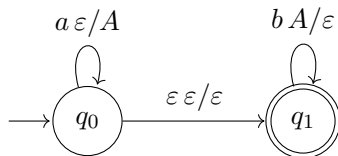
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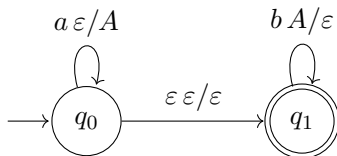
Example:

- Input:  $aaabbb$
- Stack:  $[\epsilon]$
- ✓ No input to read, empty stack,  $q_1$  is accepting

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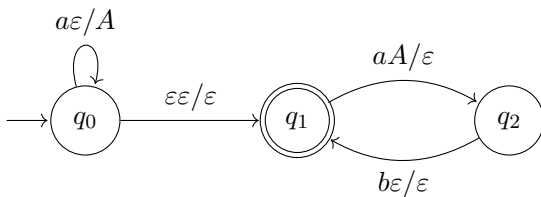
In contrast:

- ✗  $abb$  is not accepted: symbol  $b$  is left over, with an empty stack
- ✗  $aab$  is not accepted: no symbols to read, the stack is not empty

## Example 2



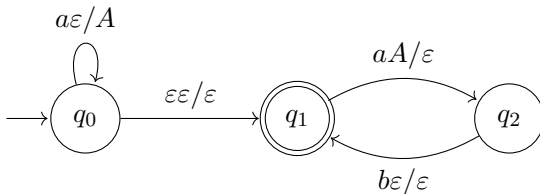
A simple PDM that accepts the language  $L_2$ :



## Example 2



A simple PDM that accepts the language  $L_2$ :



$$L_2 = \{a^i(ab)^i \mid i \geq 0\}$$

# Configurations and Acceptance



We want to define a *step* relation  $\vdash_M$  between configurations.

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A **configuration** for a PDM is defined as a triple  $[q, w, \beta]$  with

- $q \in Q$ : the current state
- $w \in \Sigma^*$ : the remainder of the input
- $\beta \in \Gamma^*$ : the current contents of the stack

Then:

- $[q, aw, X\gamma] \vdash_M [r, w, Y\gamma]$  if  $(r, Y) \in \delta(q, a, X)$
- $[q, w, X\gamma] \vdash_M [r, w, Y\gamma]$  if  $(r, Y) \in \delta(q, \varepsilon, X)$

Both  $X$  and  $Y$  can be  $\varepsilon$ .

**Intuitively:** If in state  $q$  symbol  $a$  is read from the input, symbol  $X$  is popped from the stack, and  $(r, Y) \in \delta(q, a, X)$  then the PDM can push symbol  $Y$  onto the stack and move to state  $r$ .

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The language accepted by a PDM:

$$L(M) = \{w \in \Sigma^* \mid \exists q \in F : [q_0, w, \varepsilon] \vdash^* [q, \varepsilon, \varepsilon]\}$$

Acceptance by **accepting state** and **empty stack**.

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## Variations on acceptance:

- By accepting state only (the stack may be not empty)
- By empty stack only (final state may not be accepting)

## Variations on the machine itself:

- Atomic PDMs  
Each transition performs one of three actions:  
pop the stack, push onto the stack, process an input symbol
- Extended PDMs  
Transitions push strings of symbols onto the stack, rather than just one symbol

All variants are equivalent to simple PDMs (with acceptance by both accepting state and empty stack)



We have seen: acceptance by **accepting state** and **empty stack**:

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Alternatives

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2. By empty stack only (final state may not be accepting)

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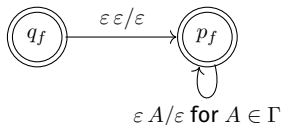
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Give a machine  $M'$  with new transitions that empty the stack.



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Give an  $M'$  identical to  $M$ , with all states defined as accepting.



## Atomic PDMs:

- Transitions have the form:

$(q_j, \varepsilon) \in \delta(q_i, a, \varepsilon)$  [read an input symbol]

$(q_j, \varepsilon) \in \delta(q_i, \varepsilon, A)$  [pop a stack element]

$(q_j, A) \in \delta(q_i, \varepsilon, \varepsilon)$  [push a stack element]

## Extended PDMs:

- Push a sequence of symbols onto the stack at the same time
- We modify the transition relation: from  $Q \times \Gamma$  to  $Q \times \Gamma^*$ :

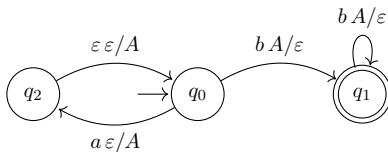
$$\delta : Q \times (\Sigma \cup \{\varepsilon\}) \times (\Gamma \cup \{\varepsilon\}) \rightarrow \mathcal{P}(Q \times \Gamma^*)$$



# Comparison



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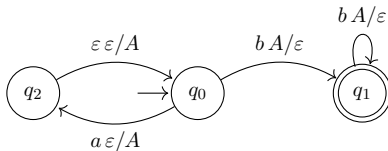


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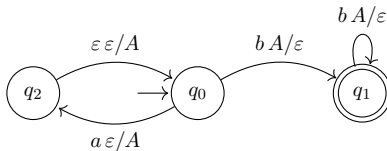


Q: What is the language recognized? A:  $\{a^i b^{2i} \mid i \geq 1\}$

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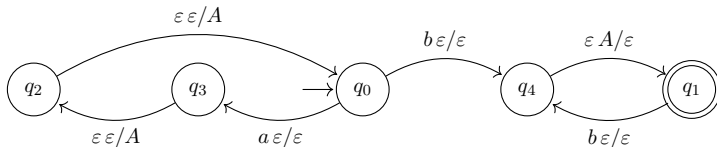


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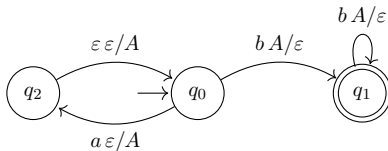
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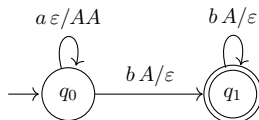


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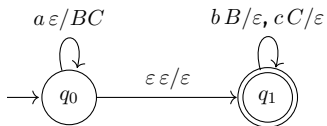


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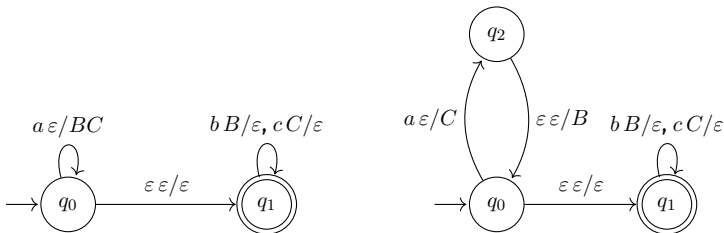
Extended PDM:



**Example:** An extended PDM, and its corresponding simple PDM

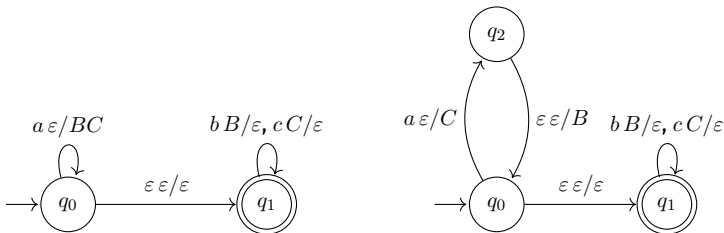


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Q: What is the language recognized?

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Q: What is the language recognized? A:  $\{a^n(bc)^n \mid n \geq 0\}$

Context-Free Grammars

Simple Pushdown Machines  
Examples

Variants of PDMs

CFGs and PDMs

From CFGs to PDMs

From PDMs to CFGs

Closure Properties for CFLs





Production rules like  $A \rightarrow a_1 A_1 \dots A_n$   
can be read “operationally” as a  
procedure  $A$  which:

1. read  $a_1$
2. call procedure  $A_1$
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This corresponds to a *leftmost*  
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What do we need to implement these patterns? Call stack for CFGs, just jumps to RGs.

# From Context-Free Grammars to PDMs



- Assume we have  $G$  a **normalized** CFG: for every  $A \rightarrow w \in P$ ,  $w$  is either a single terminal or a string of non-terminals

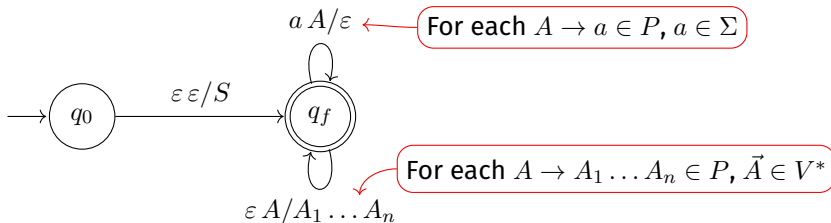
$$A \rightarrow a$$

$$A \rightarrow BCD$$

# From Context-Free Grammars to PDMs



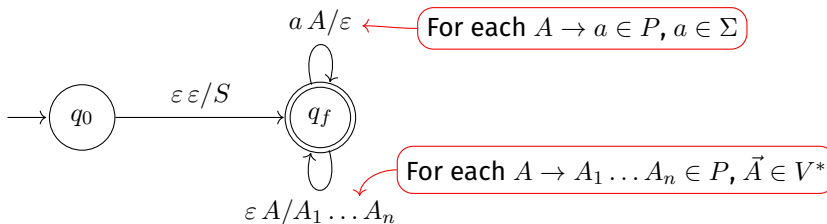
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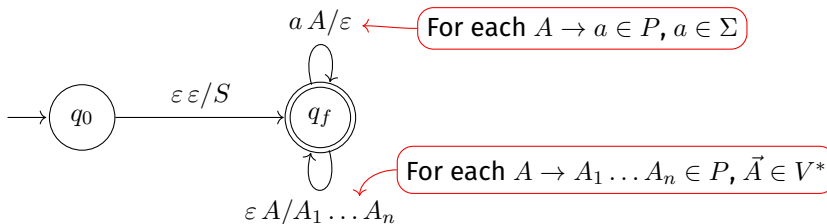


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- Notice: the stack only stores non-terminals:  $\Gamma = V$
- Given  $w \in \Sigma^*$ , we have the following equivalence:

$$[q_f, w, S] \vdash^* [q_f, v, \alpha] \equiv \exists u \in \Sigma^* : w = uv \wedge S \Rightarrow_{lm}^* u\alpha$$



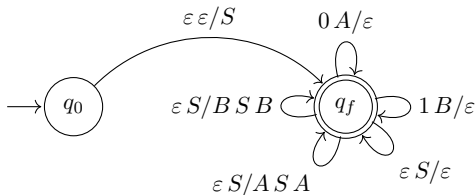
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Given the normalized grammar

$$S \rightarrow A S A \mid B S B \mid \varepsilon \quad A \rightarrow 0 \quad B \rightarrow 1$$

We have the following extended PDM:



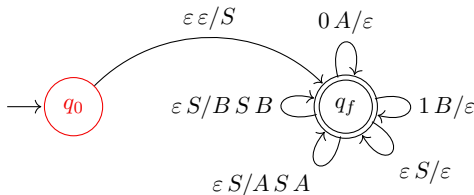
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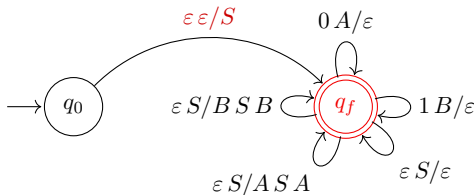
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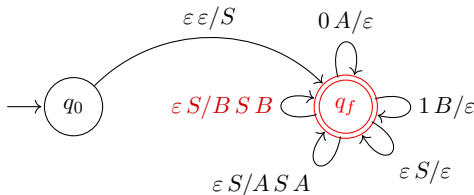
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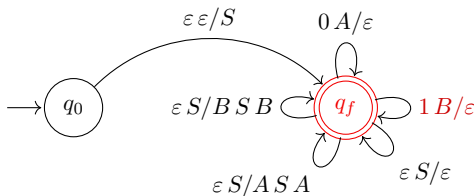
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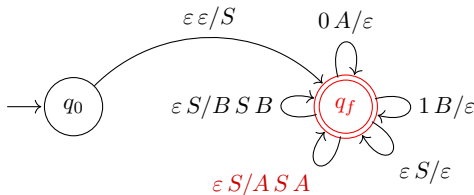
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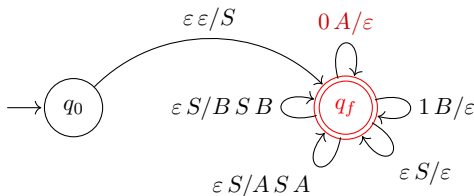
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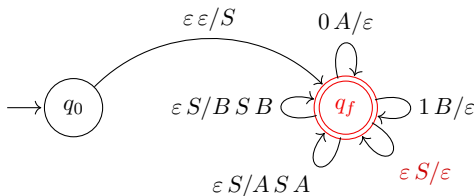
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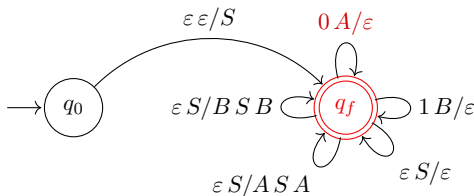
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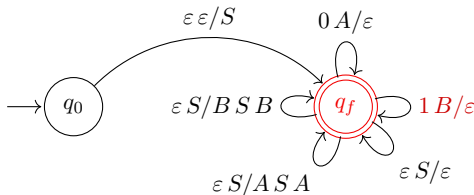
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Idea: given a machine  $M$ , non-terminals of the form  $S$  or  $\langle q, A, r \rangle$  for  $q, r \in Q, A \in \Gamma \cup \{\varepsilon\}$

$\langle q, A, r \rangle \Rightarrow w \sim M$  can read  $w$  from the state  $q$  with the stack  $A$ , and end with the empty stack in  $r$

Add the following production rules:

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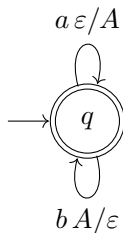
Use  $A$  later from an intermediate  $p$

Done processing

# Example



1.  $S \rightarrow \langle q, \varepsilon, q \rangle$
2.  $\langle q, \varepsilon, q \rangle \rightarrow \varepsilon \mid a \langle q, A, q \rangle$
3.  $\langle q, A, q \rangle \rightarrow b \langle q, \varepsilon, q \rangle \mid \langle q, \varepsilon, q \rangle \langle q, A, q \rangle$



Context-Free Grammars

Simple Pushdown Machines  
Examples

Variants of PDMs

CFGs and PDMs  
From CFGs to PDMs  
From PDMs to CFGs

Closure Properties for CFLs



# Closure Properties for CFLs



- CFLs are closed under union, concatenation, and Kleene star  
In all cases: construct a CFG from the CFGs of the given CFLs
- CFLs are *not* closed under intersection  
Take CFLs  $L_1 = \{a^i b^i c^k \mid i, k \in \mathbb{N}\}$ ,  $L_2 = \{a^i b^k c^k \mid i, k \in \mathbb{N}\}$ .  
But  $L_1 \cap L_2 = \{a^n b^n c^n \mid n \in \mathbb{N}\}$  is not a CFL  
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- CFLs are *not* closed under complementation

Assume, for a contradiction, closure under complementation.

Let  $L_1, L_2$  be any CFLs. Then  $L = \overline{\overline{L_1} \cup \overline{L_2}}$  is CFL.

Now, by De Morgan's law,  $L = L_1 \cap L_2$ ; this contradicts the above.

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Now, by De Morgan's law,  $L = L_1 \cap L_2$ ; this contradicts the above.
- If  $R$  is a regular language and  $L$  is a CFL, then  $R \cap L$  is CFL  
Take a DFSA recognizing  $R$  and a simple PDA recognizing  $L$ .  
Build a PDA that applies both machines simultaneously.



- Context-free languages/grammars
- Balanced parenthesis
- Pushdown machines (PDMs): simple and extended
- From CFGs to PDMs
- From PDMs to CFGs
- Closure properties for CFLs

We didn't cover (self study!):

- Pumping Lemma for CFLs (Sect 4.2)

**Reading:** Reader: 4.1-4.3; Kozen: 19-25; Sudkamp: 7.1-7.5.

**Next Lecture(s):** Turing Machines