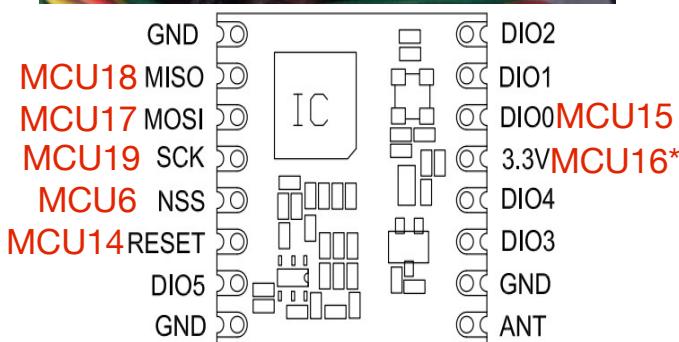
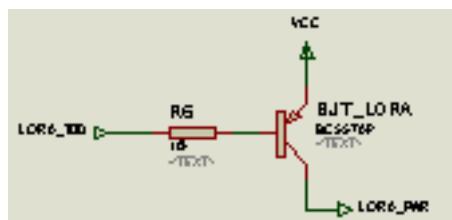
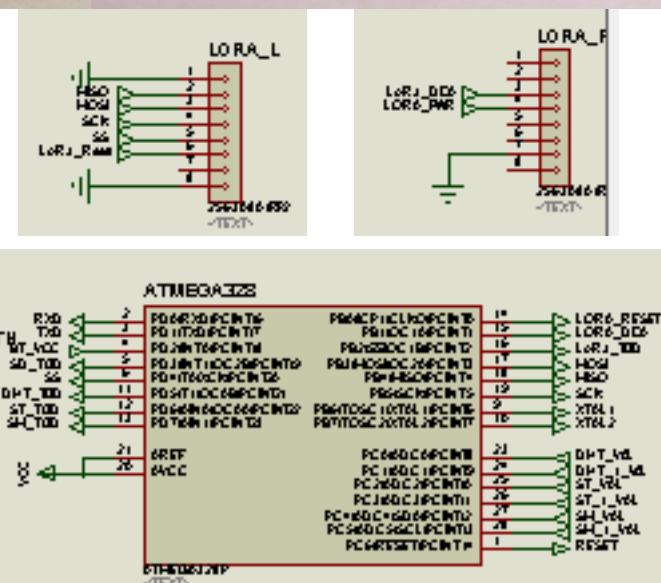
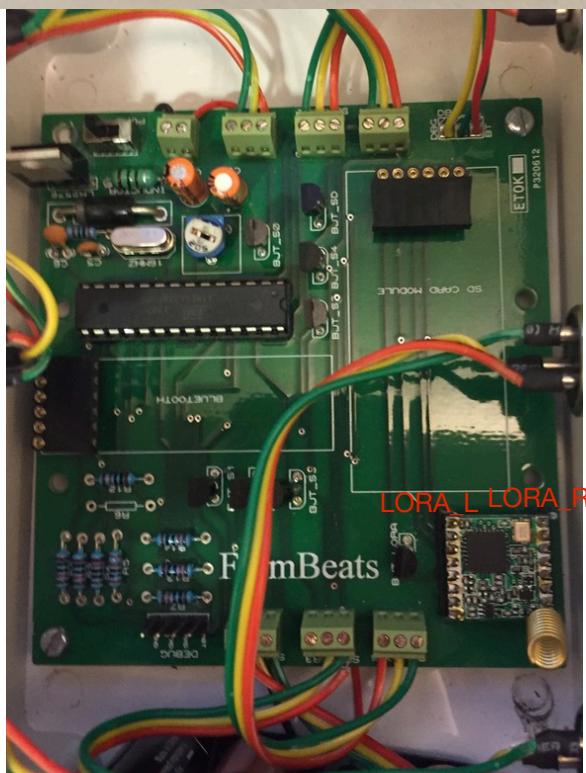
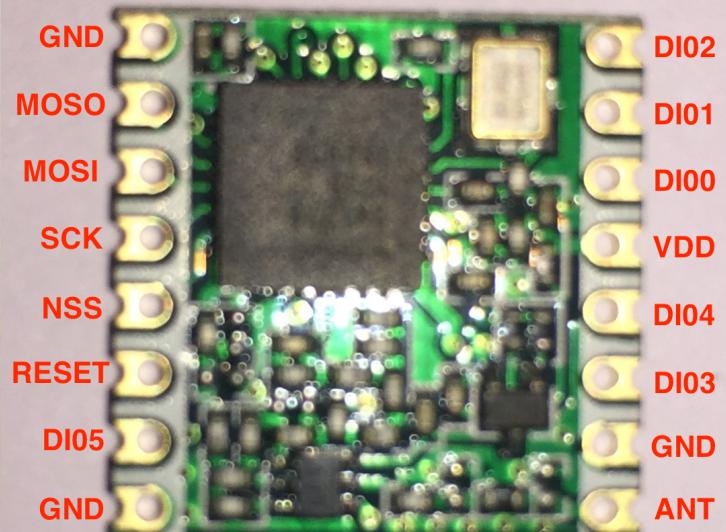
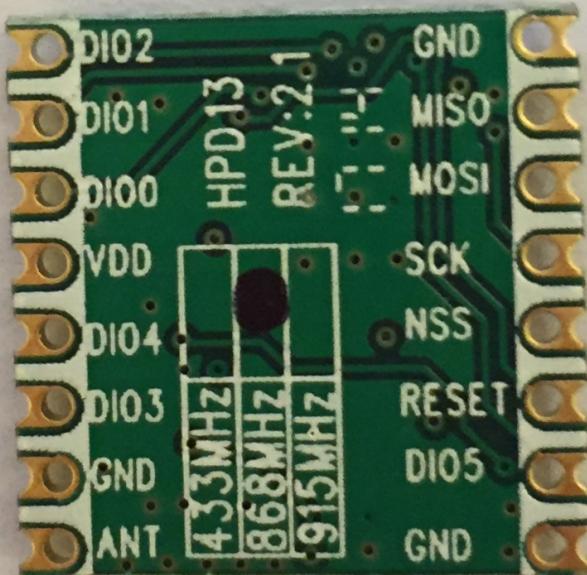


LORA 868MHZ 8X1278 RF MODULE - RFM95W COMPATIBLE



* via R6 and BJT_LORA

Figure 2. Pin Diagrams

Part Number	Frequency Range	Spreading Factor	Bandwidth	Effective Bitrate	Est. Sensitivity
RFM95W	868/915 MHz	6 - 12	7.8 - 500 kHz	.018 - 37.5 kbps	-111 to -148 dBm

Description			
Description Stand Alone Mode			
1	GND	-	Ground
2	MISO	I	SPI Data output
3	MOSI	O	SPI Data input
4	SCK	I	SPI Clock input
5	NSS	I	SPI Chip select input
6	RESET	I/O	Reset trigger input
7	DIO5	I/O	Digital I/O, software configured
8	GND	-	Ground
9	ANT	-	RF signal output/input.
10	GND	-	Ground
11	DIO3	I/O	Digital I/O, software configured
12	DIO4	I/O	Digital I/O, software configured
13	3.3V	-	Supply voltage
14	DIO0	I/O	Digital I/O, software configured
15	DIO1	I/O	Digital I/O, software configured
16	DIO2	I/O	Digital I/O, software configured

MCU

Atmega328

(PCINT14/RESET)	PC6	1	28	PC5 (ADC5/SCL/PCIN)
(PCINT16/RXD)	PD0	2	27	PC4 (ADC4/SDA/PCIN)
(PCINT17/TXD)	PD1	3	26	PC3 (ADC3/PCINT11)
(PCINT18/INT0)	PD2	4	25	PC2 (ADC2/PCINT10)
(PCINT19/OC2B/INT1)	PD3	5	24	PC1 (ADC1/PCINT9)
(PCINT20/XCK/T0)	PD4	6	23	PC0 (ADC0/PCINT8)
	VCC	7	22	GND
	GND	8	21	AREF
(PCINT6/XTAL1/TOSC1)	PB6	9	20	AVCC
(PCINT7/XTAL2/TOSC2)	PB7	10	19	PB5 (SCK/PCINT5)
(PCINT21/OC0B/T1)	PD5	11	18	PB4 (MISO/PCINT4)
(PCINT22/OC0A/AIN0)	PD6	12	17	PB3 (MOSI/OC2A/PCI)
(PCINT23/AIN1)	PD7	13	16	PB2 (SS/OC1B/PCINT)
(PCINT0/CLKO/ICP1)	PB0	14	15	PB1 (OC1A/PCINT1)