

Semnale de control MIPS32

Instruc țiune	Opcode <i>Instr[31-26]</i>	Reg Dst	ExtOp	ALUSrc	Branch	Br_gtz	Jump	Mem Write	Memto Reg	Reg Write	ALUOp[1:0]	function <i>Instr[5-0]</i>	ALUCtrl[2:0]
add	000000	1	0	0	0	0	0	0	0	1	10(R)	100000	000(+)
sub	000000	1	0	0	0	0	0	0	0	1	10(R)	100010	100(-)
sll	000000	1	0	0	0	0	0	0	0	1	10(R)	000000	011(<<)
srl	000000	1	0	0	0	0	0	0	0	1	10(R)	000010	101(>>)
and	000000	1	0	0	0	0	0	0	0	1	10(R)	100100	001(&)
or	000000	1	0	0	0	0	0	0	0	1	10(R)	100101	010()
xor	000000	1	0	0	0	0	0	0	0	1	10(R)	100110	110(^)
sra	000000	1	0	0	0	0	0	0	0	1	10(R)	000011	111(>>a)
addi	001000	0	1	1	0	0	0	0	0	1	00(+)	xxxxxx	000(+)
lw	100011	0	1	1	0	0	0	0	1	1	00(+)	xxxxxx	000(+)
sw	101011	x	1	1	0	0	0	1	x	0	00(+)	xxxxxx	000(+)
beq	000100	x	1	0	1	0	0	0	x	0	01(-)	xxxxxx	100(-)
andi	001100	0	0	1	0	0	0	0	0	1	11(&)	xxxxxx	010(&)
bgtz	000111	x	1	0	0	1	0	0	x	0	01(-)	xxxxxx	100(-)
j	000010	x	x	x	x	0	1	0	x	0	xx	xxxxxx	xxx