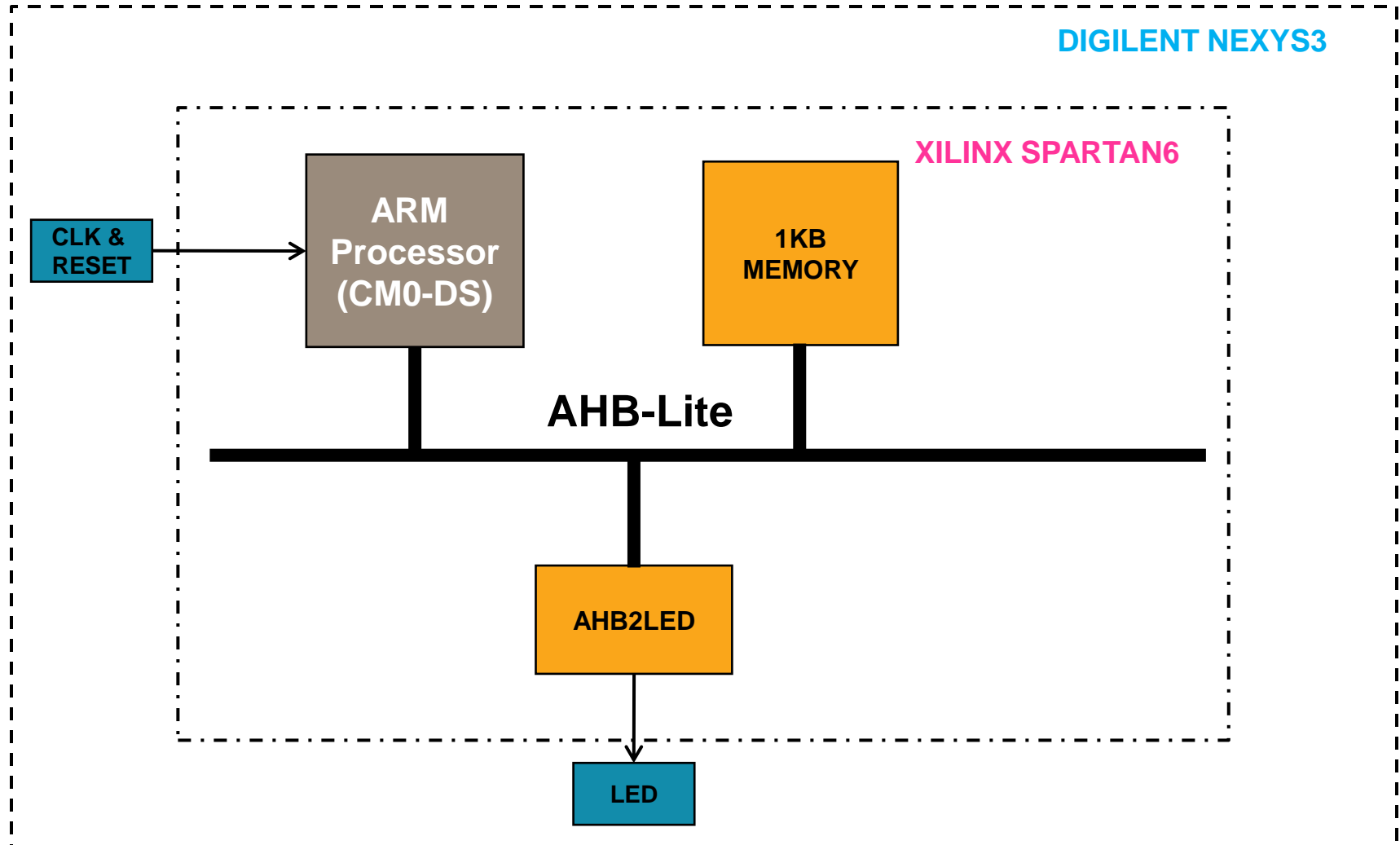


# Example System

## ARMSOC\_1



# ARMSOC\_1 (Hello World)



# Module Hierarchy



# Memory Map

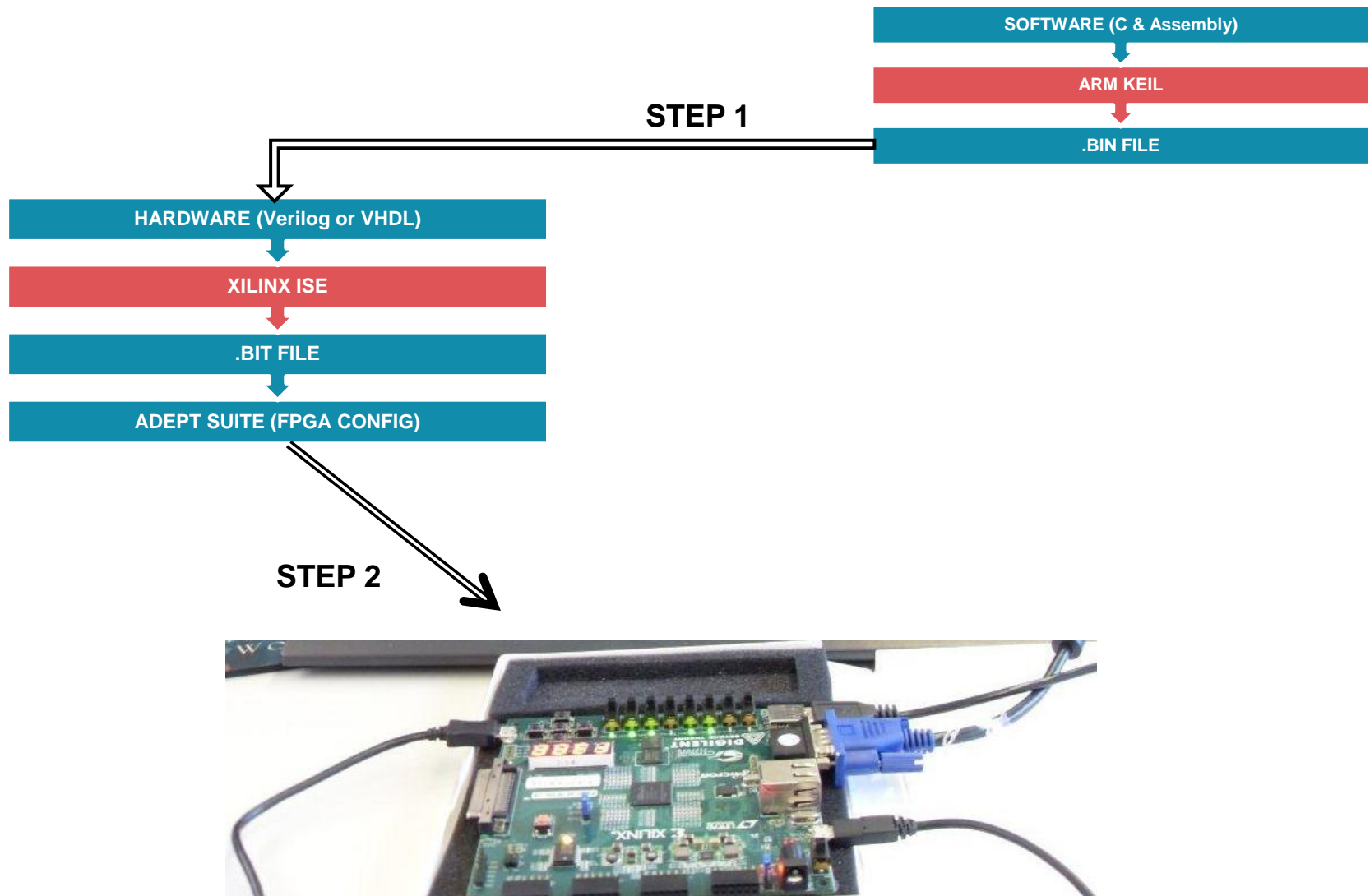
---

	START ADDRESS	END ADDRESS	SIZE
MEM	0x0000_0000	0x0000_03FF	1KB (256 Words)
LED	0x5000_0000	0x50FF_FFFF	16MB

# System Software

```
03          PRESERVE8
04          THUMB
05
06          AREA    RESET, DATA, READONLY
07          EXPORT  __Vectors
08
09  __Vectors          DCD      0x0000FFFC
10                    DCD      Reset_Handler
11                    DCD      0
12                    DCD      0
13                    DCD      0
14                    DCD      0
15
16                    .
17                    .
18                    .
19                    .
20                    .
21                    .
22                    .
23                    .
24                    .
25                    .
26                    .
27                    .
28                    .
29                    .
30                    .
31                    .
32
33
34
35
36
37
38
39
40
41          DCD      0
42          DCD      0
43          DCD      0
44
45          AREA |.text|, CODE, READONLY
46  ;Reset_Handler
47  Reset_Handler  PROC
48                  GLOBAL Reset_Handler
49                  ENTRY
50
51                  LDR      R1, =0x50000000
52                  LDR      R0, =0x55
53                  STR      R0, [R1]
54  HERE
55                  ENDP
56
```

# DESIGN FLOW (NEXYS3)



**THE END**

---