

Entrada 1



```
graph LR; E1[Entrada 1] --> D[Divider]; E2[Entrada 2] --> D; D --> S[Saída];
```

The diagram shows a central rectangular block labeled "Divider". To the left of the block, there are two horizontal lines representing input ports, labeled "Entrada 1" (top) and "Entrada 2" (bottom). To the right of the block, there is a single horizontal line representing an output port, labeled "Saída". The entire diagram is rendered in a clean, minimalist style with black lines and text on a white background.

Entrada 2

Divider

Saída