

HW6

1.

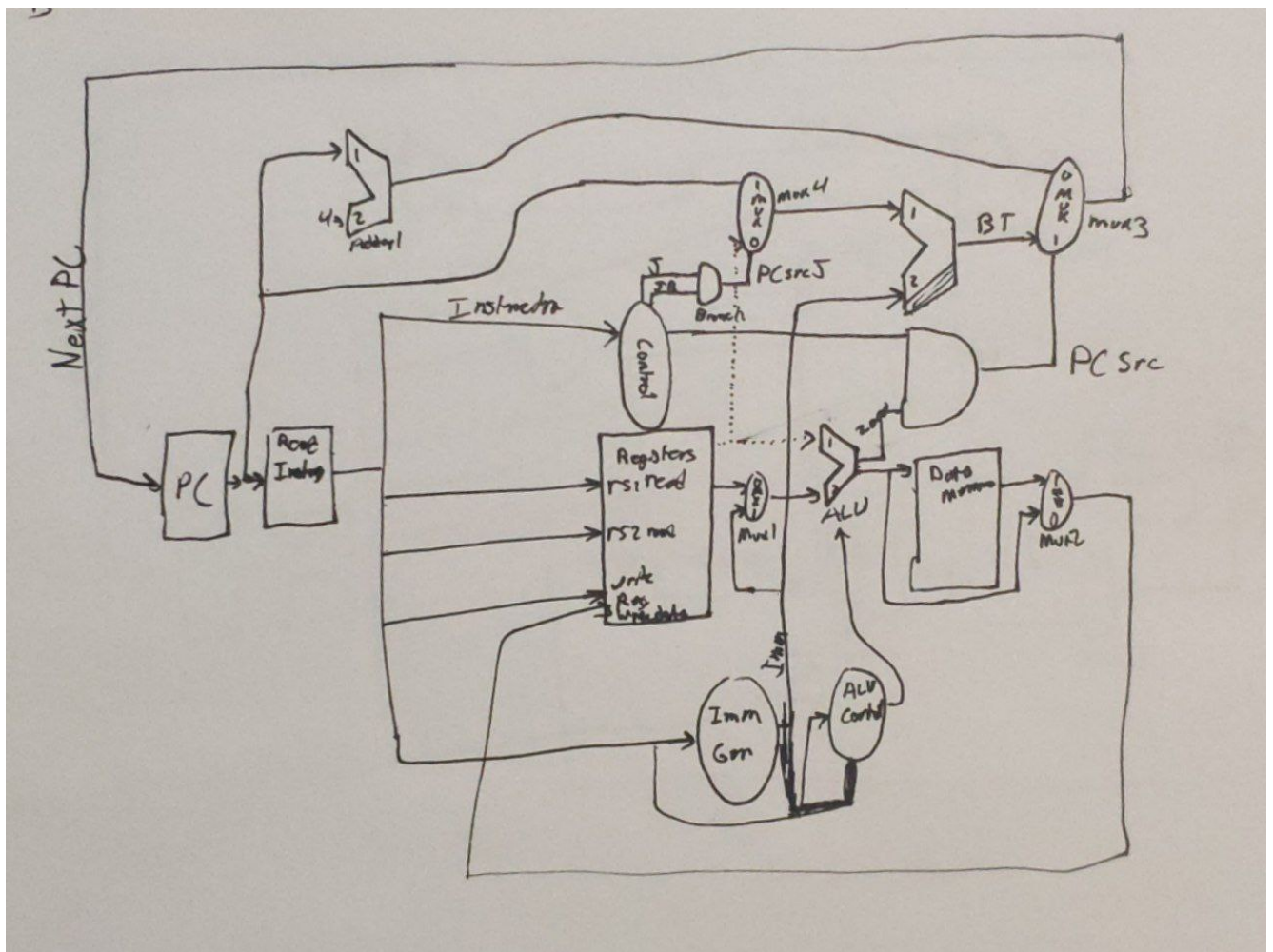
1111111 00101 01000 010 00000 0100011

SW

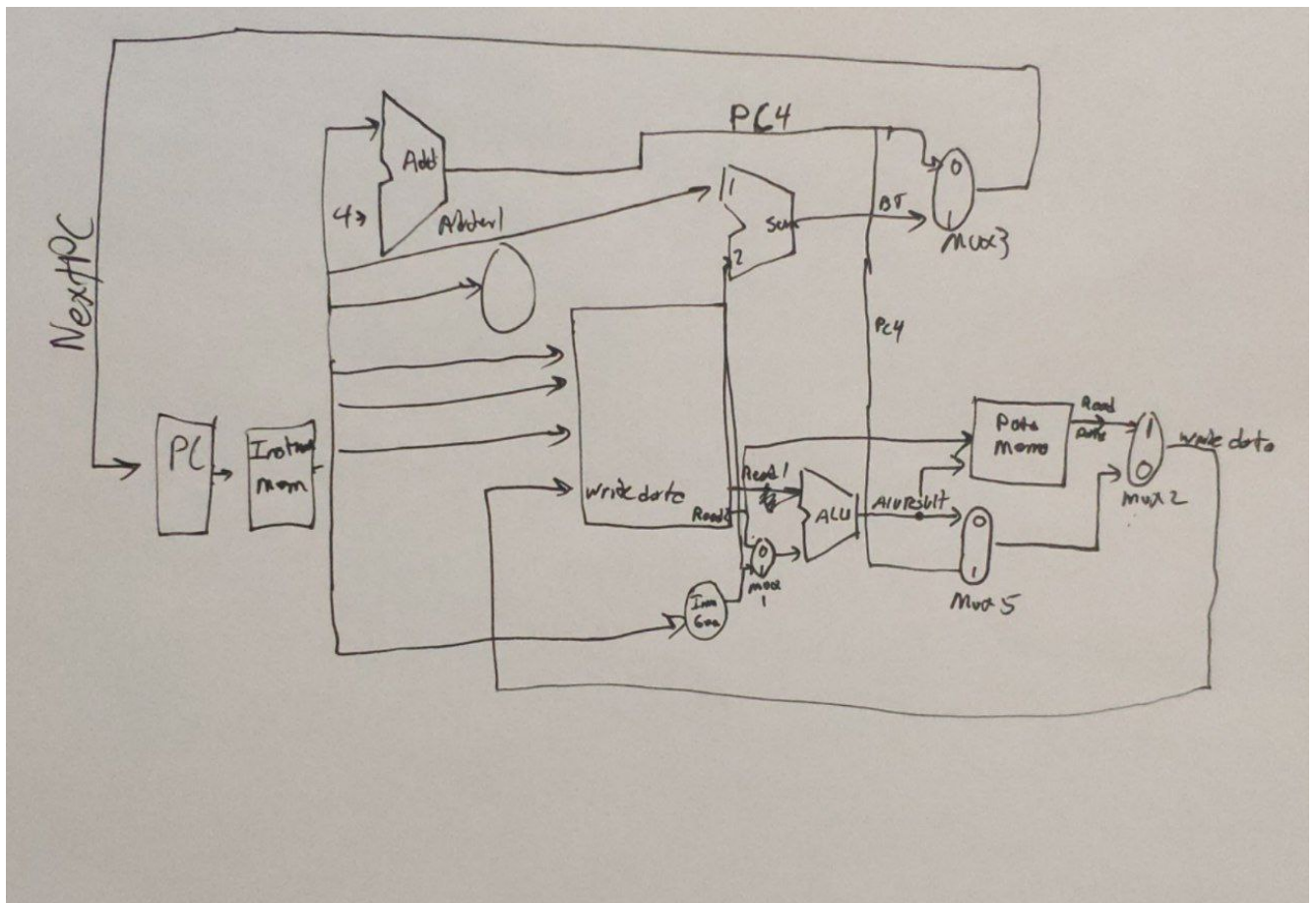
- Opcode = 0100011
- RS1 = 01000
- RS2 = 00101
- RD = 00000
- ALU op = 00
- ALU operation = 0010
- PCSrc = 0
- Immediate = 111111100000 = 0xFFFFFEE0
- BT = 01000000000000010000000000 - 32
 - 0100000000000000111100000 = 0x004001E0
- NextPC = 0x00400204

2. Add JAL and JALR

a.



b.



c.

Instruction	ALU Source	MemtoReg	Reg Write	Mem Read	Mem Write	Branch	J	JR
JAL	X	0	1	0	0	0	1	0
JALR	X	0	1	0	0	0	1	1

- 2c JAL = x0100010
- 2c JALR = x0100011