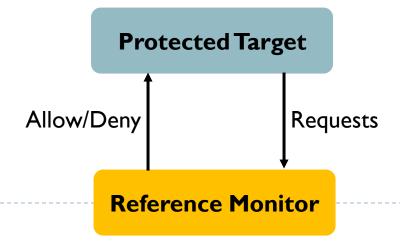
Reference Monitor (RM)

A conceptual framework

- Enforces access control policies over any protected target in a system.
- Mediates all access requests, and deny any request that violates policy

Significance

- Trusted Computer System Evaluation Criteria (TCSEC) emphasizes the necessity of a reference monitor in achieving higher security
- RM serves as the foundation for various security models, ensuring that the access control policies are consistently enforced across the system



Requirements of RM

Function requirement

- ▶ RM must intercept and evaluate every access request without exception.
- RM is able to deny the malicious requests

Security requirement

RM must be tamper-proof, and protected from unauthorized modification to maintain its integrity

Assurance requirement

The validation mechanism must be small enough to be thoroughly analyzed and tested for correctness.

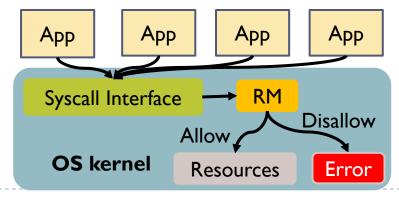
Example: OS-based RM

A core component within the OS kernel

- Enforce access control policies by monitoring and mediating all system calls made by applications.
- Ensure that all applications operate within their authorized permissions, preventing unauthorized access to system resources, including file operations, network communications, and process control.

Implementation

- Intercept all system calls, check permissions and allow/disallow execution.
- Typical examples: Security-Enhanced Linux (SELinux)



Example: Application-based RM

A security mechanism embedded within applications

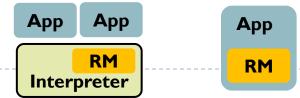
Enforce access control policies, provide fine-grained control over application behaviors, and prevent unauthorized actions.

Integrating RM with interpreter

- Every operation will be checked against security policies before execution
- Example: JavaScript engine enforces sandboxing by restricting access to certain APIs or resources during script execution.

Inline RM

- Inserting RM directly into the application's code. This could be achieved with source code instrumentation, or binary rewriting.
- Example: StackGuard



Example: Hardware-based RM

Responsible for monitoring and regulating all the software activities, including OS kernel.

Any operation violating the security policy will throw a hardware exception

Hardware-based RMs conduct various checking

- Memory access management.
 - If each memory access is within the process' memory range.
 - If each access follows the allowed permission (read, write, executable, set in the Page Table Entry). Recall the Non-executable Memory mechanism.
- Privilege mode management.
 - At any time, CPU can be in one mode, either user or kernel.
 - Privileged instructions can only be issued in kernel mode.
 - Context switch is required for user mode to call privileged functions.