ANALOG DEVICES

AD1100 DEVICE DRIVER RELEASE NOTES   SW1.0.0

BUILD TYPE: **RELEASE**

RELEASE DATE: **AUGUST 18, 2021**

INCLUDED DELIVERABLES: **SOFTWARE INSTALLER FOR ARM SOURCE CODE, API, EXAMPLE CODE**

PREVIOUS RELEASE BUILD: 0.4.0
   (NOTE: This is the reference baseline for all changes outlined in this document.)

OVERVIEW

The SW1.0.0 RELEASE build provides incremental updates to support the ADIN1100 10BASE-T1L PHY device.

The supported features included in this build are:
- Support for U1, U2 and U3
- PHY powerup and initialization
- Auto-negotiation configuration for master/slave and Tx levels
- Reading auto-negotiation and link status
- Loopback modes
- Test modes
- Link quality
- Frame generator and checker
- Clause 22 access (U2/U3 only)

The following sections describe the changes and enhancements provided in this build.

SOURCE CODE CHANGES

- Header files update
- Unmask by default all bits in CRSM_IRQ_STATUS register that can indicate a hardware error
- Fixed an issue with PHY register accesses when both MDIO_GPIO and MDIO_CL22 symbols are defined

API CHANGES

- No changes to previous APIs

KNOWN ISSUES/LIMITATIONS

ADDITIONAL INFORMATION