Total Pages: 2

Pag Na.	
Reg No.:	Name:

## APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY FIRST SEMESTER MCA DEGREE EXAMINATION, DECEMBER 2017

Course Code: RLMCA109

		Course Name: DIGITAL FUNDAMENTALS	
Max	x. M	arks: 60 Duration:	3 Hours
		PART A	
Answer all questions, each carries 3 marks.		Mark	
1 2 3 4 5		Convert 3.248 x 10 <sup>4</sup> into single precision floating point binary number.  Justify the statement: NAND and NOR gates are universal gates  Specify the minterms of A+BC.  Compare and contrast ripple carry adder and carry look ahead adder.  What is a de-multiplexer?	<ul> <li>(3)</li> <li>(3)</li> <li>(3)</li> <li>(3)</li> <li>(3)</li> </ul>
6 7 8		Differentiate between combinational logic and sequential logic circuits.  Why asynchronous counters are also known as ripple counters?  What do you mean by a Modulo-N Counter?	<ul><li>(3)</li><li>(3)</li><li>(3)</li></ul>
		PART B	
		Answer six questions, one full question from each module and carries 6 marks.	
		Module I	
9	a)	Convert 1110001.0001 to decimal and hexadecimal.	(3)
	b)	Given A= 1001010 and B=1000. Perform A-B, A/B and A x B.	(3)
OR			
10		Convert the pair into binary and add using 2's complement: -72 and 27.	(3)
	b)	Express -34 in sign magnitude, 1's complement form and 2's complement form.	(3)
		Module II	
11		State and prove (i) $A+A'B=A+B$ , (ii) $A+AB=A$ .	(3)
	b)	State and prove Demorgan's Theorems.	(3)
		OR	
12		Simplify using K-Map $y=\Sigma(0,1,3,5,9,12) + \Sigma d(2,4,6,7)$	(6)
		Module III	
13		Implement the Boolean function F (A, B, C, D) = $\sum (1,3,4,11,12,13,14,15)$ using 8-to-1 multiplexer.	(6)
		OR	
14		Explain the working principle of full adder in detail. Design a full adder using a decoder.	(6)
		Module IV	
15		J K flip-flop can be used for solving the 'indeterminate state' in SR Flip-flop.  Justify the statement.	(6)

board.

## OR What is the disadvantage of level triggering? How can we overcome it by using (6) 16 master slave Flip-flop? Module V Classify shift registers based on the data movement in register. OR 18 Differentiate between up asynchronous counter and down asynchronous counter (6) with suitable logic diagrams. Module VI 19 Describe the components of a computer with a block diagram. (6)20 Explain in detail about the hardware and software components of an Arduino (6)

\*\*\*\*