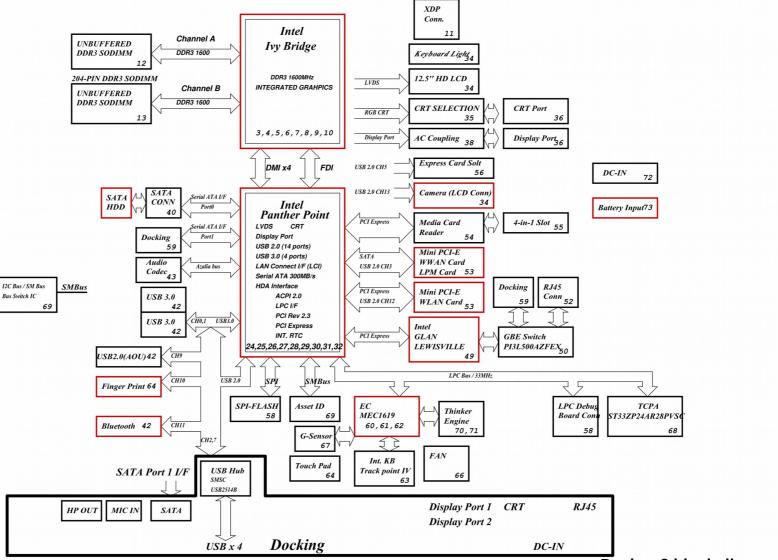
Regaining Trust in Everyday Computers the Hacky Way™

<33c3@jaseg.net>

What is the problem?

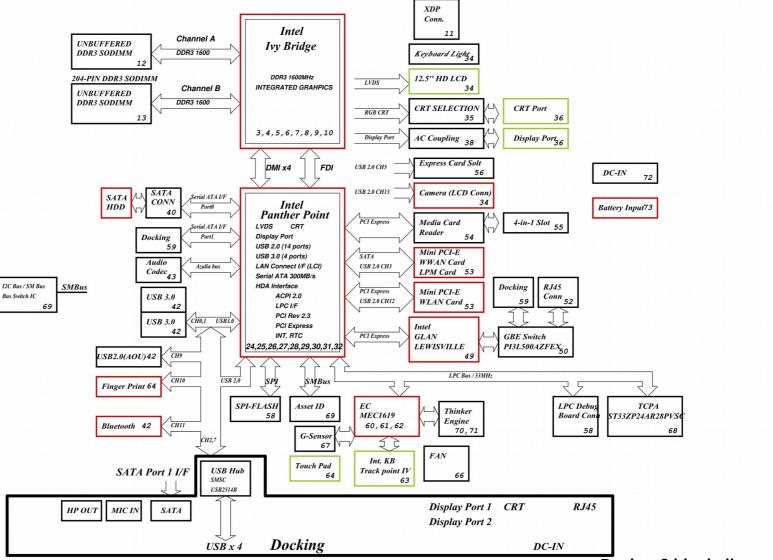


Specifically...

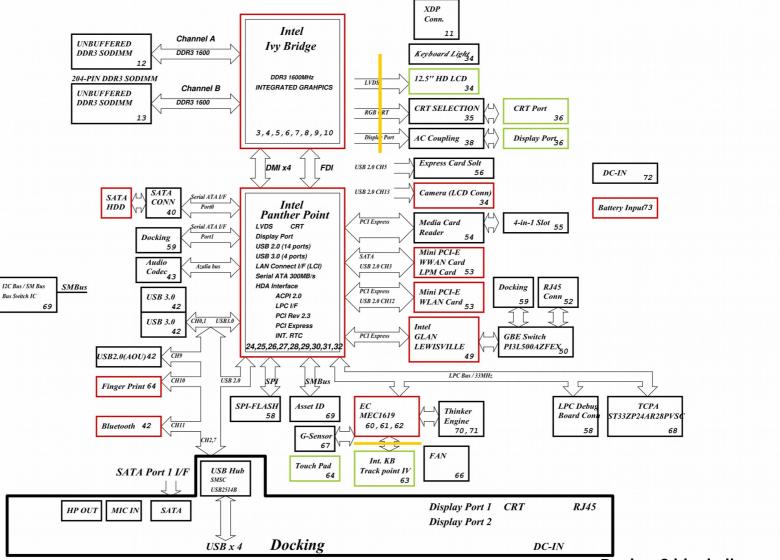


Dasher-2 block diagram, Lenovo

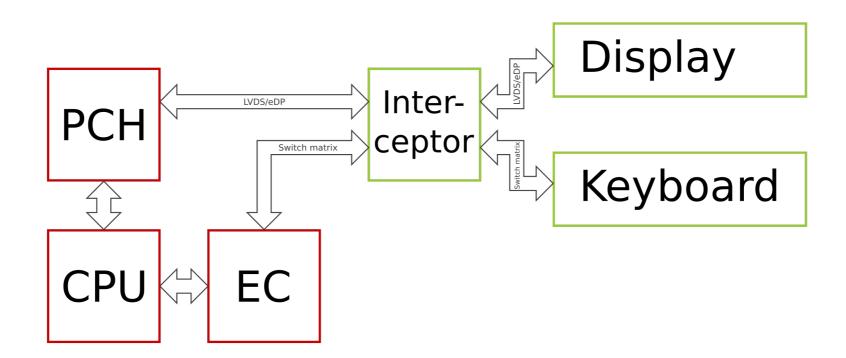


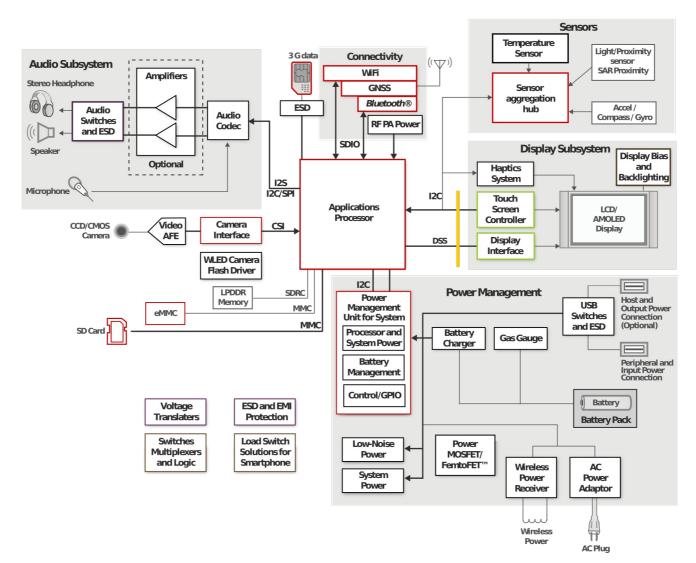


Dasher-2 block diagram, Lenovo

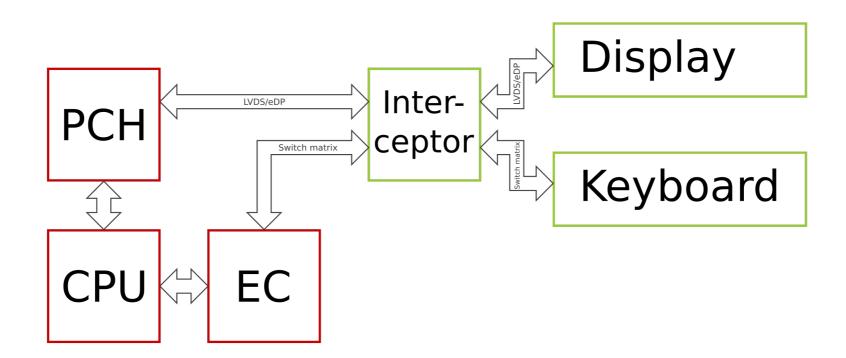


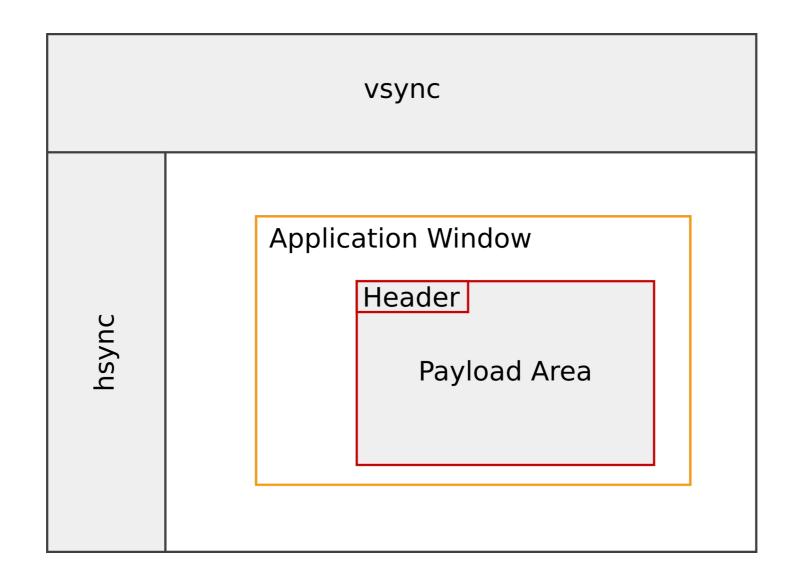
Dasher-2 block diagram, Lenovo

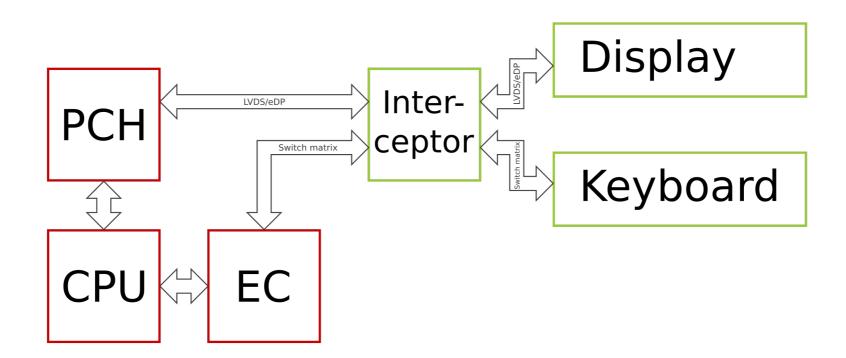


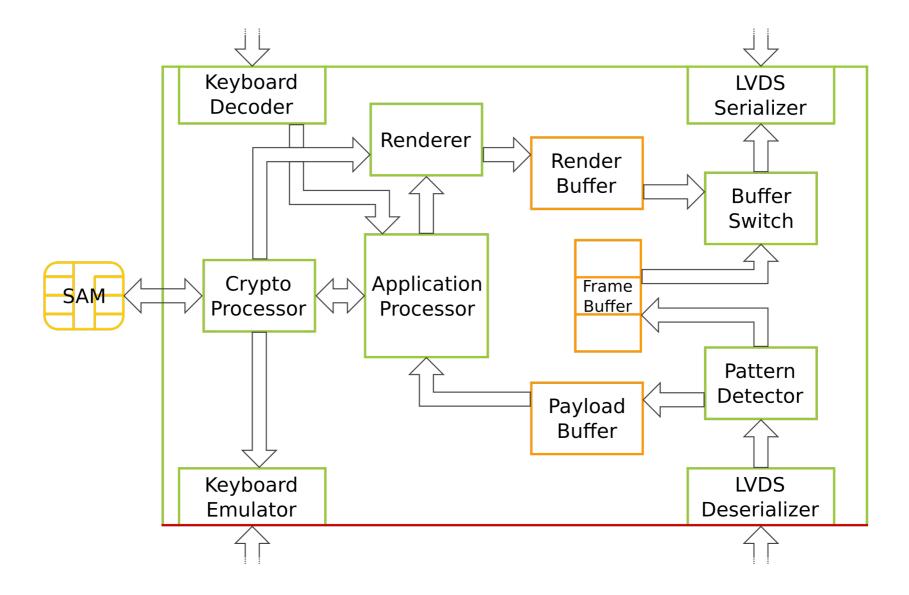


TI Smartphone Solutions Guide, Texas Instruments 2014









HSM Intrusion Detection

- Mesh: Continuity testing a printed maze trace on case inside
- RF Interference: Continuously measuring RF characteristics of wire stuffed into case chaotically
- Ultrasonic: Continuously measuring ultrasonic propagation inside potting material
- Triboluminescence: Photodiodes detecting mechanical disturbance of clear potting material loaded with "frictionglow"/"smash-glow" crystals

Attack Surface

Protocol Decoder -> Keep it simple, in hardware

Social Engineering → You are not idiots

Physical Compromise > Limited HSM features

FPGA Backdoor → Not terribly realistic?

Logic Flaws → Hardware-level Countermeasures

Device Loss → Continuous Re-authentication

Attack Scenarios

	w/o Interceptor	with Interceptor
Lost Device	?	✓
Police Raid/Unlocked Device Stolen	X	✓
Targeted Attack	×	✓
Audio/Video Bugging	X	X
Physical Access (Shipping/Border Checkpoint)	?	?
1984-style Total Communication Surveillance	✓	✓
Backdoored Interceptor	n/a	×

Major Tasks

- Disassembling and reassembling LVDS/eDP
- Rendering Unicode
- Defining a secure packet format
- Getting keyboard pass-through semantics right
- Splitting crypto in a meaningful way
- Finding and making use of a smart card

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github.com/jaseg/tachibana_talk