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VHDL lectures by Prof. Preeti Ranjan Panda "Synthesis of Digital Systems" NPTEL videos (lectures 3-6)
https://www.youtube.com/watch?v=arxCaTj8lJA&list=PLJ5C_6qdAvBGSzCBhvktFLeqUhZQKC-Gu

VHDL by examples
<http://esd.cs.ucr.edu/labs/tutorial>

Chapter-2 of the following book
[http://ebook.pldworld.com/_eBook/FPGA%EF%BC%8FHDL/-Eng-Digital%20Systems%20Design%20Using%20VHDL%20\(Charles%20Roth\).pdf](http://ebook.pldworld.com/_eBook/FPGA%EF%BC%8FHDL/-Eng-Digital%20Systems%20Design%20Using%20VHDL%20(Charles%20Roth).pdf)

Others:
https://www.youtube.com/watch?v=zm-RA6BsYmc&list=RDCMUC0wEPiFb0J6AZZ3oPXRoRpw&start_radio=1&rv=zm-RA6BsYmc --
by intel

<https://sites.google.com/site/learnvhdl/home/hour-01-digital-electronics-review-part-1?authuser=0>