

FbHash

Knez, Mežnar,
Pegan

Kazalo

Uvod

Sorodna dela

Algoritem

Eksperimenti v
članku

Naši
eksperimenti

Zaključek

FbHash: shema za izračun podobnosti datotek v digitalni

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- ▶ Sorodna dela
- ▶ Algoritem
- ▶ Eksperimenti v članku
- ▶ Naši eksperimenti
- ▶ Zaključek

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- ▶ Implementation bla bla:
 - ▶ bla bla bla
 - ▶ higher performance at smaller power consumption

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Zaključek

- ▶ Implementation bla bla:
 - ▶ bla bla bla
 - ▶ higher performance at smaller power consumption
- ▶ Some beautiful chips:
 - ▶ noise and learning ability of analogue and digital designs
 - ▶ ASIC and FPGA (field programmable gate array)

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- ▶ An approximate multiplier, introduced by Babic et al. (2010):
- ▶ The product of the two numbers, N_1 and N_2

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Theorem

The probability of an error in the circuit is directly proportional to the trouble it can cause.

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Theorem

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Proof.

The proof is straightforward. ☐

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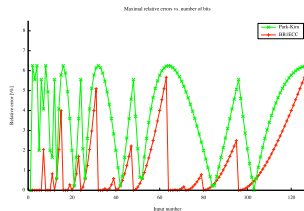
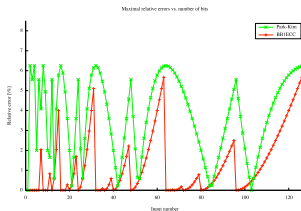
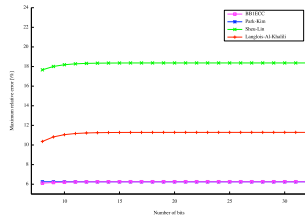
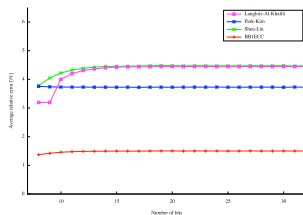


Figure: The l-o-n-g caption for all the subfigures (FirstFigure through FourthFigure) goes here.

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Zaključek

- ▶ The proposed approach improves the average and maximum relative errors compared to the existing square approximations.

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Zaključek

- ▶ The proposed approach improves the average and maximum relative errors compared to the existing square approximations.
- ▶ Error analysis has shown that an error in the circuit is directly proportional to the trouble it can cause.