

```
        * End Linear Resistor

** End Circuit Netlist **

** Analysis **
    .TF V(out_2) Vid1
    .op
    .TRAN 1n 150n
    *.ac dec 10 10 2000meg

* Output Swing
    *.DC Vswp 0.749 0.751 0.00001

* Common Mode Input Range
    *.DC Vswp 0 1.5 0.0001

** End Analysis

** Control Information **

    .options post=2 nomod

** End Control Information **

.END
```