/cpu/reset

/cpu/clk

/cpu/pc_out 0 1 2 3 2 3 2 3 2 3 2 3 2 3 2 3 2 3 2

/cpu/rs_out 0 2 0 3 0 4 0 5 0 6 0 7 0 8 0 9 0 ...

/cpu/rt_out 0 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1 0 1

0 ns   10 ns   20 ns   30 ns   40 ns   50 ns   60 ns   70 ns
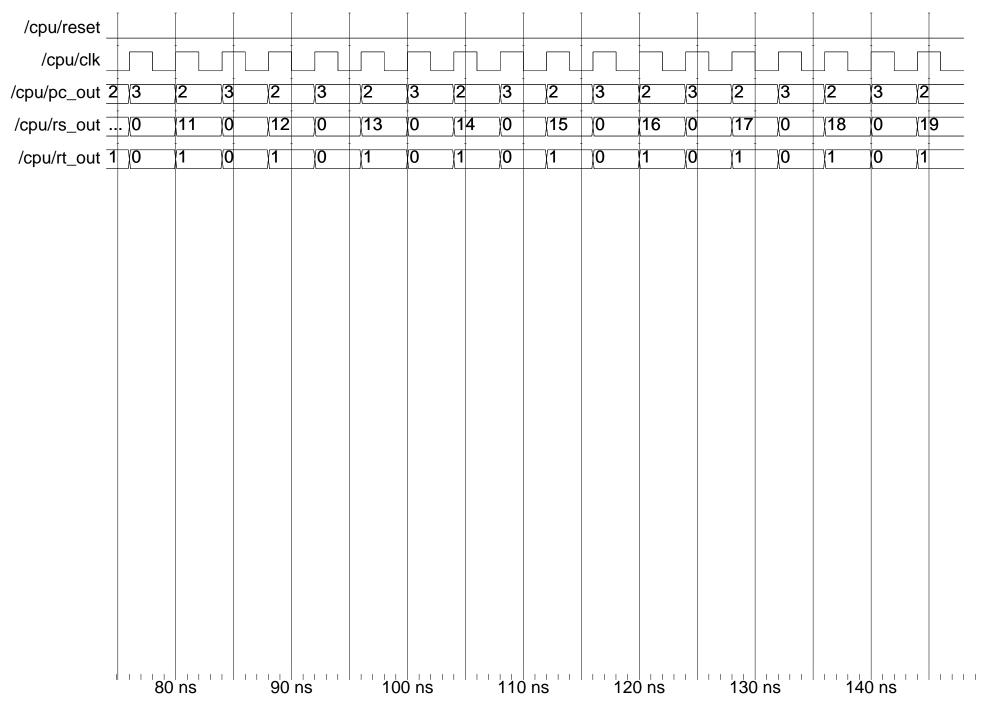
Entity:cpu Architecture:rtl Date: Mon Nov 28 05:29:58 PM EST 2022   Row: 1 Page: 1

Entity:cpu Architecture:rtl Date: Mon Nov 28 05:29:58 PM EST 2022   Row: 2 Page: 2