

CDA 3201L

Sequential Logic Circuits (III)

Lab 7

Demonstration Due Date: Friday, April 3rd by end of class.

Report Due Date: Sunday, April 5th by 11:59PM.

Part A

Design a nine-step counter to count in the following sequence using J-K flip-flops (TTL 7476):

0011, 0101, 1001, 1000, 1011, 1010, 0110, 0100, 0111, 0011,

Include in the design a means for resetting the counter to 0011.

IMPORTANT: You must demonstrate your solution to part on the breadboard. Lab grade will depend on the working of the circuit and will be checked off by the lab instructor. This demonstration must be completed within the window of your lab section meeting.