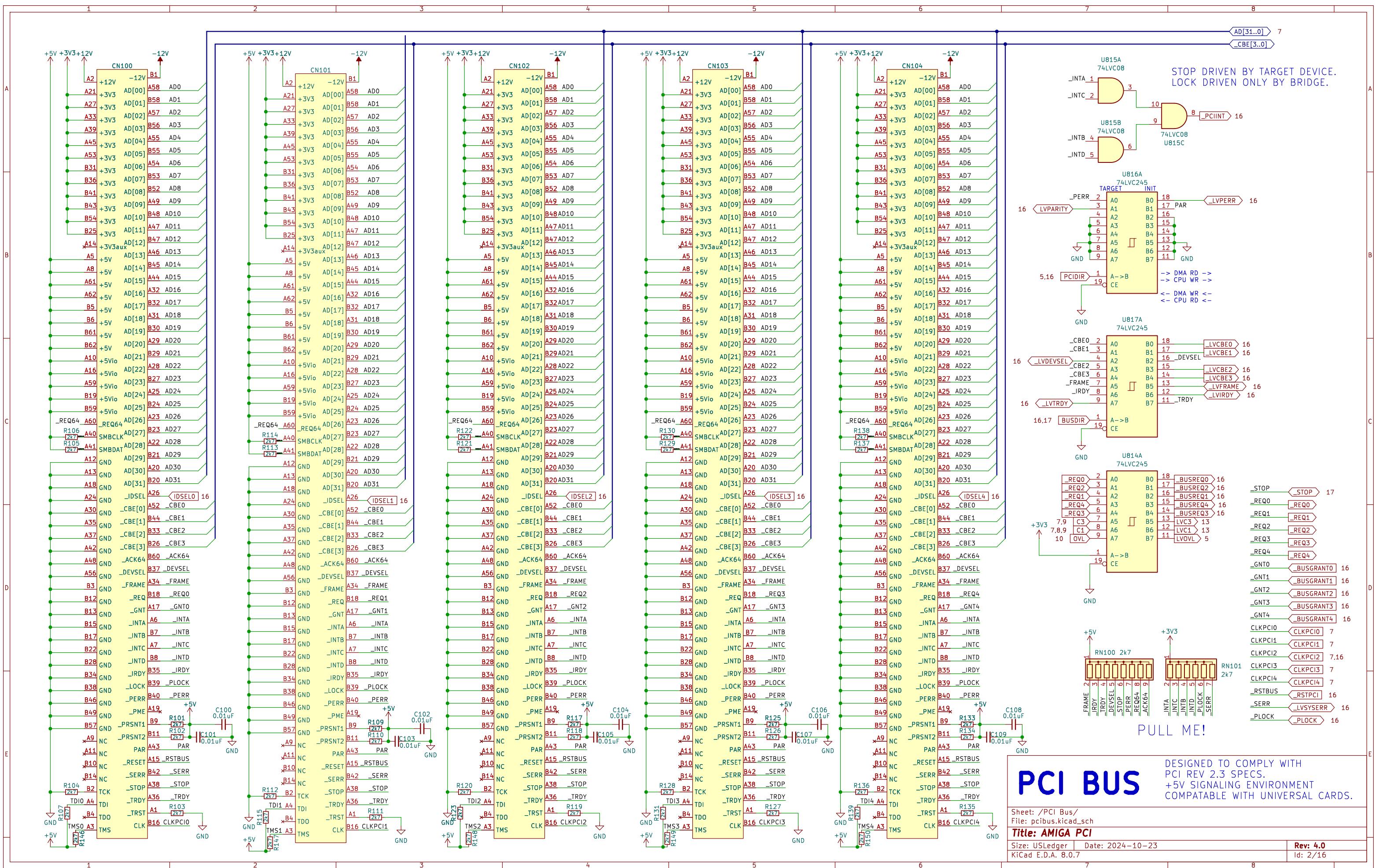
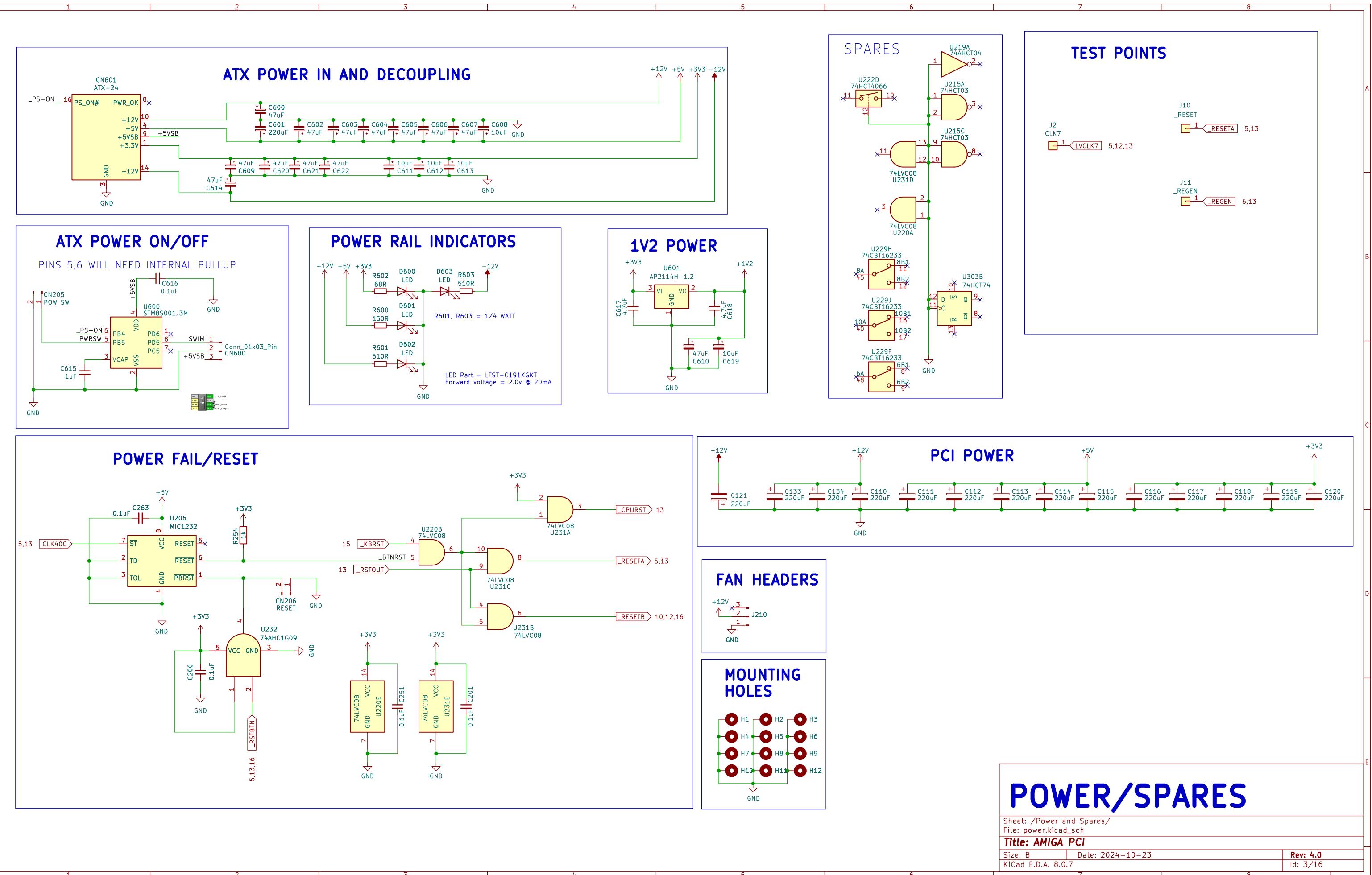


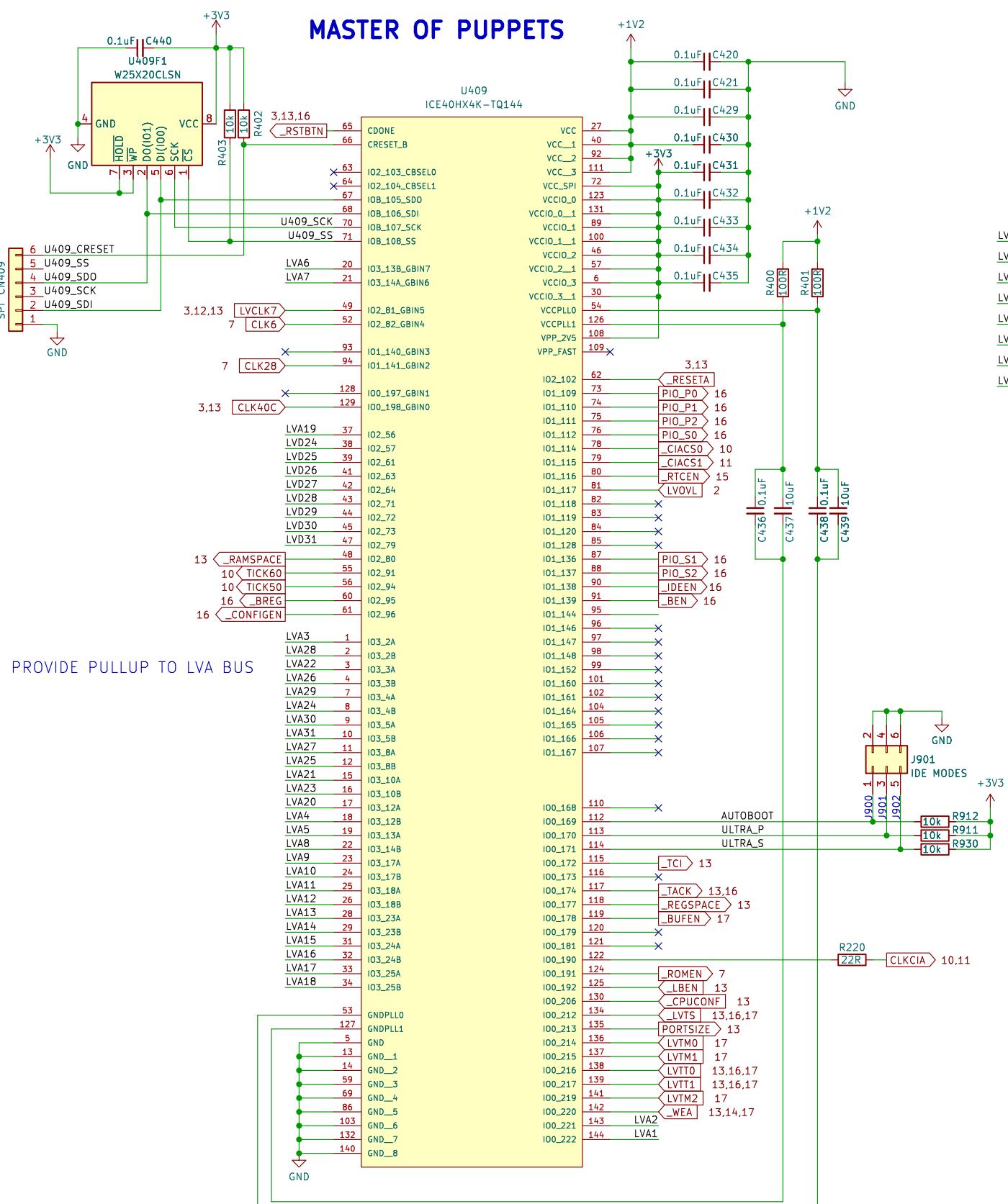


100 = PCI BUS
200 = CPU AND CHIPSET
300 = VIDEO
400 = FAST RAM
500 = AUDIO
600 = POWER
700 = CHIPSET RAM
800 = VOLTAGE SHIFTER
900 = ATA

BUDDY







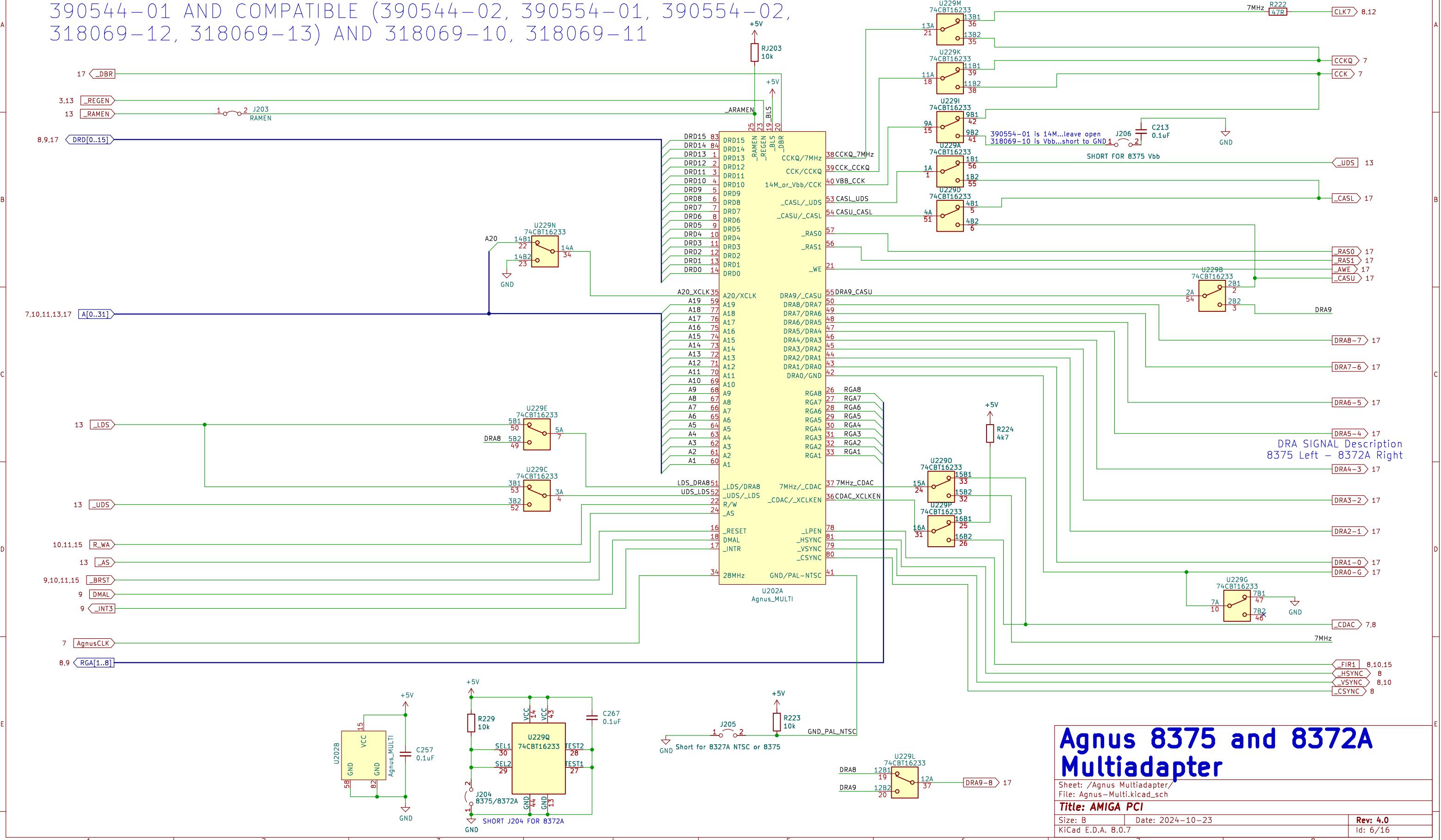
SUPPORTS 8372A (ALL PART NUMBERS)

AND 8375 THE FOLLOWING PART NUMBERS:

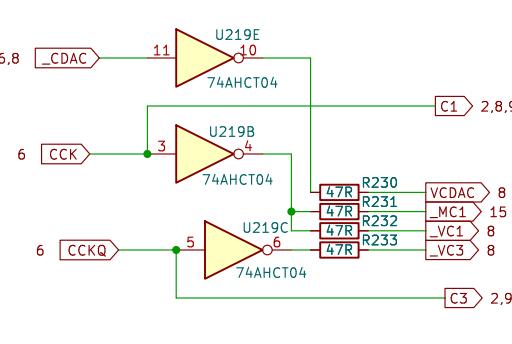
390544-01 AND COMPATIBLE (390544-02, 390554-01, 390554-02
+5V

318069-12, 318069-13) AND 318069-10, 318069-11

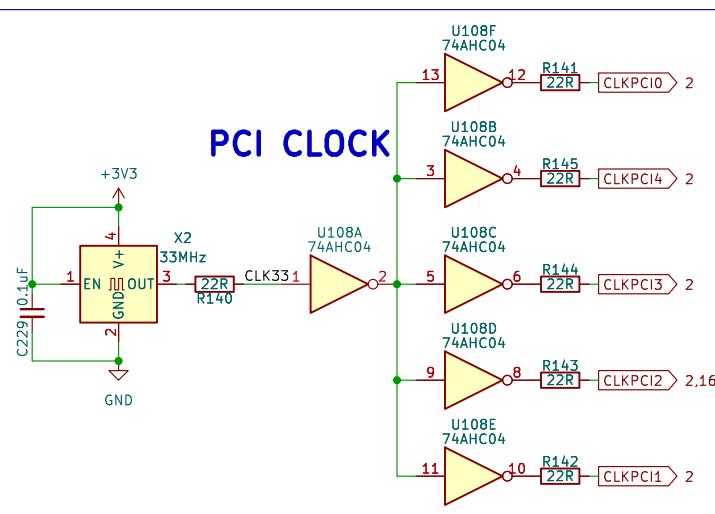
JUMPERS MUST BE CONFIGURED BY AGNUS PART NUMBER



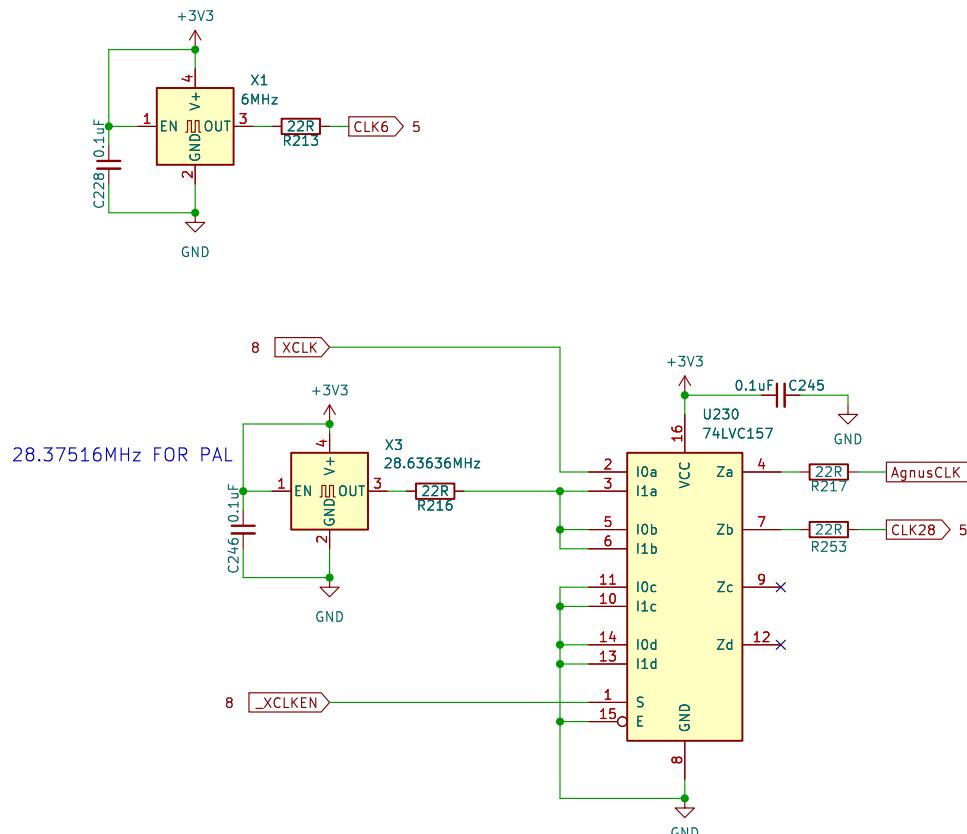
VIDEO CLOCK FANOUT



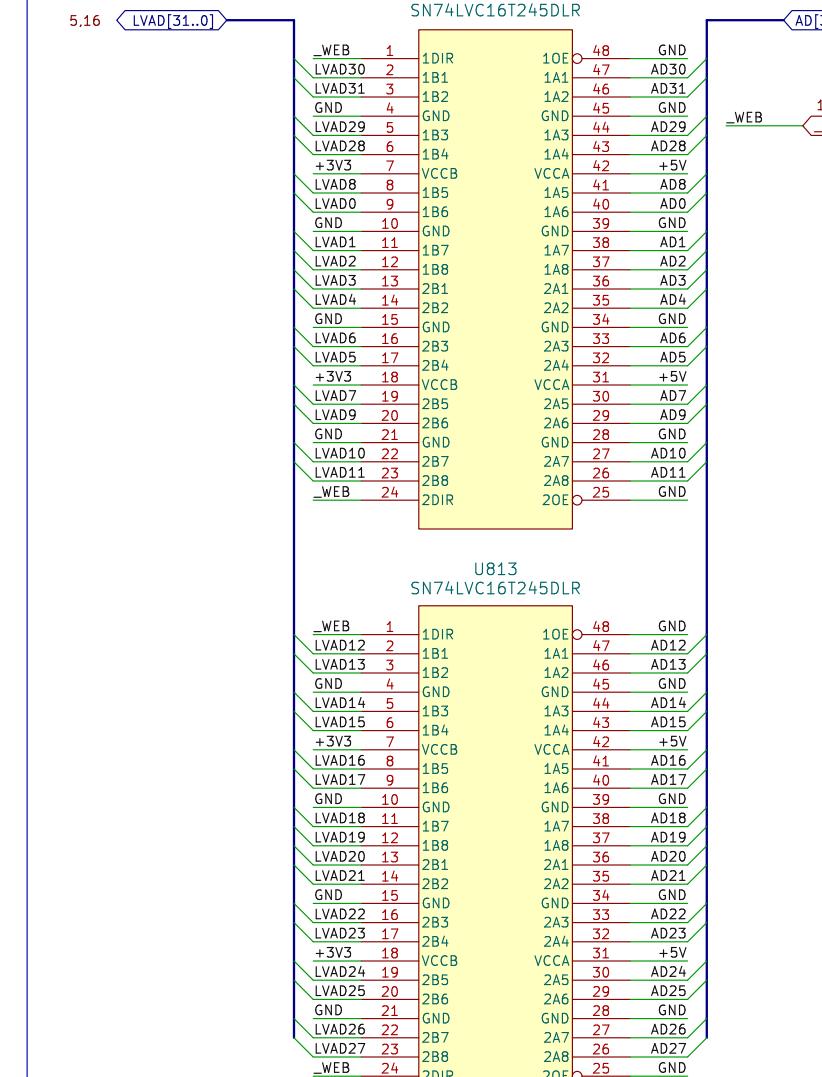
PCI CLOCK



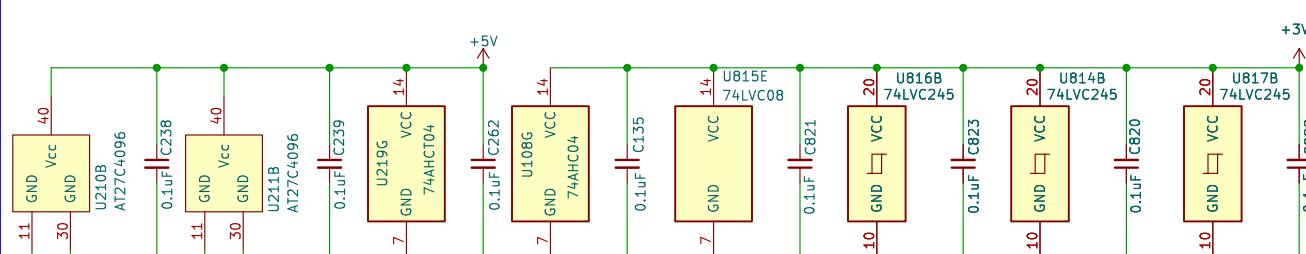
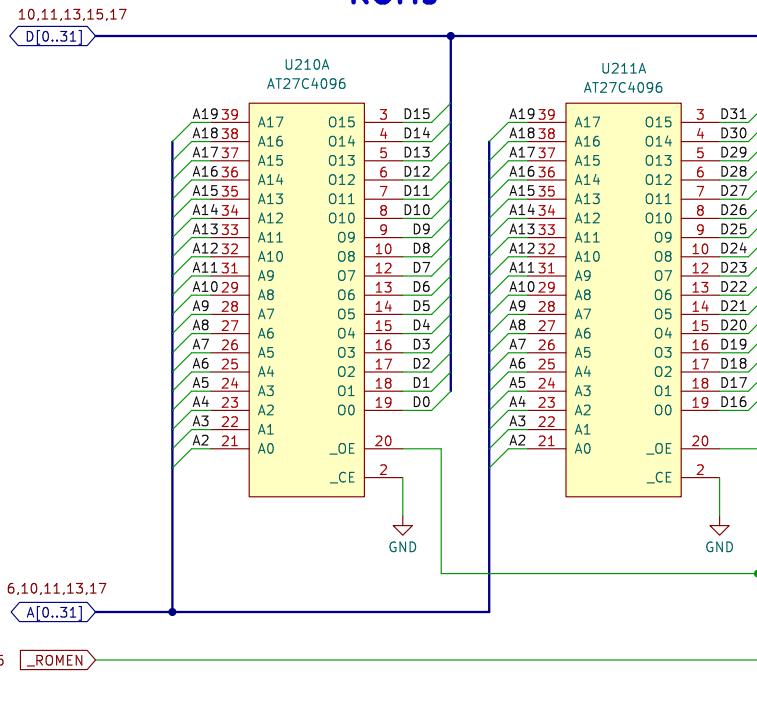
SYSTEM CLOCKS



AD BUS LEVEL SHIFT



ROMs



CLOCKS AND ROMS

Sheet: /Clocks and PCI Buffers/
 File: clocks.kicad_sch

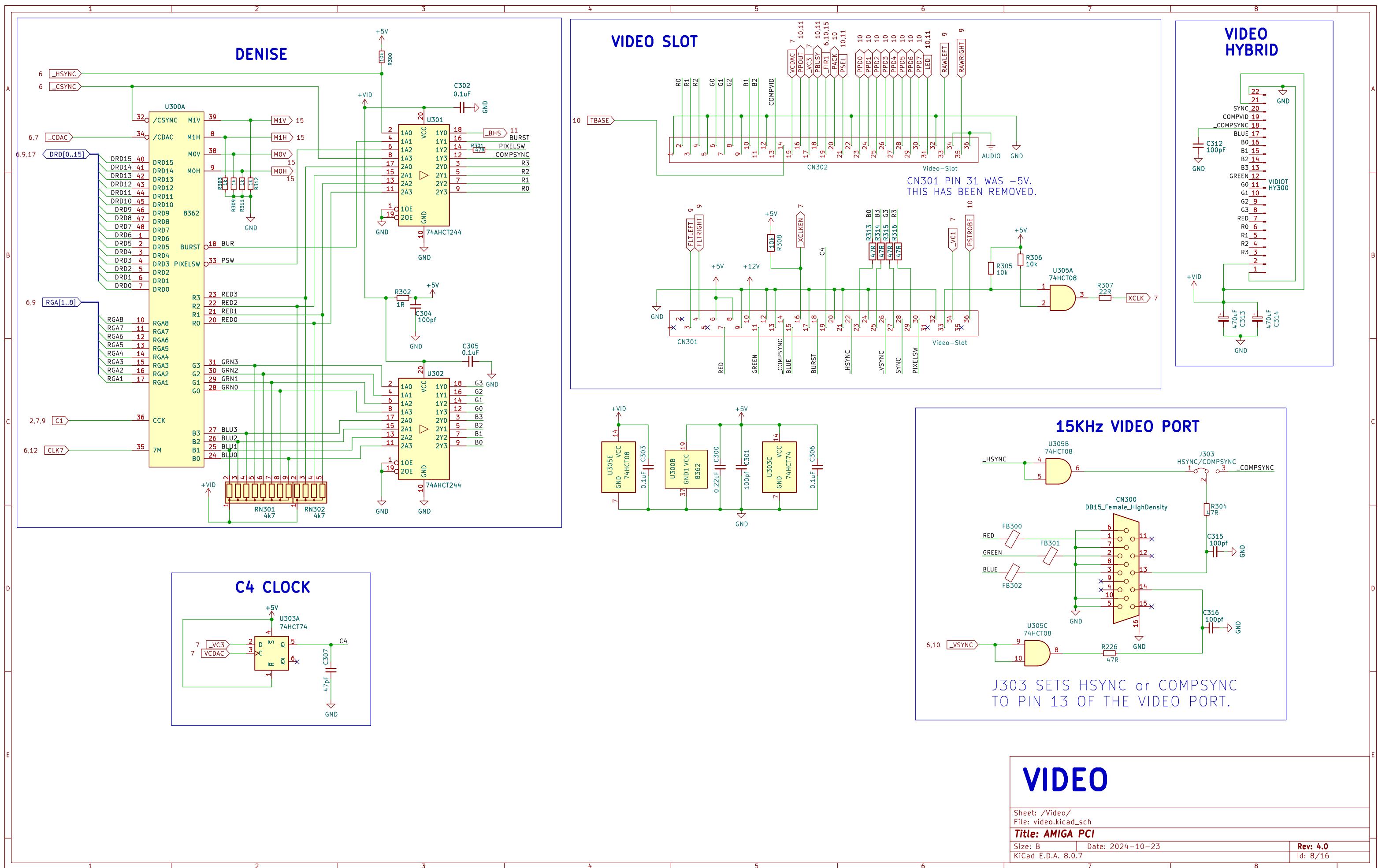
Title: AMIGA PCI

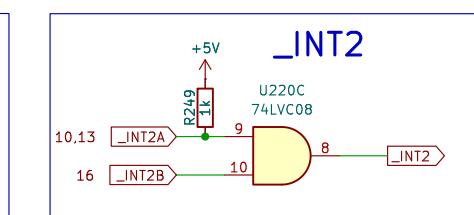
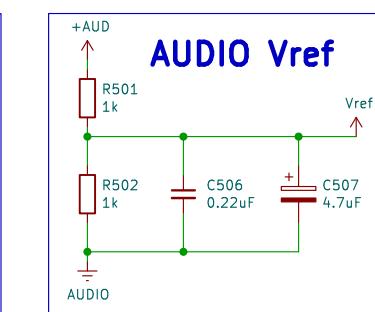
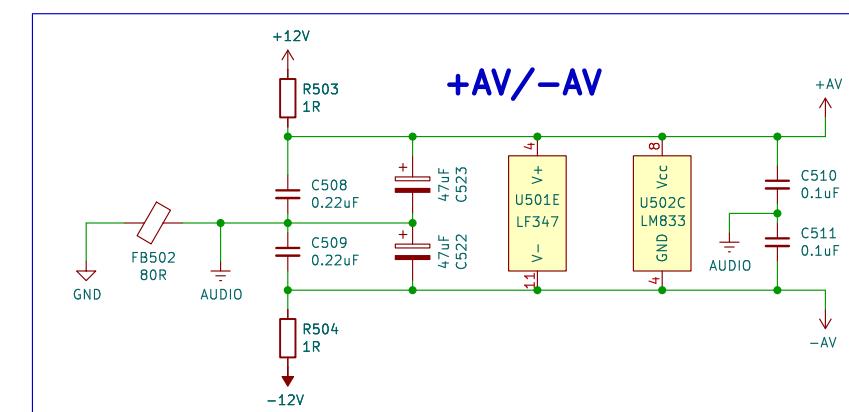
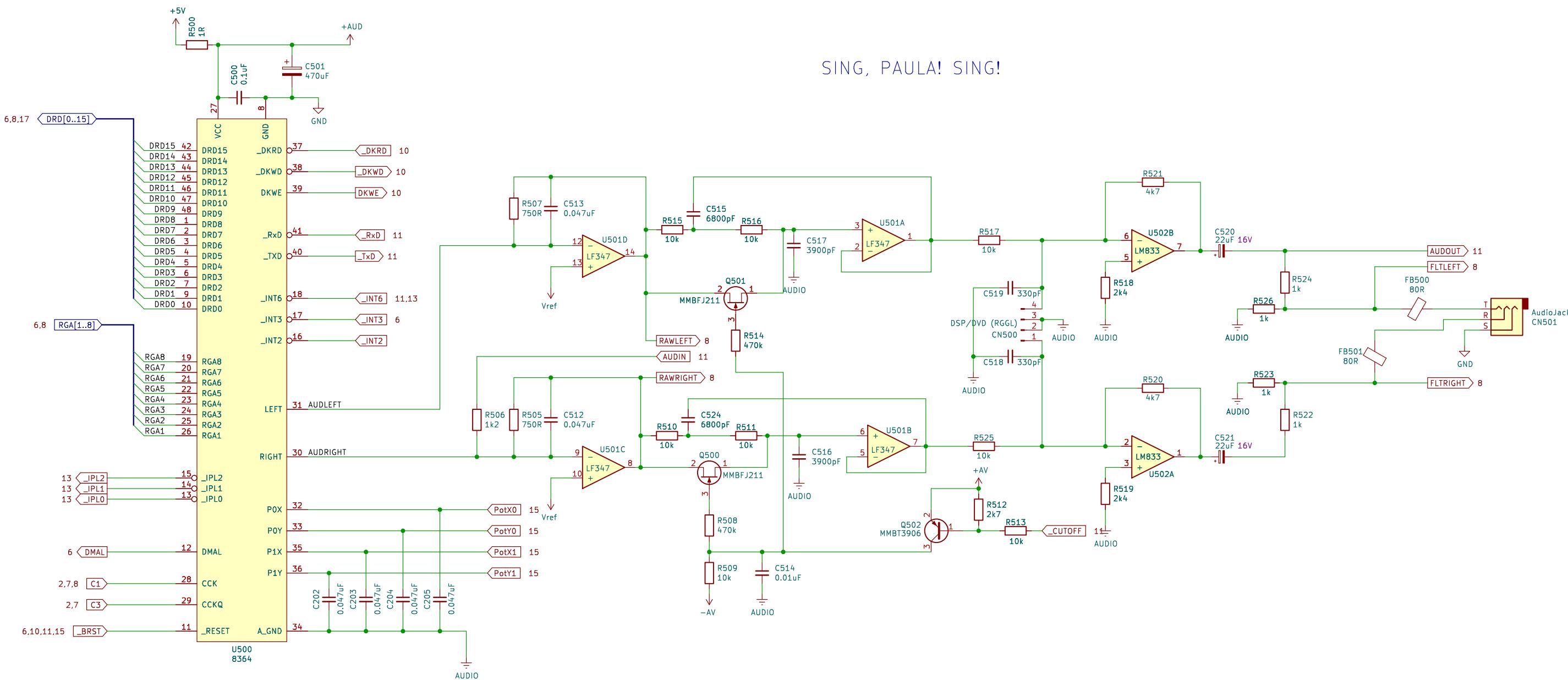
Size: B Date: 2024-10-23

KiCad E.D.A. 8.0.7

Rev: 4.0

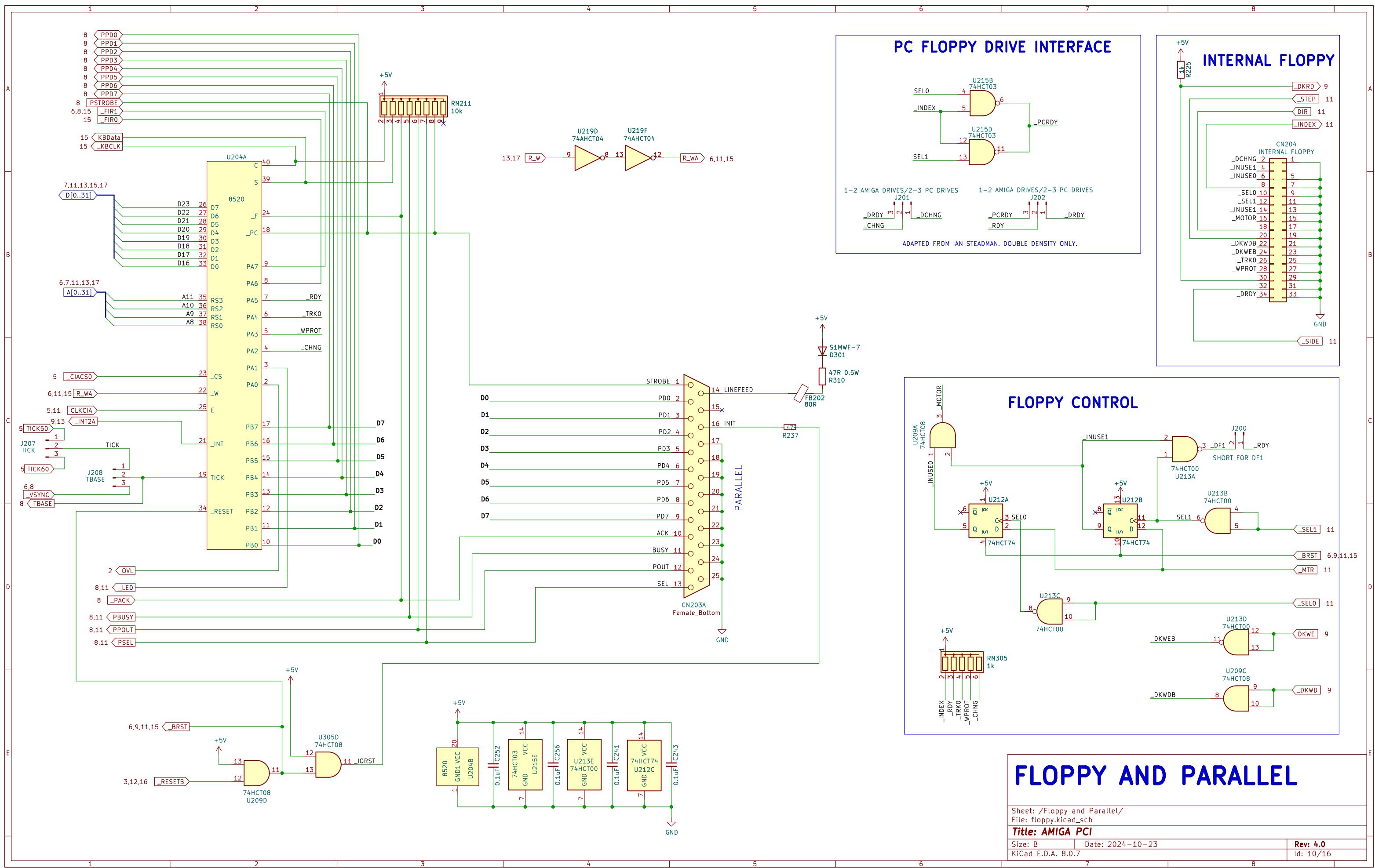
Id: 7/16

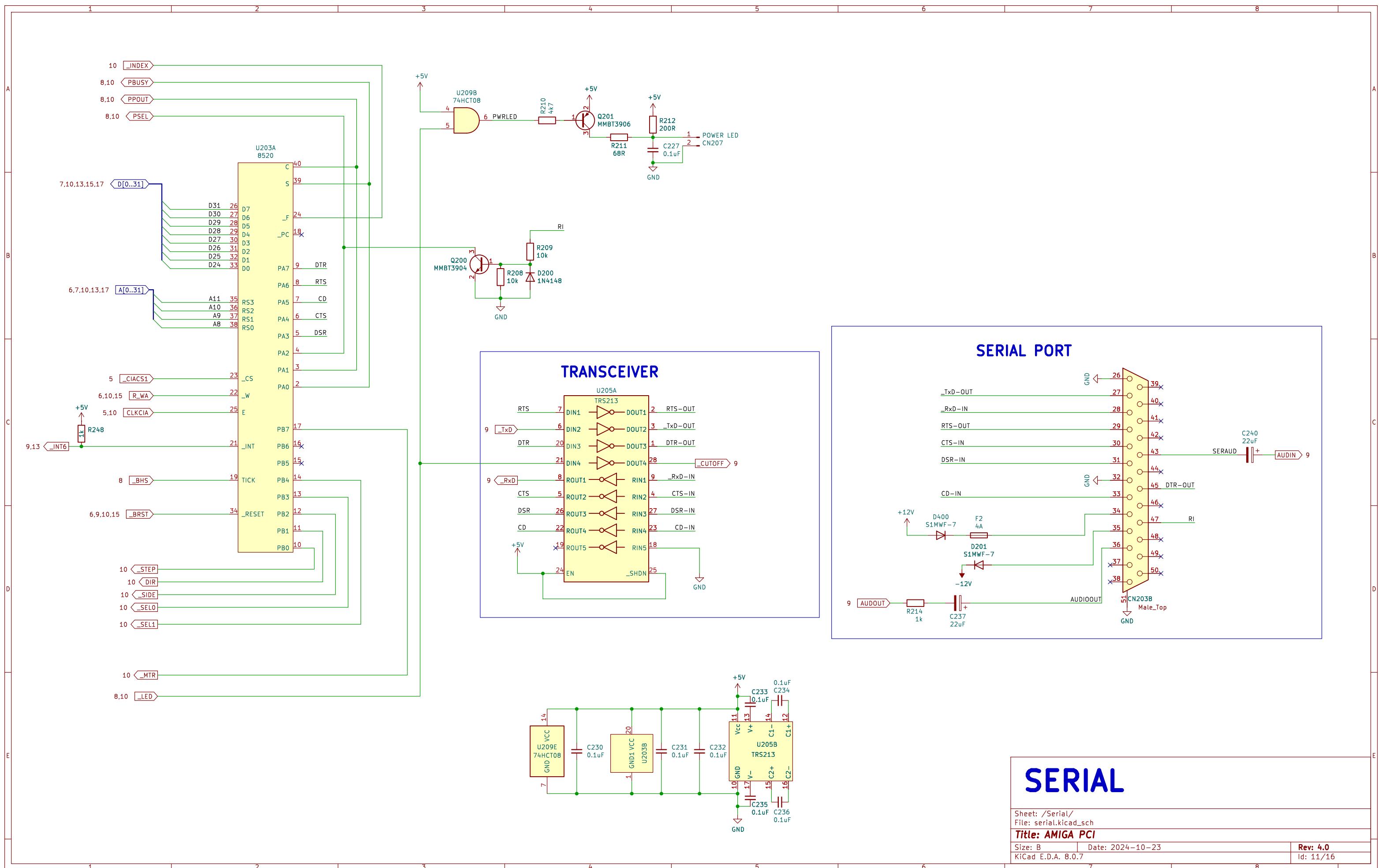




PAULA/AUDIO

Sheet: /Audio/
File: audio.kicad_sch
Title: AMIGA PCI
Size: B Date: 2024-10-23
KiCad E.D.A. 8.0.7 Id: 9/16

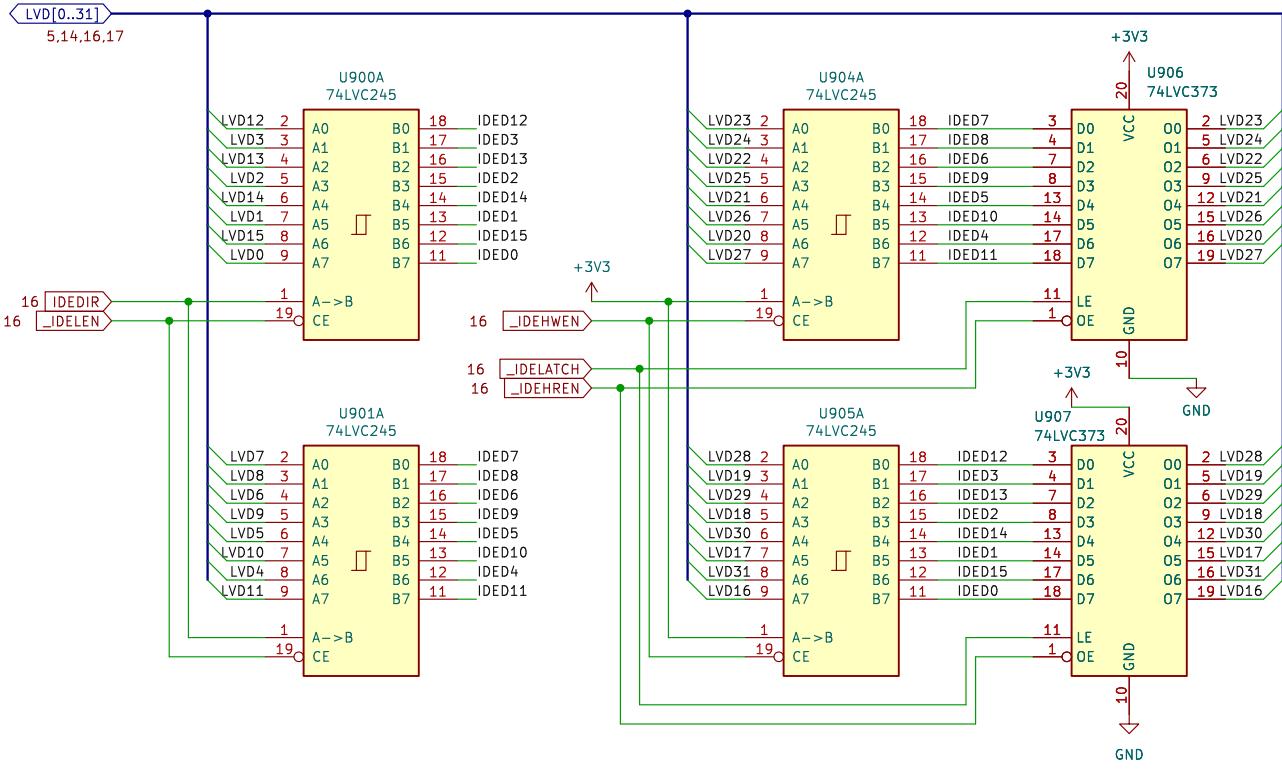




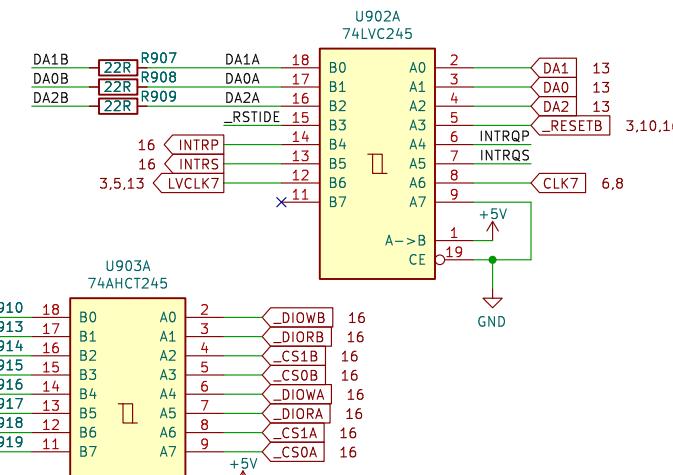
74LVC245 ENABLED FOR FIRST WORD DURING WRITES.
DATA ALWAYS FLOWS A->B.

74LVC373 LATCHES DATA FOR FIRST WORD DURING READS.

DATA BUFFERS



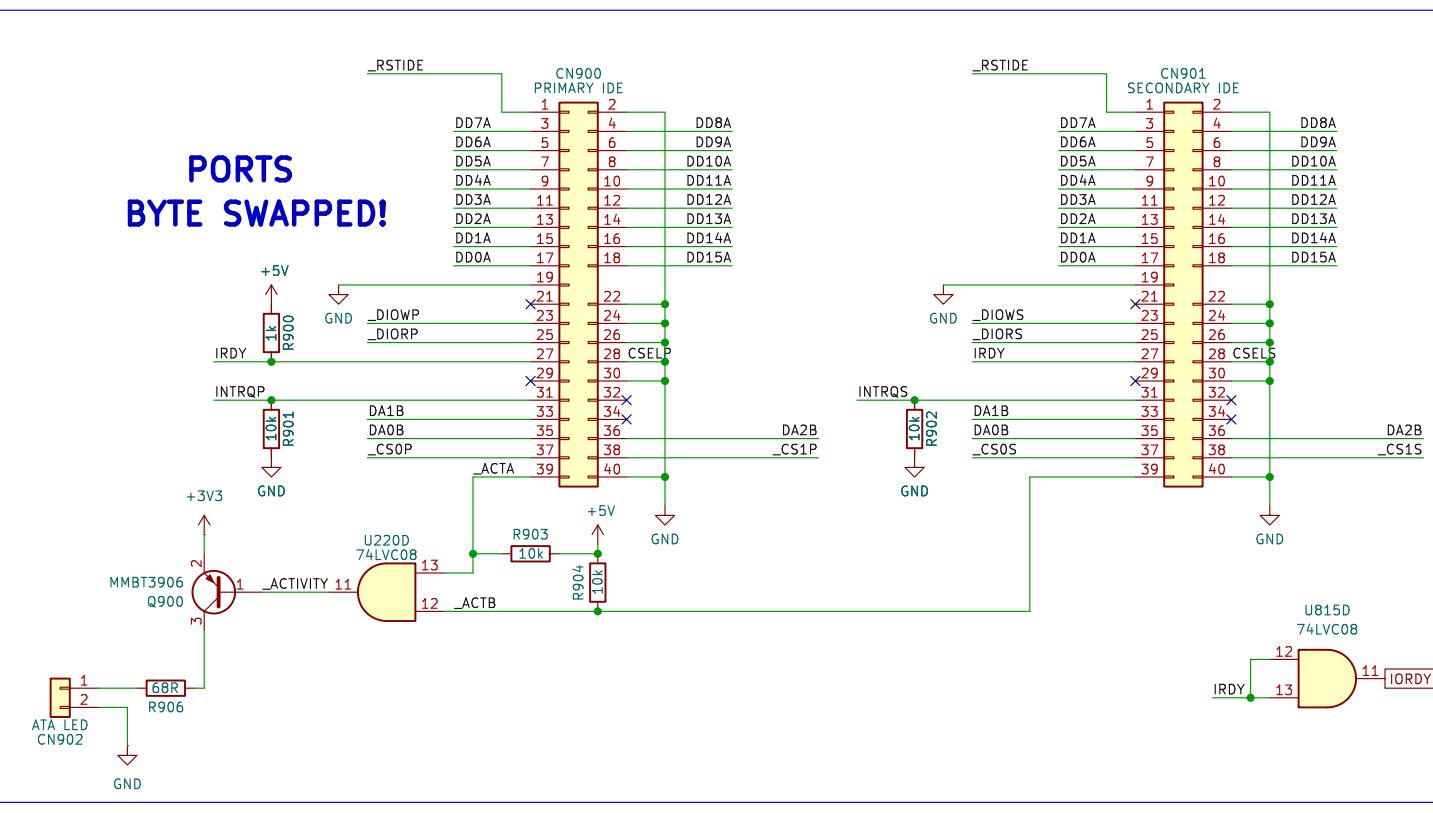
BUFFERS AND TERMINATION



DATA TERMINATION

DD8A	22R	R920	IDED0
DD7A	22R	R921	IDED15
DD9A	22R	R922	IDED1
DD6A	22R	R923	IDED14
DD10A	22R	R924	IDED2
DD5A	22R	R925	IDED13
DD11A	22R	R926	IDED3
DD4A	22R	R927	IDED12
DD3A	22R	R928	IDED11
DD12A	22R	R929	IDED4
DD2A	22R	R931	IDED10
DD13A	22R	R932	IDED5
DD1A	22R	R933	IDED9
DD14A	22R	R934	IDED6
DD0A	22R	R935	IDED8
DD15A	22R	R936	IDED7

PORTS BYTE SWAPPED!



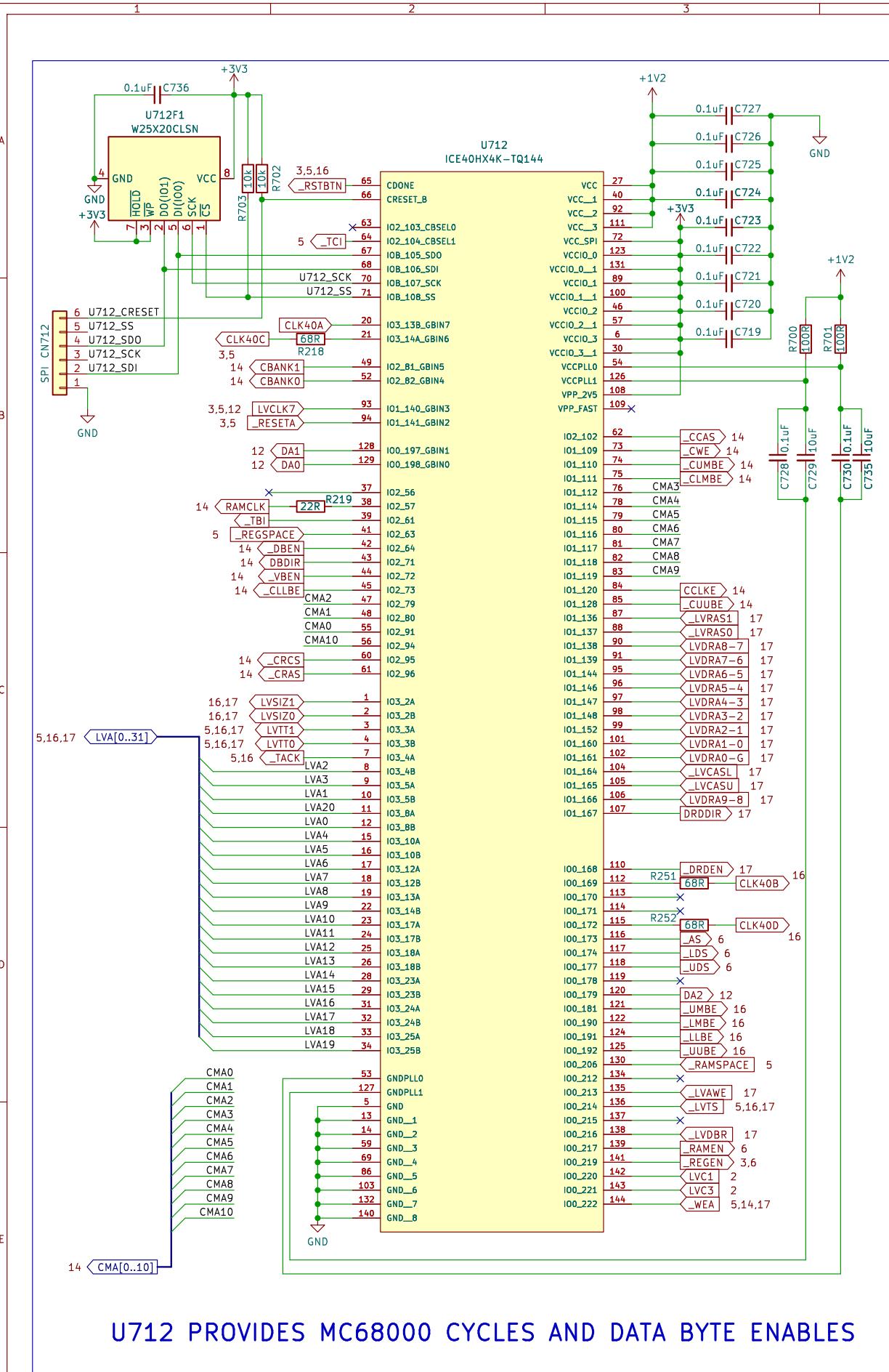
IDE INTERFACE

Sheet: /IDE Interface/
File: IDE.kicad_sch

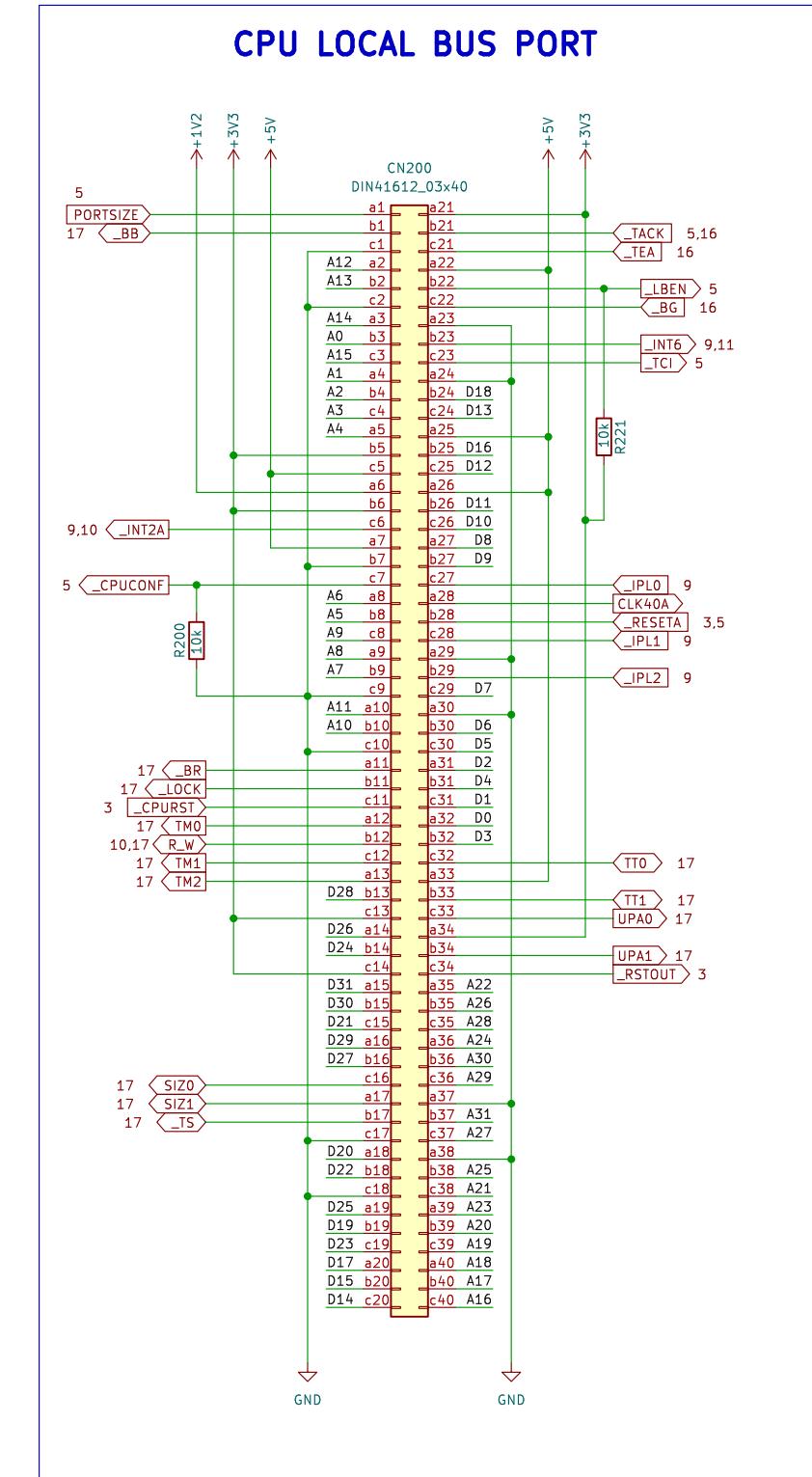
Title: AMIGA PCI

Size: B Date: 2024-10-23
KiCad E.D.A. 8.0.7

Rev: 4.0
Id: 12/16



U712 PROVIDES MC68000 CYCLES AND DATA BYTE ENABLER



CHIP RAM CONTROLLER

Sheet: /Chip RAM Controller/
File: cpu.kicad_sch

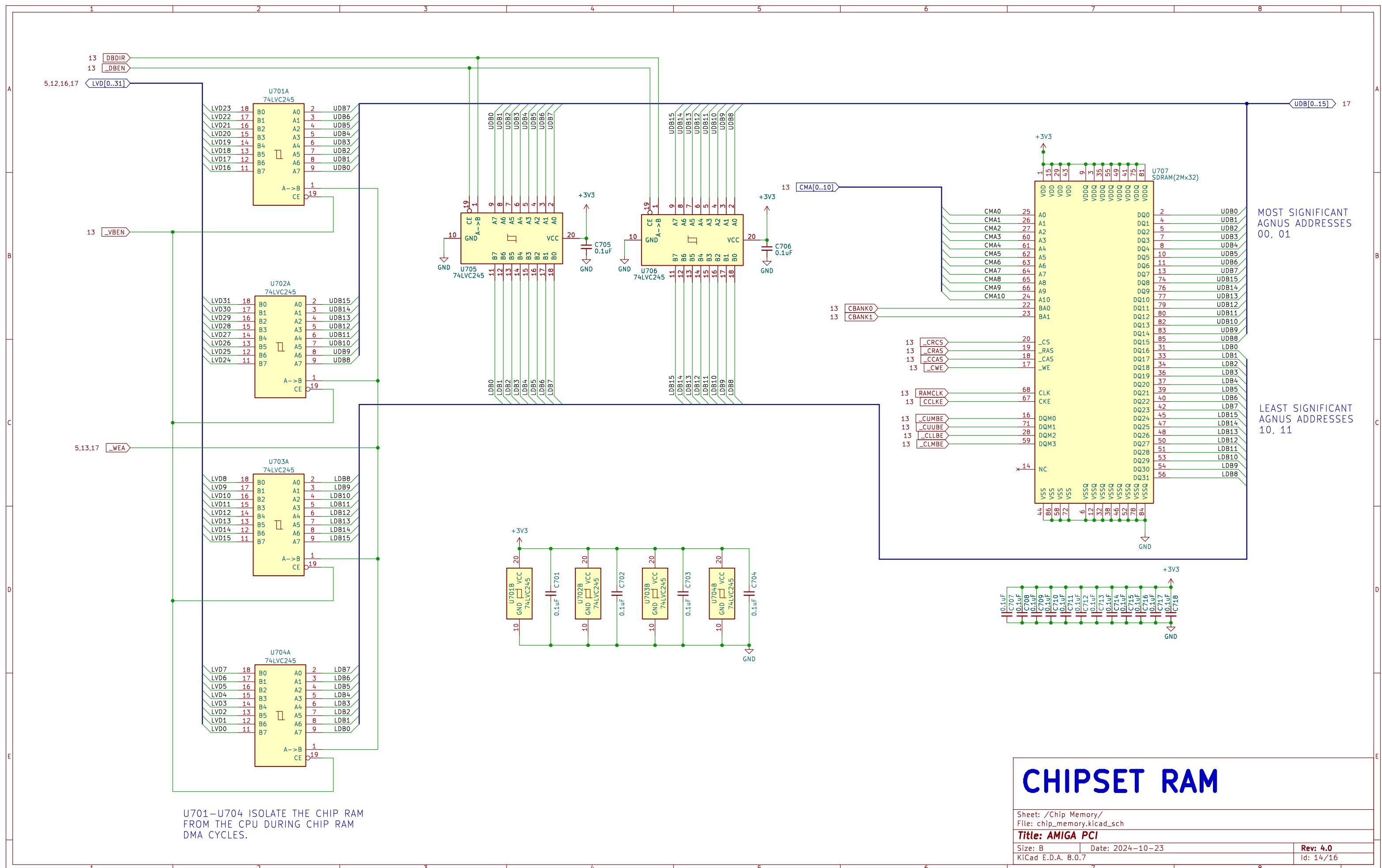
Title: AMIGA PCI

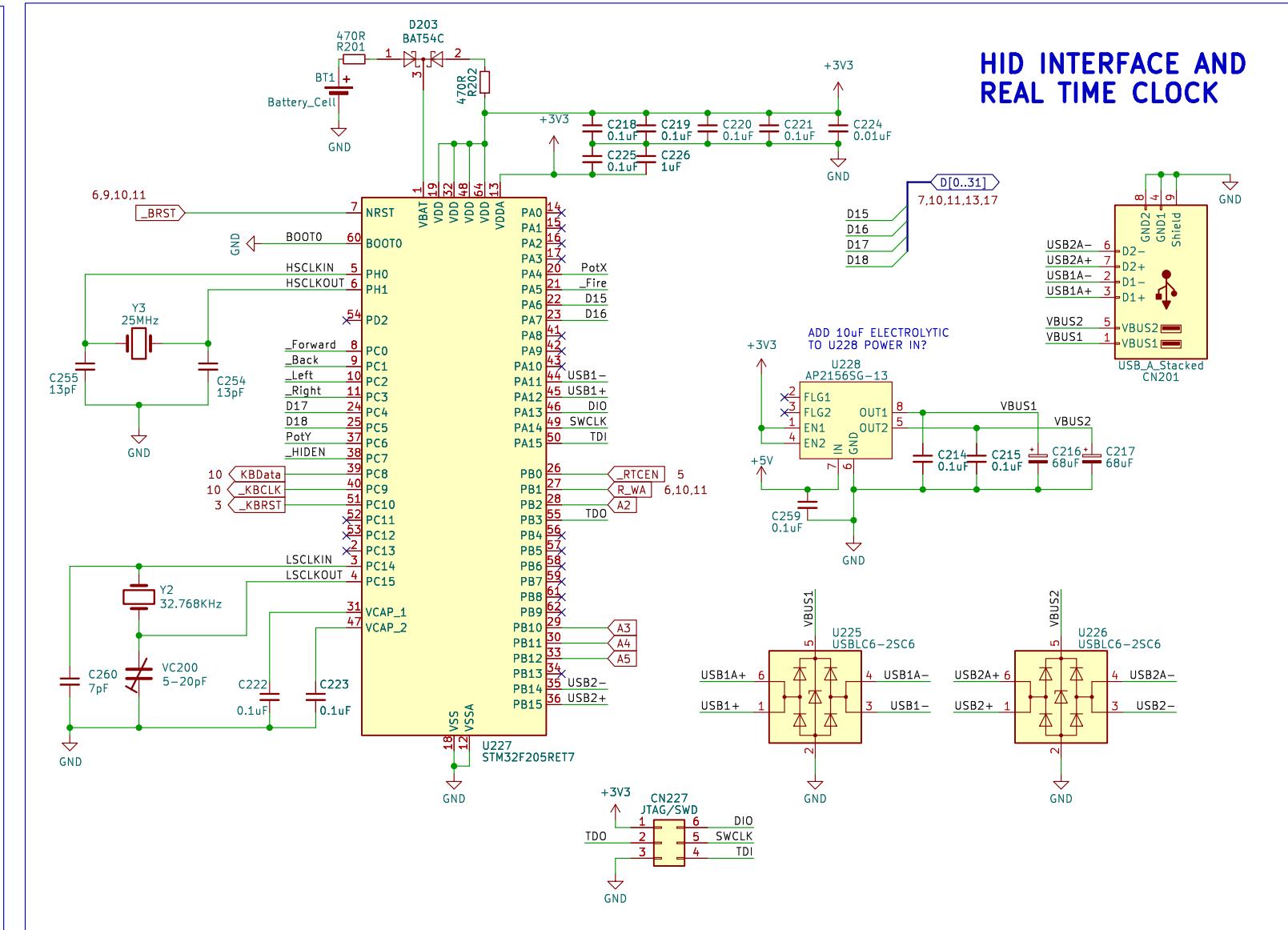
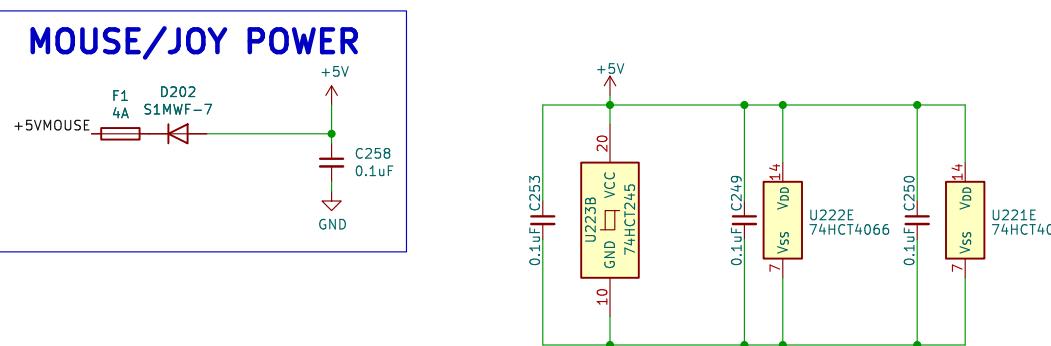
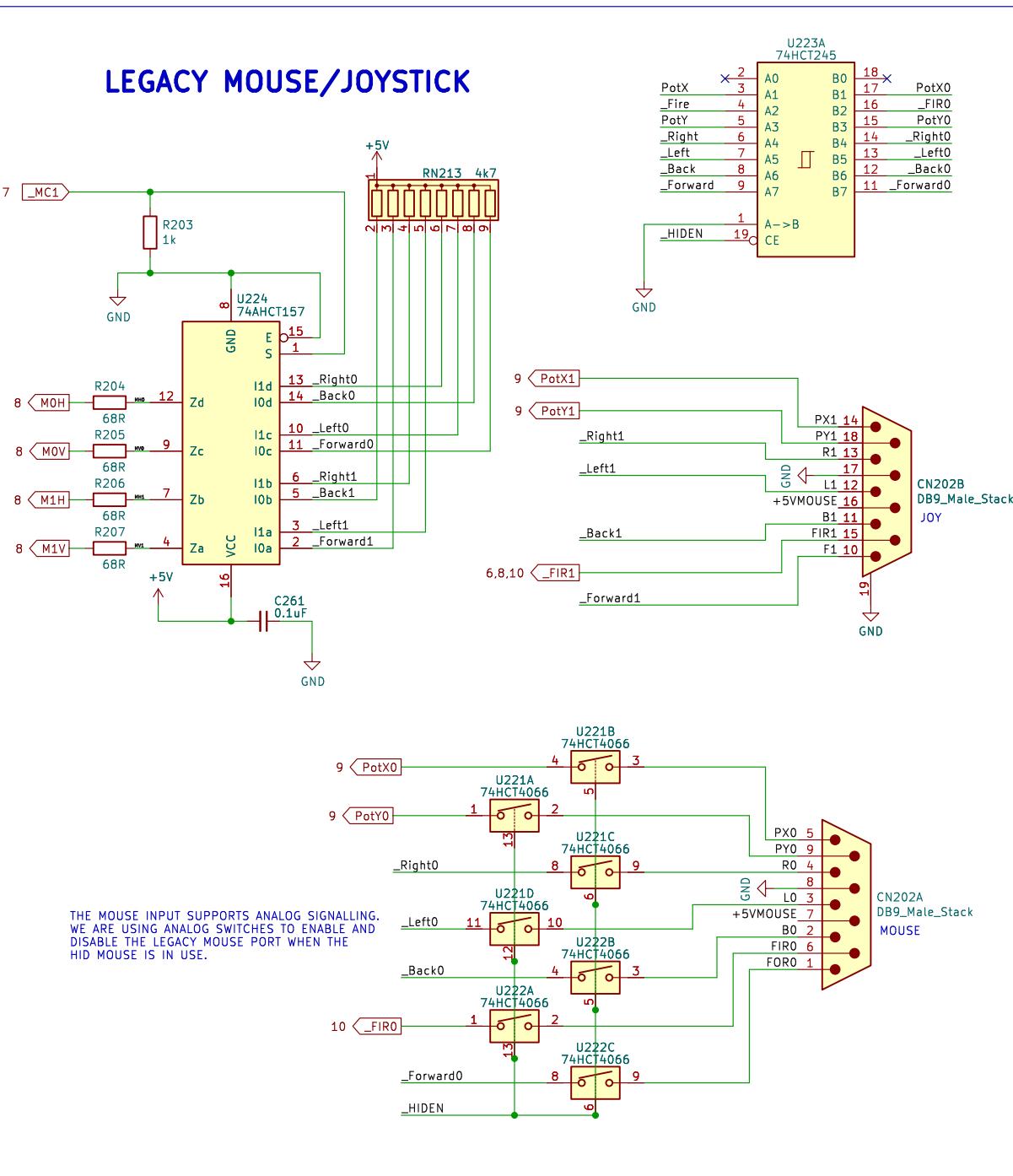
Size: B Date: 2024-10-23

KiCad E.D.A. 8.0.7

Rev: 4.0

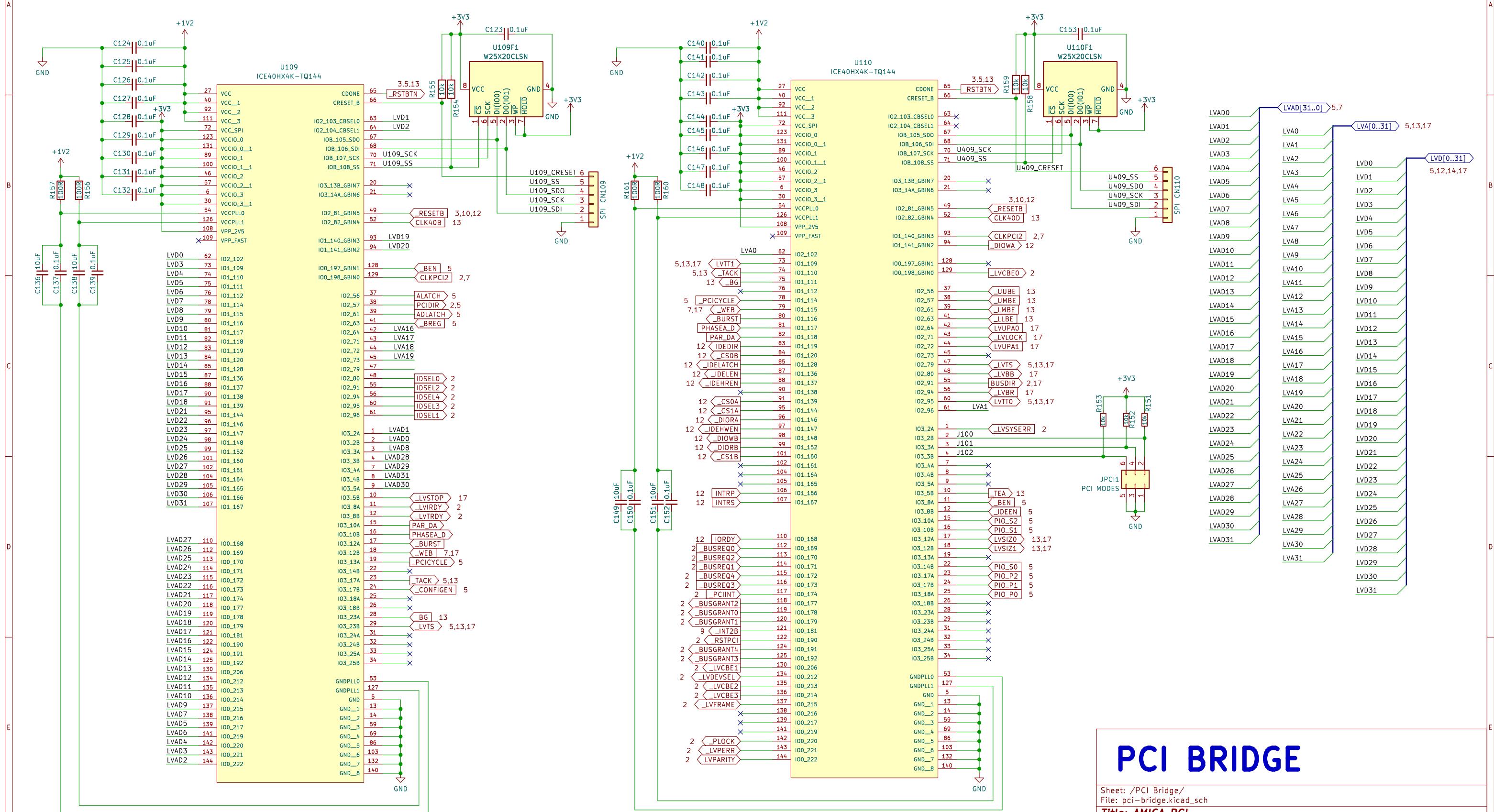
Id: 13/16





U109: BUFFERS AD/D/A BUSSES, AD PARITY CALC, BRIDGE REGISTERS

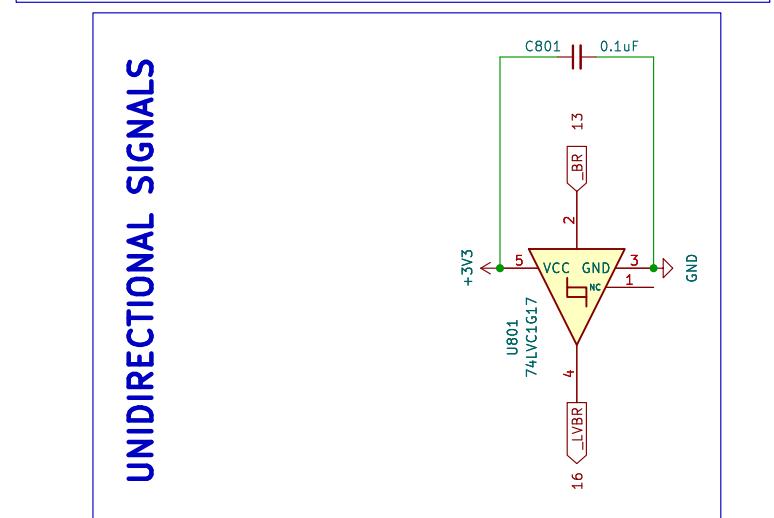
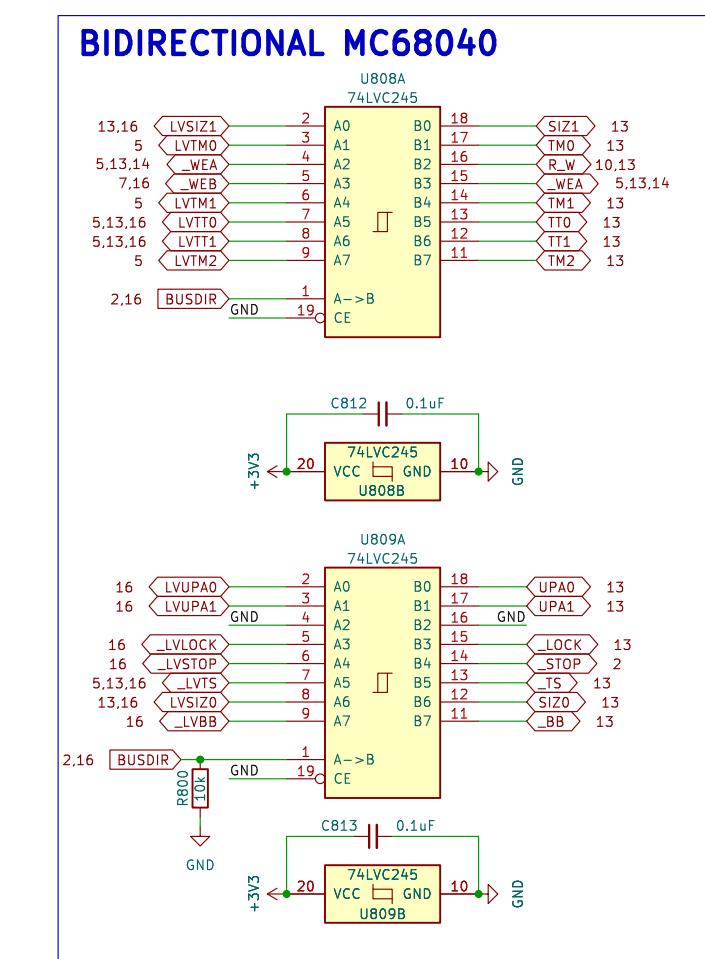
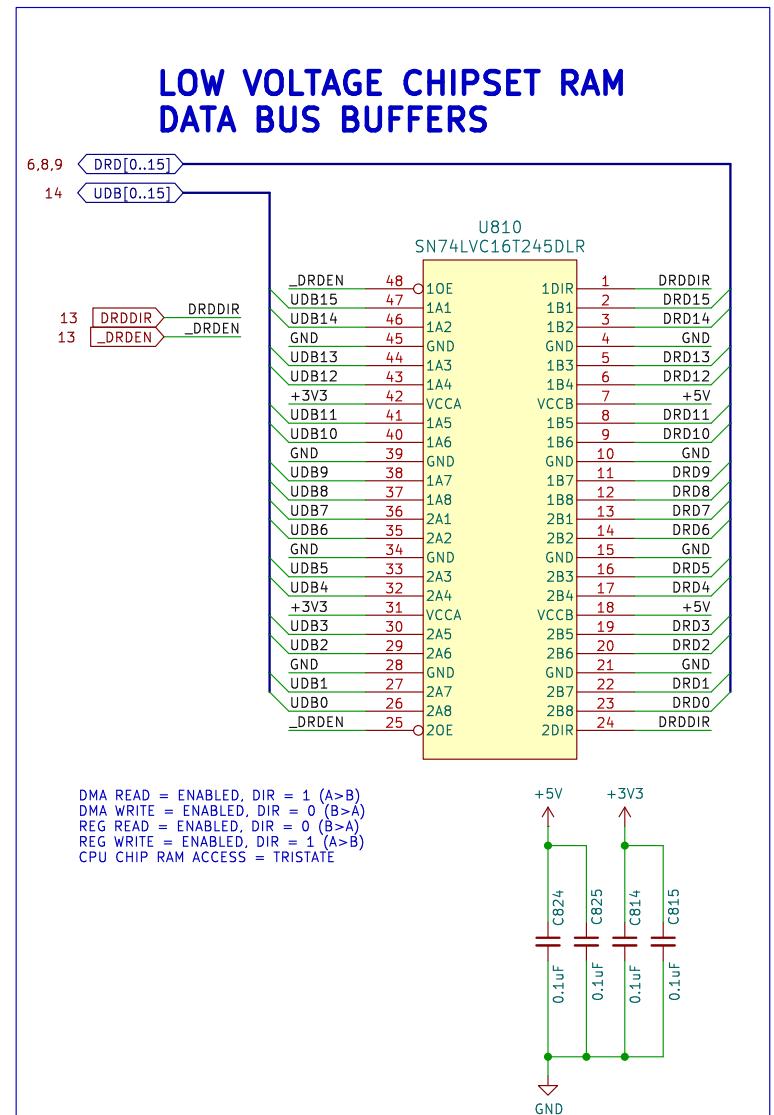
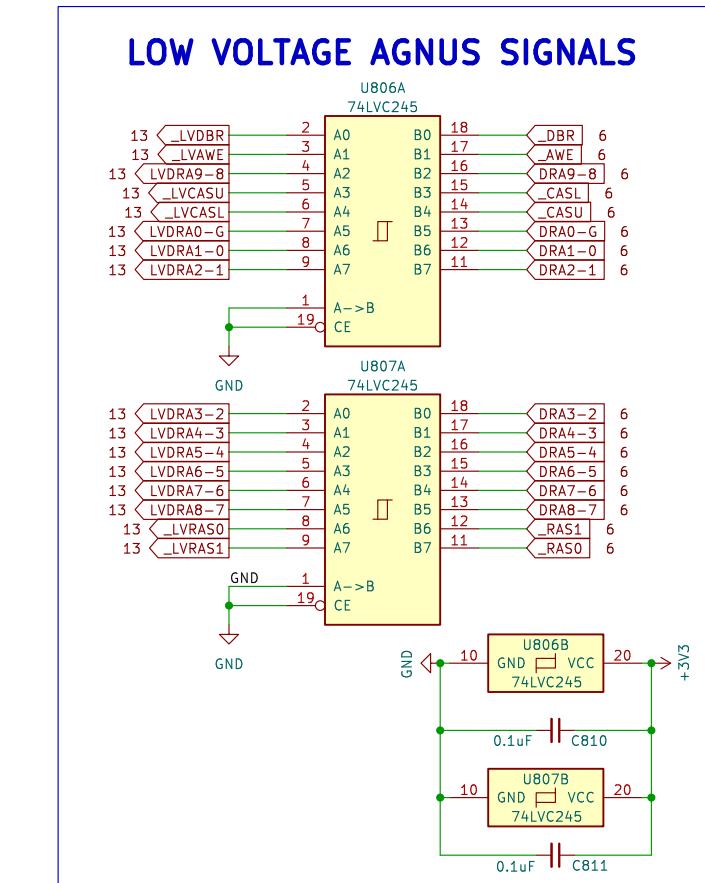
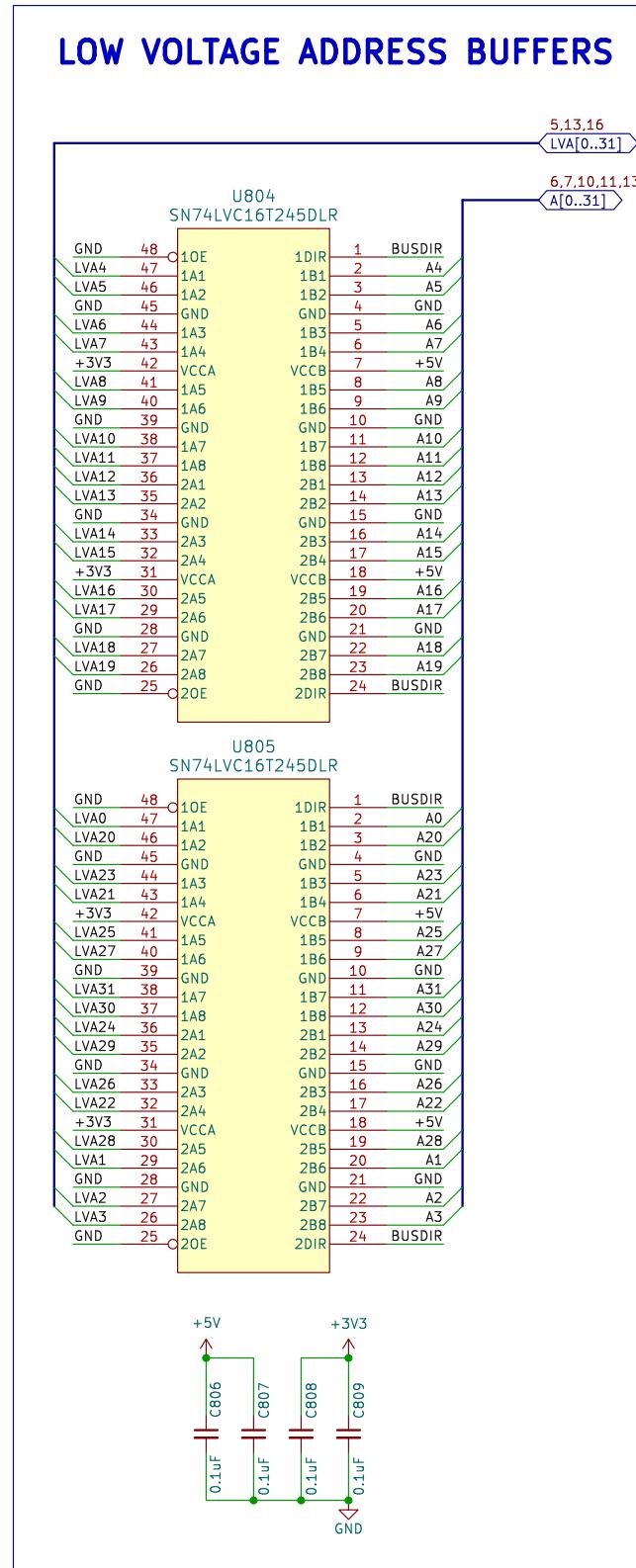
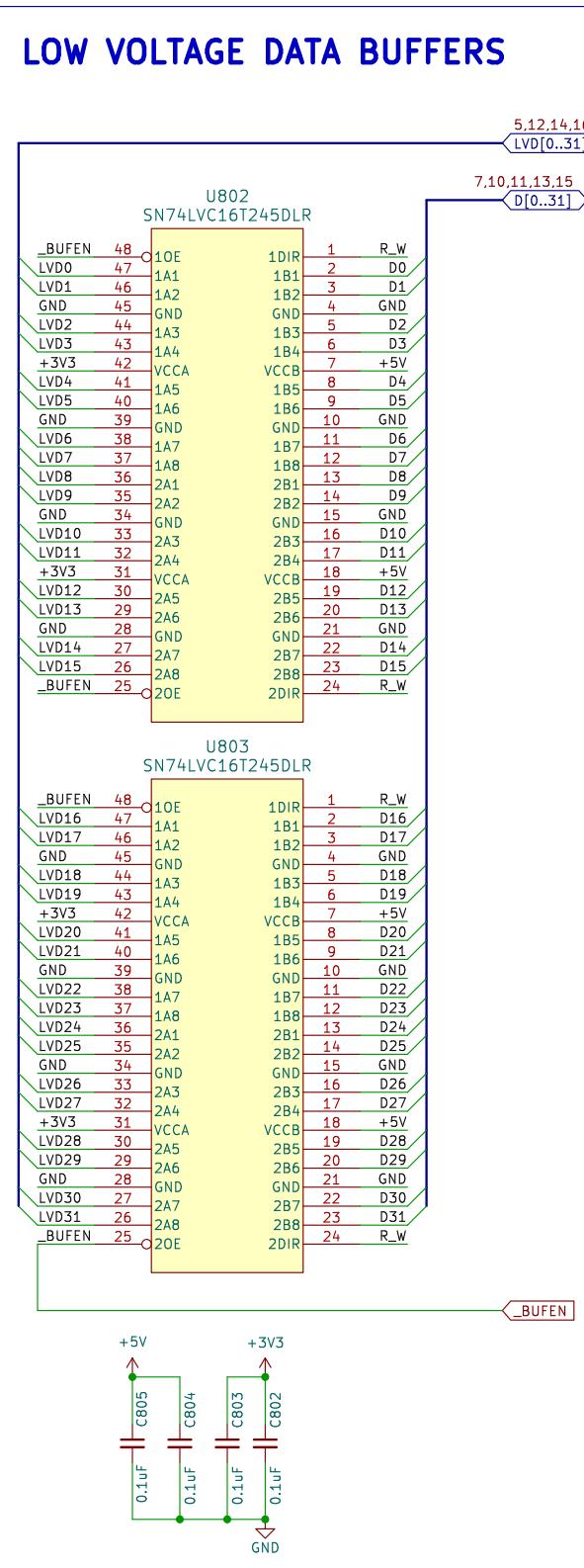
U110: BUS ARBITRATOR, INTERRUPT HANDLING, PARITY ERROR DETECTION, ATA CONTROLLER



PCI BRIDGE

Sheet: /PCI Bridge/
File: pci-bridge.kicad_sch

Title: AMIGA PCI



MC68040 LEVEL SHIFT

Sheet: /MC68040_Level_Shift/
File: mc68040_level_shift.kicad_sch

Title: AMIGA PCI

Size: B

Date: 2024-10-23

KiCad E.D.A. 8.0.7 | Id: 17/16