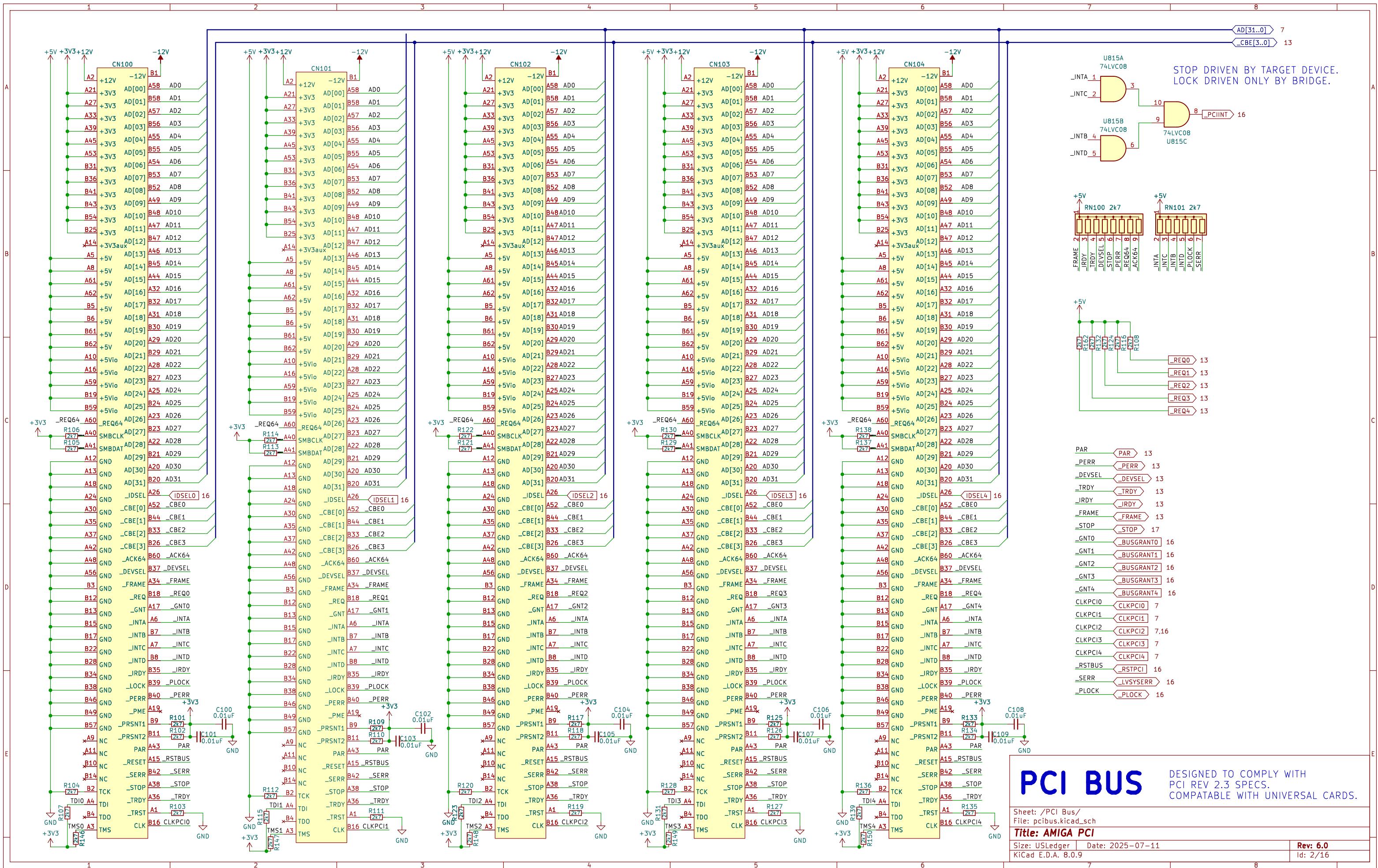
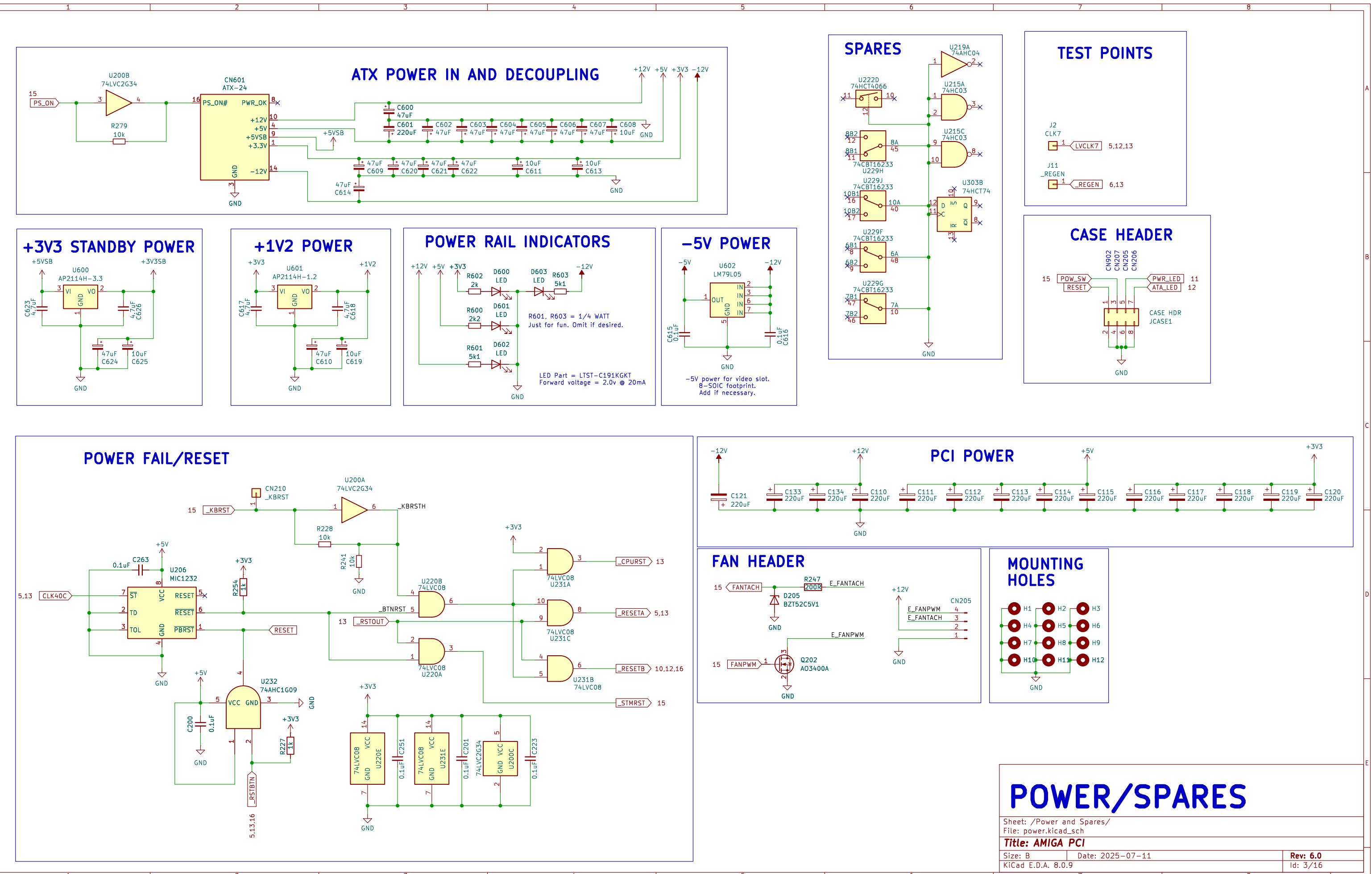
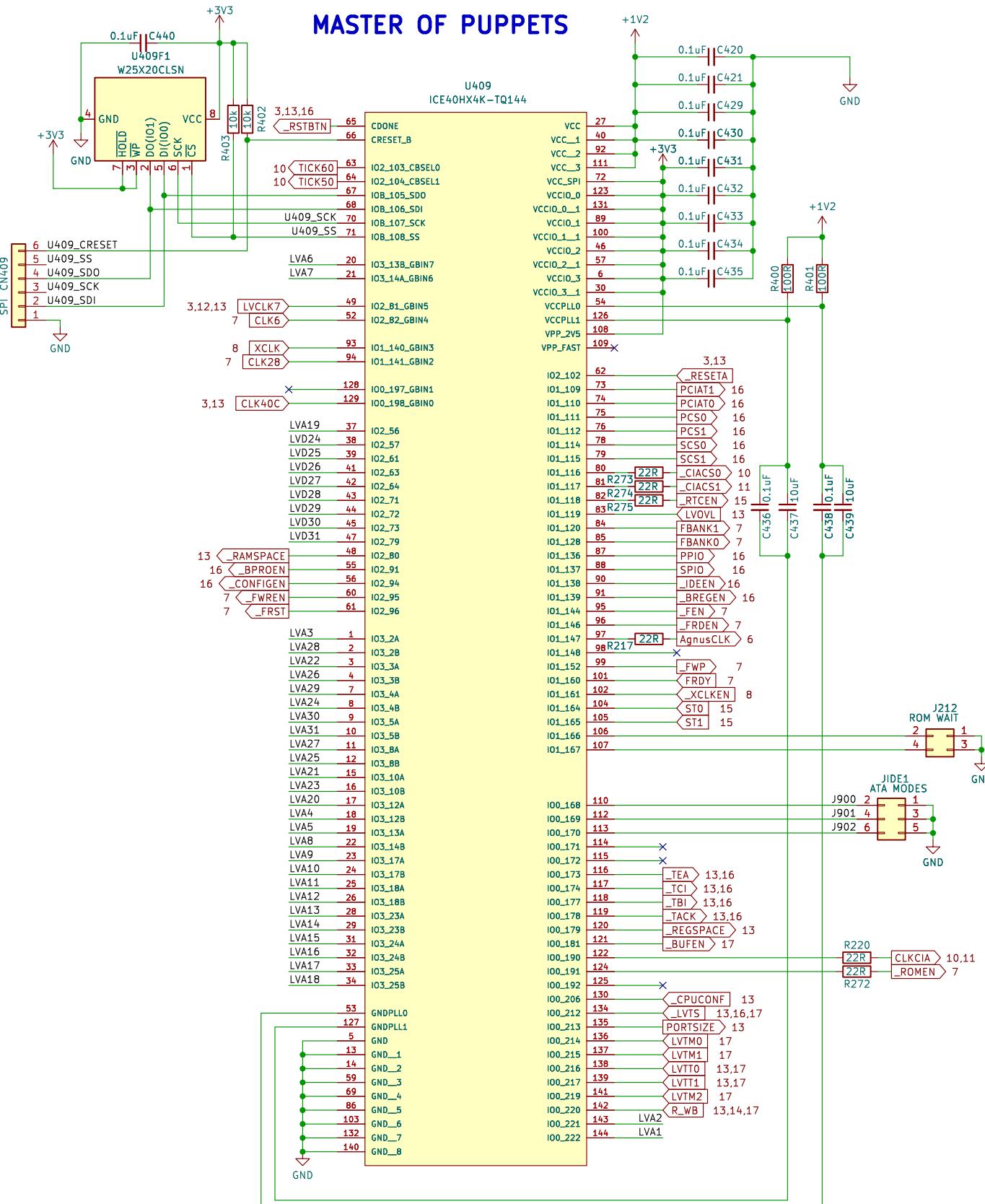


100 = PCI BUS
200 = CHIPSET
300 = VIDEO
500 = AUDIO
600 = POWER
700 = CHIPSET RAM
800 = VOLTAGE SHIFTER
900 = ATA

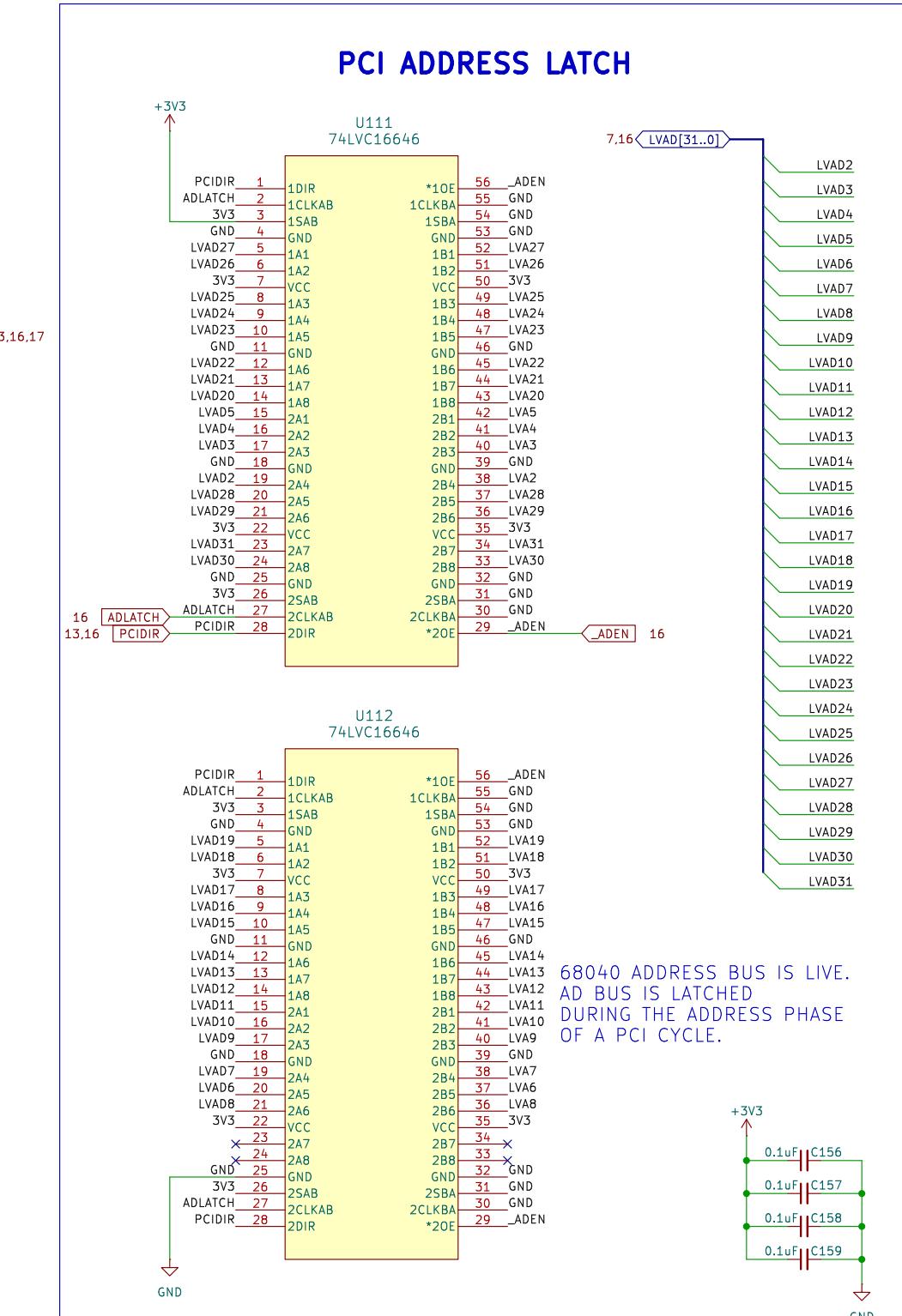
BUDDY







U409 PROVIDES ONBOARD DEVICE AUTOCONFIG, ADDRESS DECODE, AND REPLACEMENT GARY FUNCTIONS



MASTER CONTROL

Sheet: /Master Control/
File: bcontroller.kicad_sch

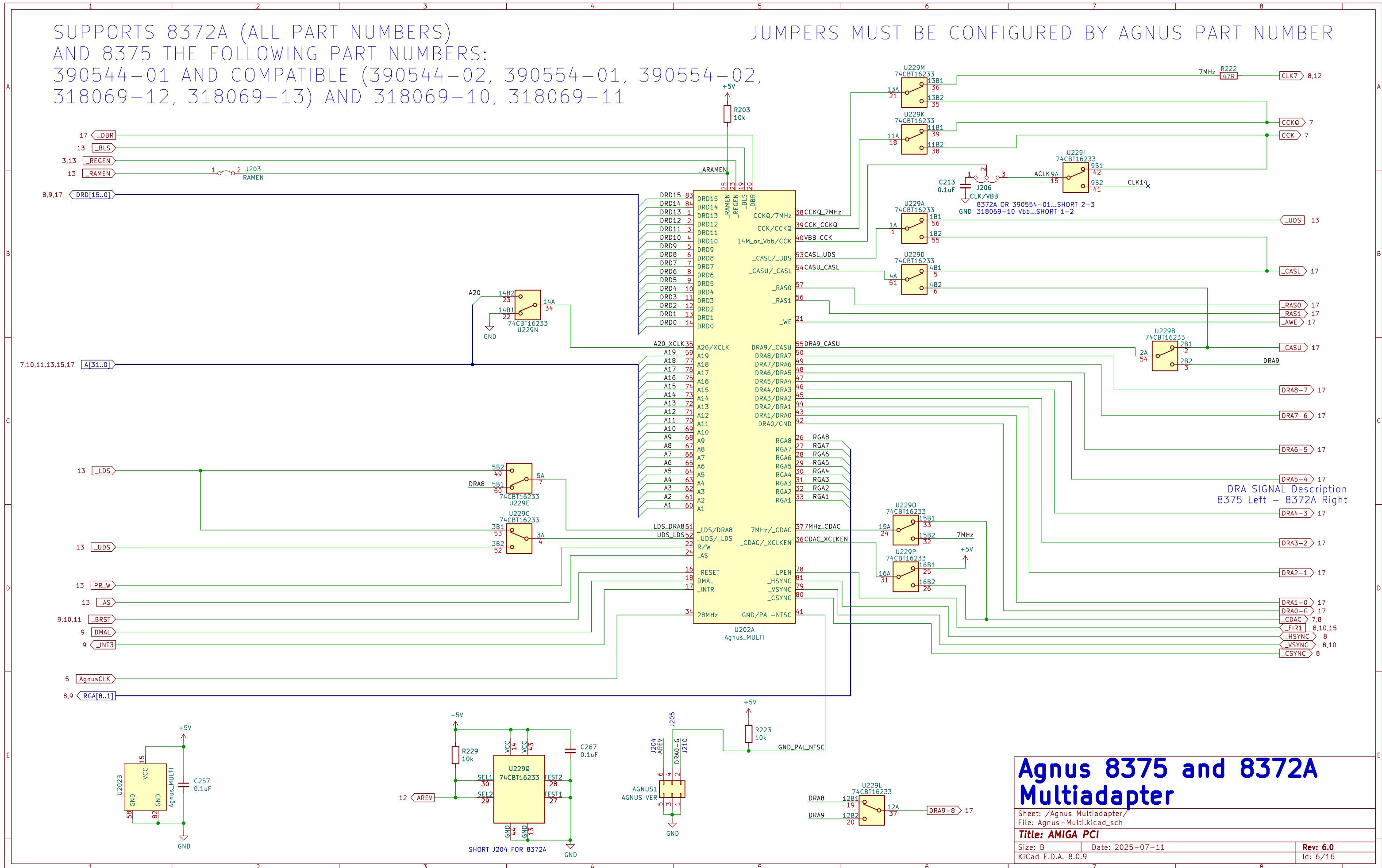
Title: AMIGA PCI

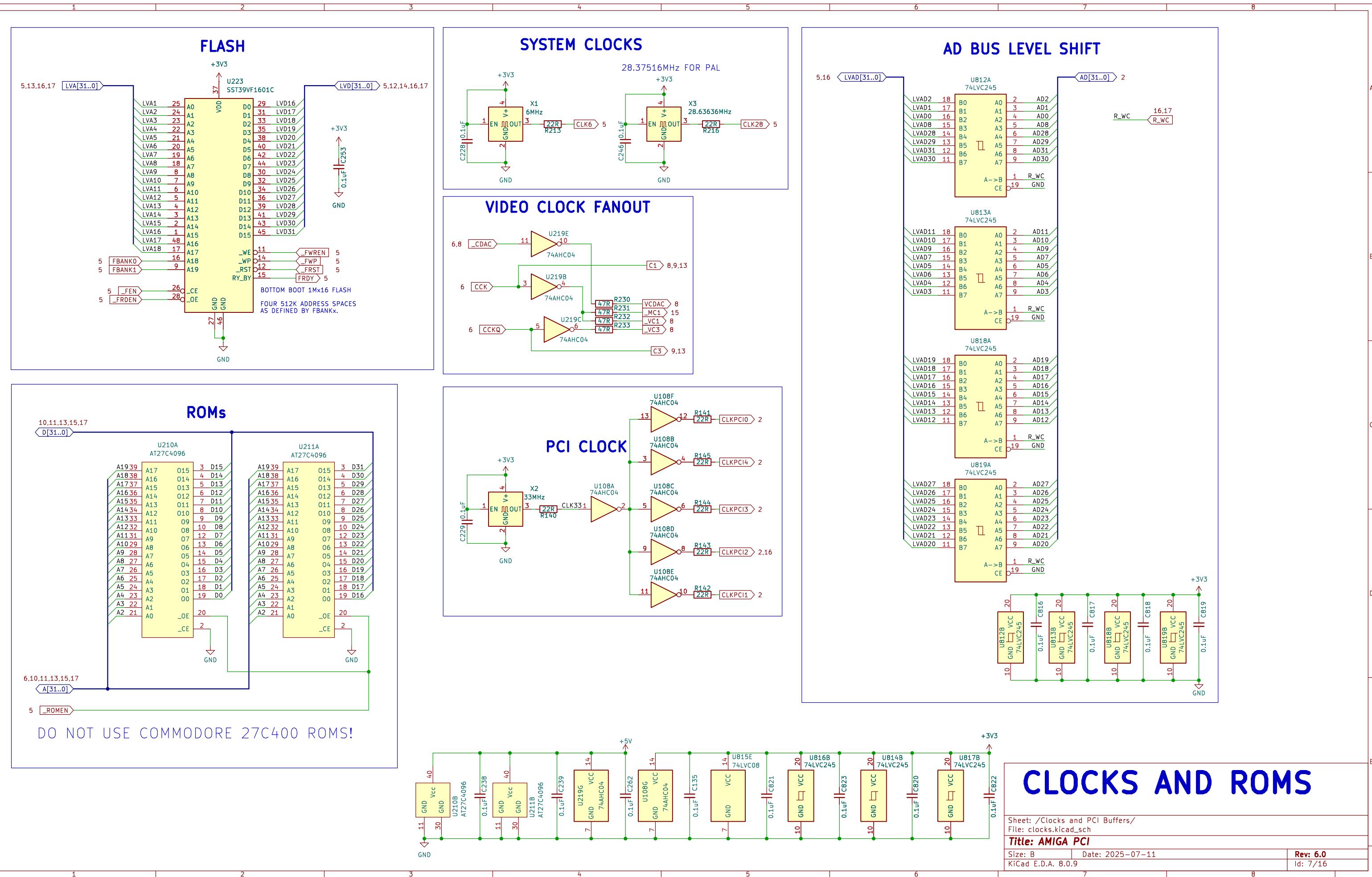
Size: B Date: 2025-07-11
KiCad E.D.A. 8.0.9

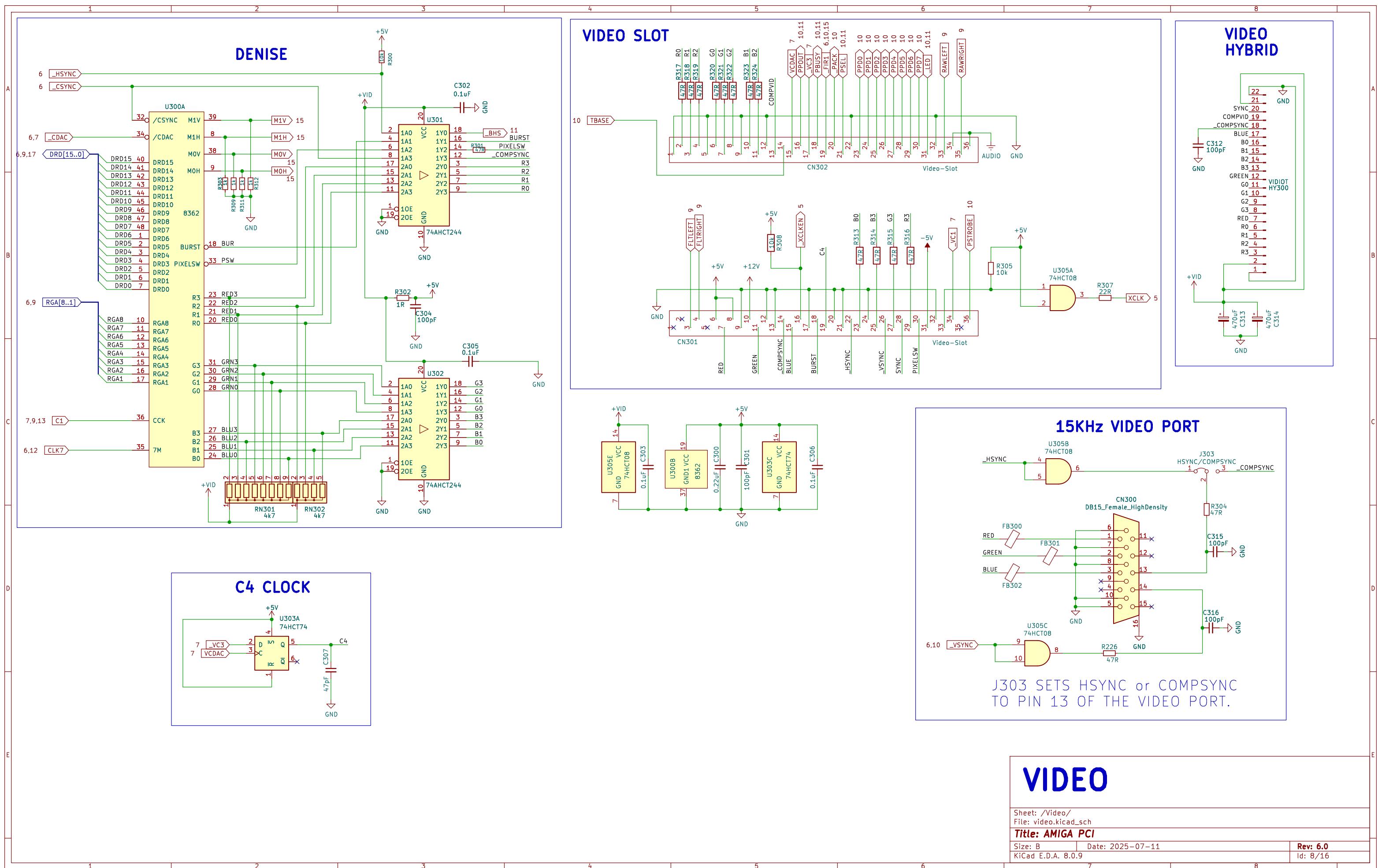
Rev: 6.0
Id: 5/16

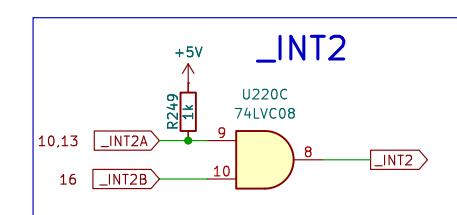
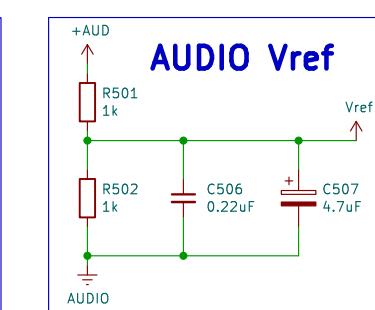
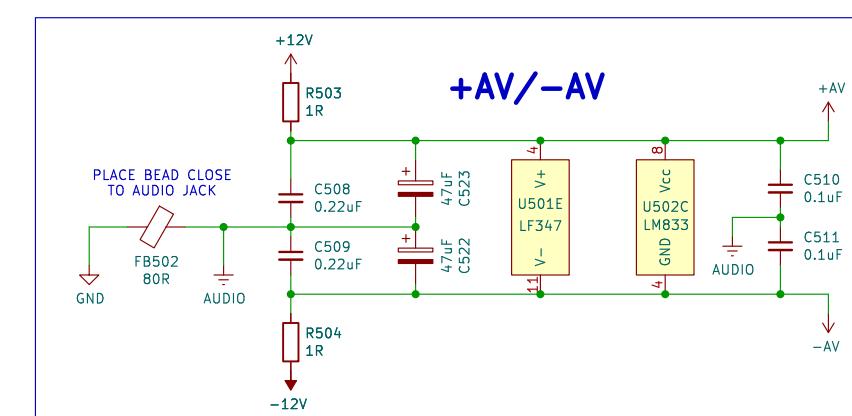
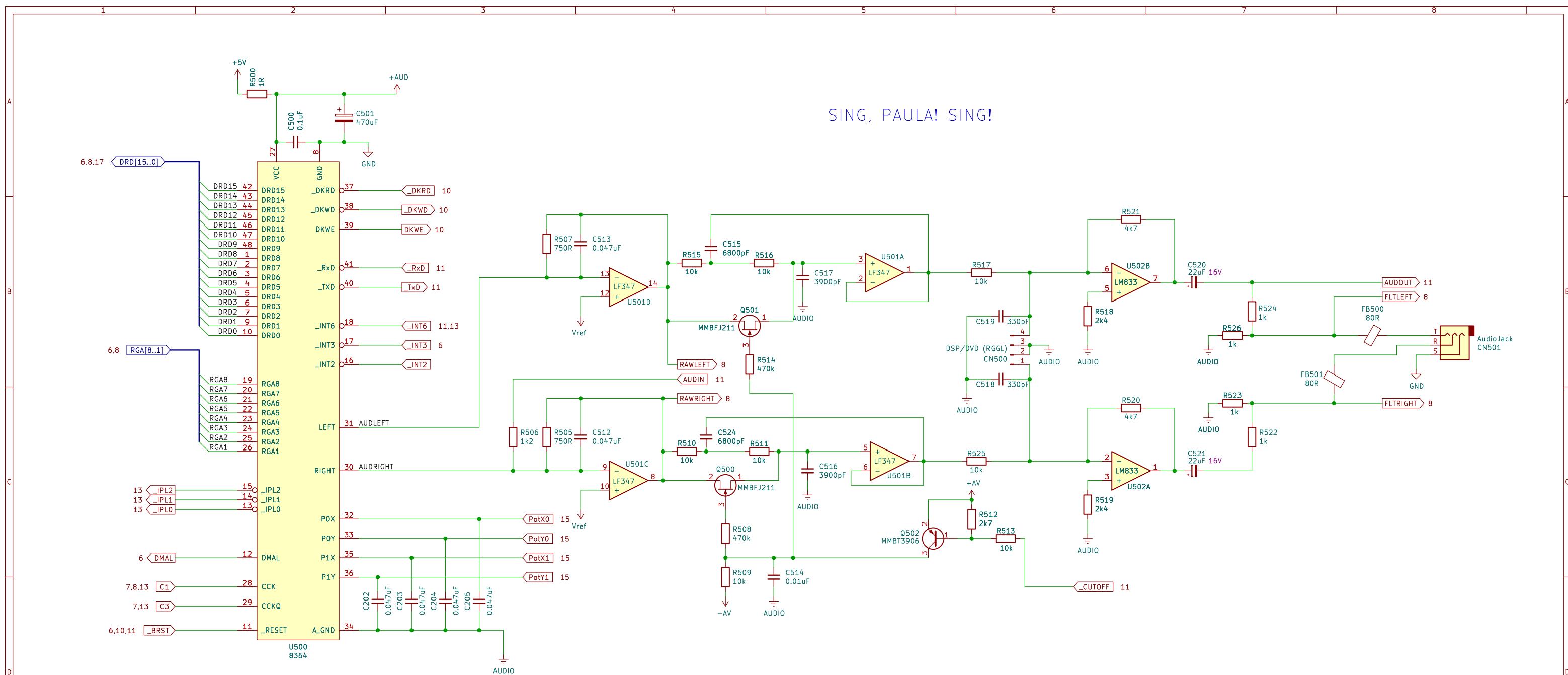
SUPPORTS 8372A (ALL PART NUMBERS)
AND 8375 THE FOLLOWING PART NUMBERS:
390544-01 AND COMPATIBLE (390544-02, 390554-01, 390554-02,
318069-12, 318069-13) AND 318069-10, 318069-11

JUMPERS MUST BE CONFIGURED BY AGNUS PART NUMBER





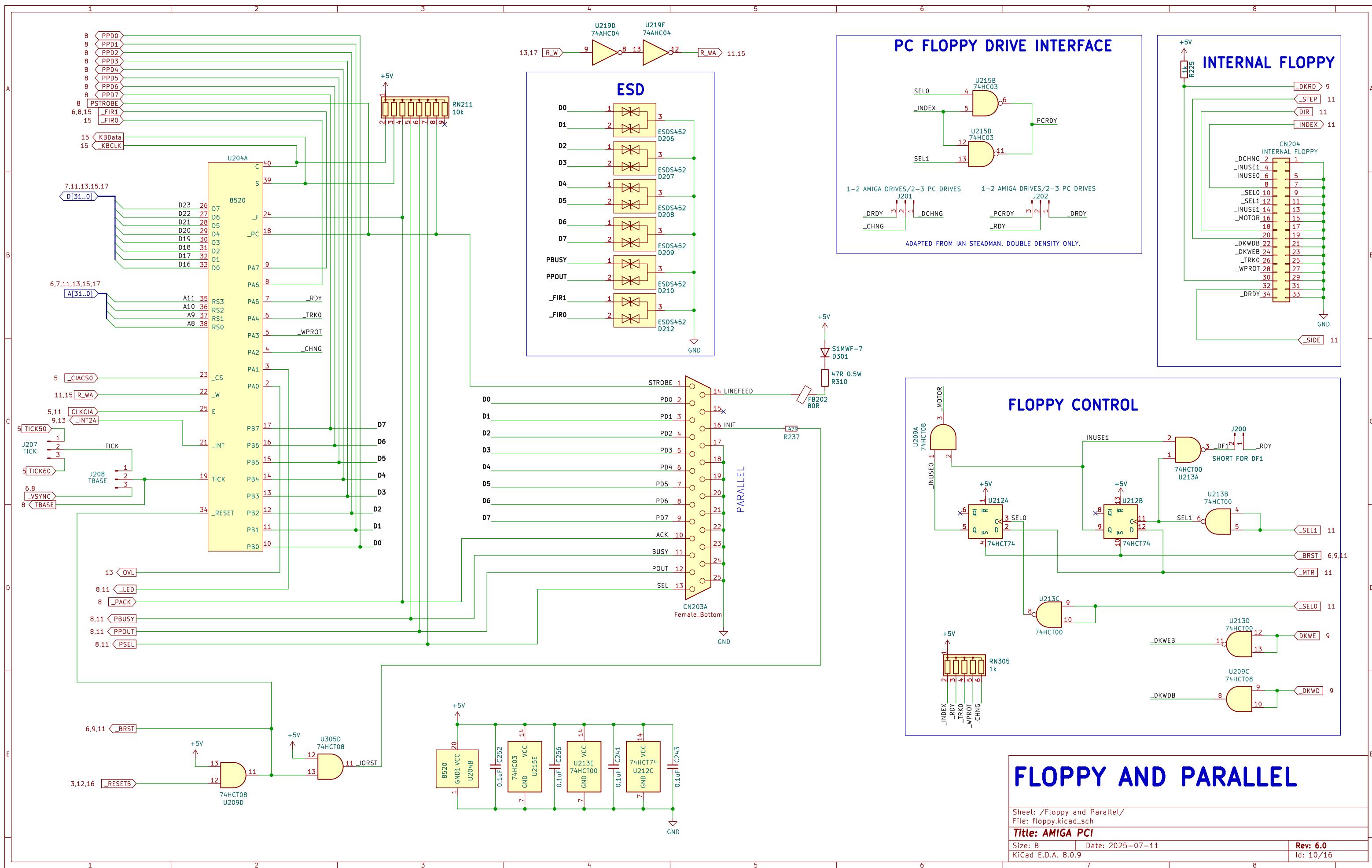


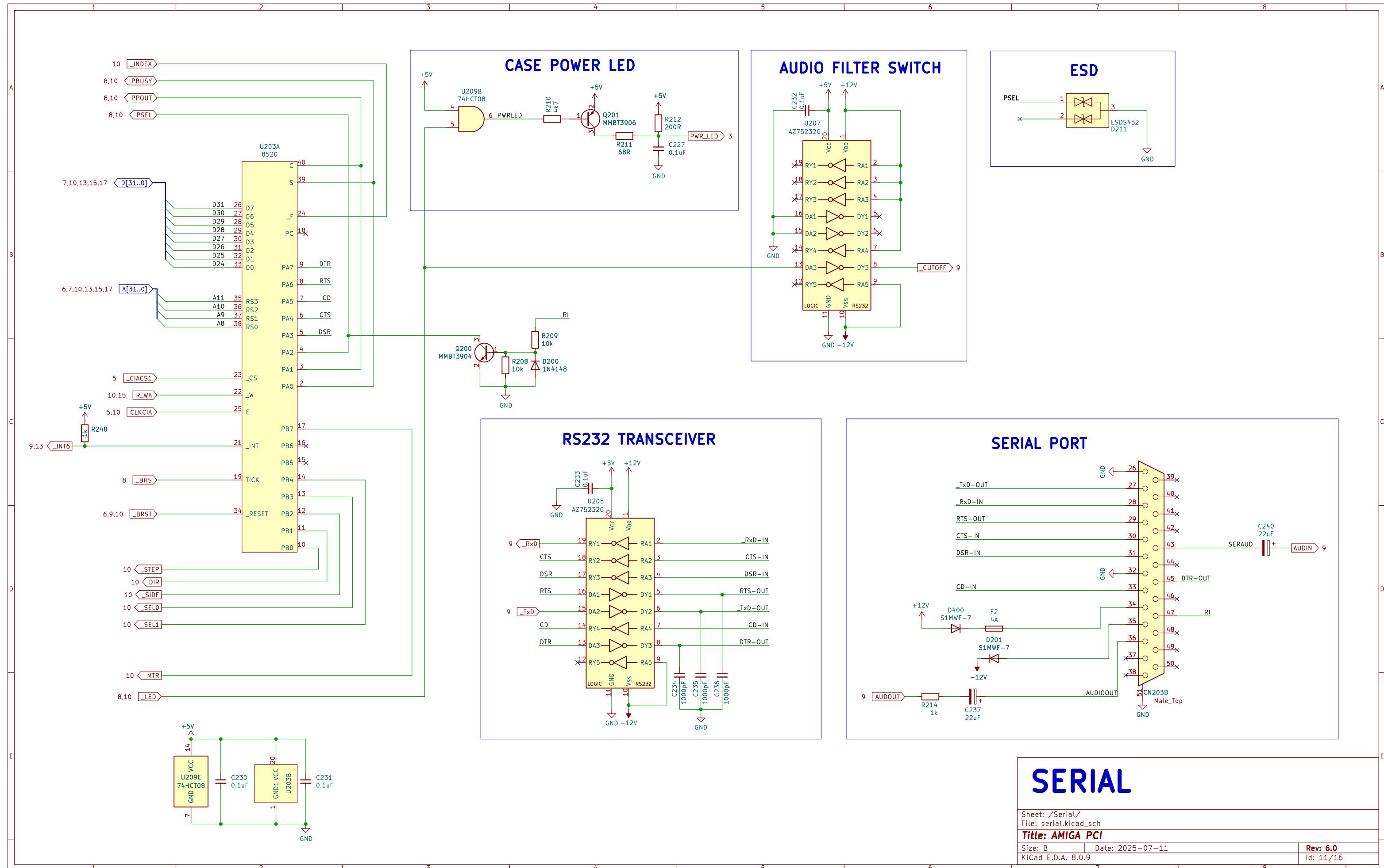


PAULA/AUDIO

Sheet: /Audio/
File: audio.kicad_sch

Title: AMIGA PCI

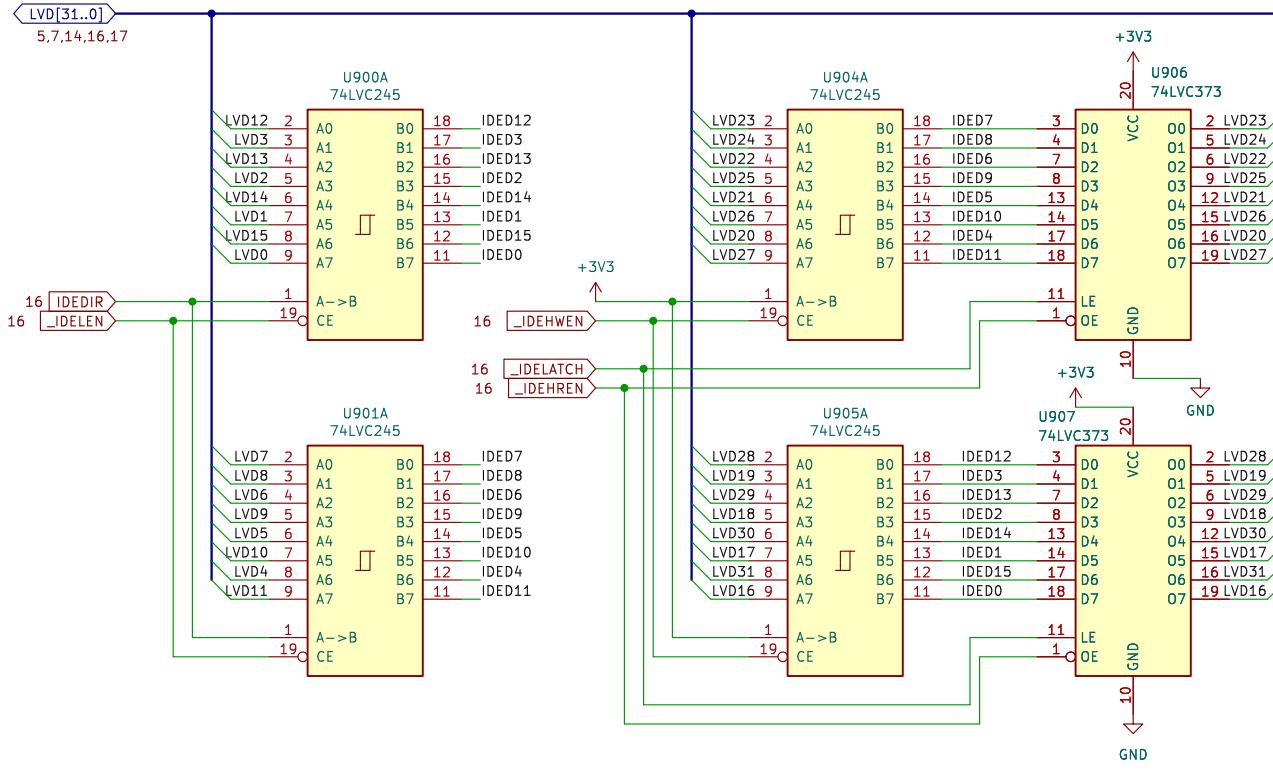




74LVC245 ENABLED FOR FIRST WORD DURING WRITES.
DATA ALWAYS FLOWS A->B.
74LVC373 LATCHES DATA FOR FIRST WORD DURING READS.

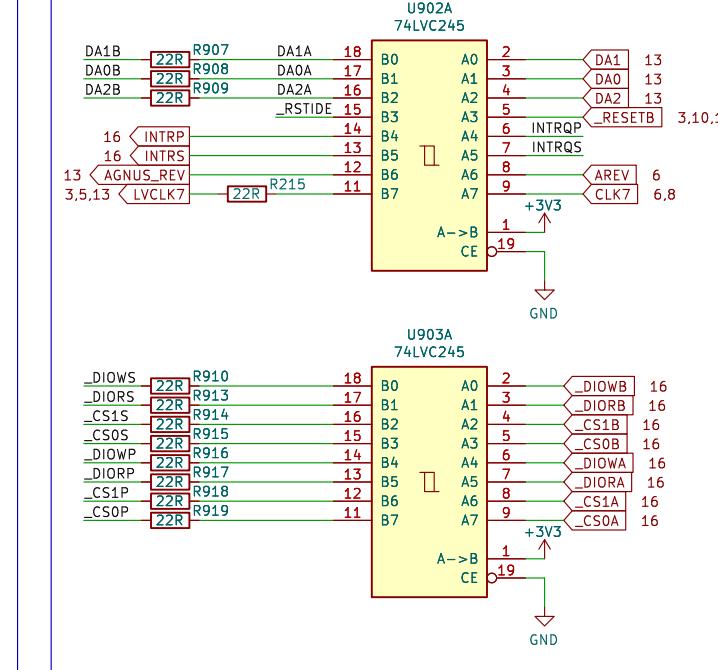
DURING WRITE CYCLES, ALL DATA IS REAL-TIME.
FOR THE FIRST WRITE COMMAND, D16..31 IS ENABLED.
FOR THE SECOND WRITE COMMAND, D0..15 IS ENABLED.

DATA BUFFERS

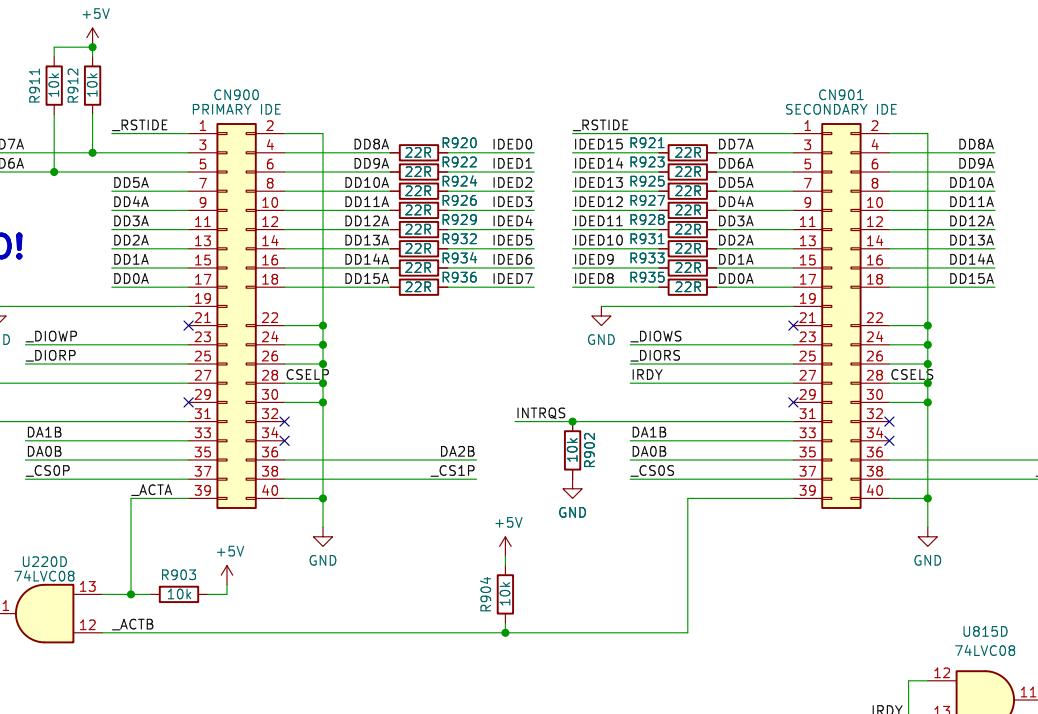


DURING READ CYCLES, D16..31 IS LATCHED
AFTER THE FIRST READ COMMAND FROM THE IDE DEVICE.
THE SECOND READ FROM THE ATA DEVICE DRIVES
D0..15 LIVE. THE DATA IS THEN LATCHED BY THE MC68040.

BUFFERS AND TERMINATION



PORTS BYTE SWAPPED!



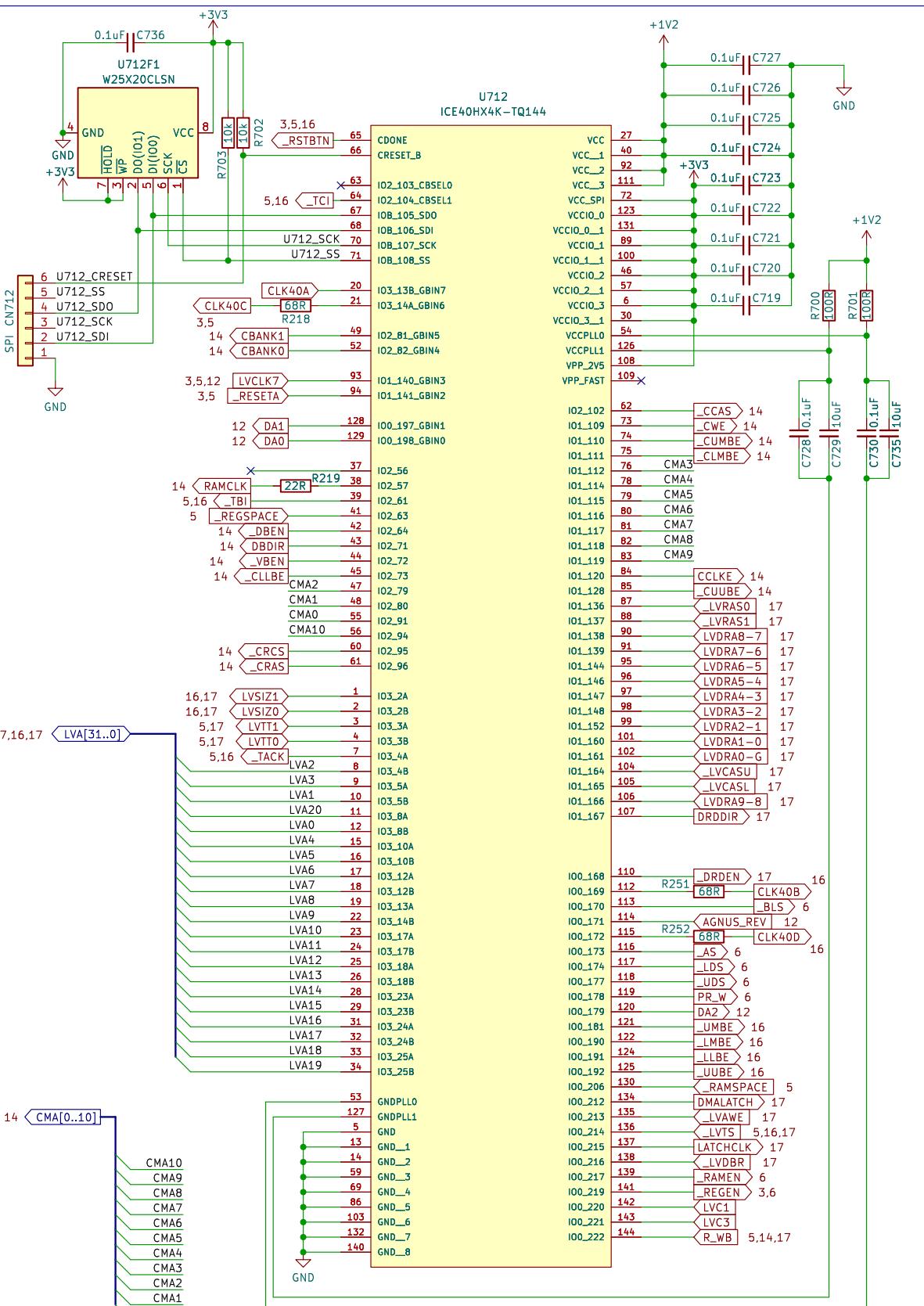
ATA INTERFACE

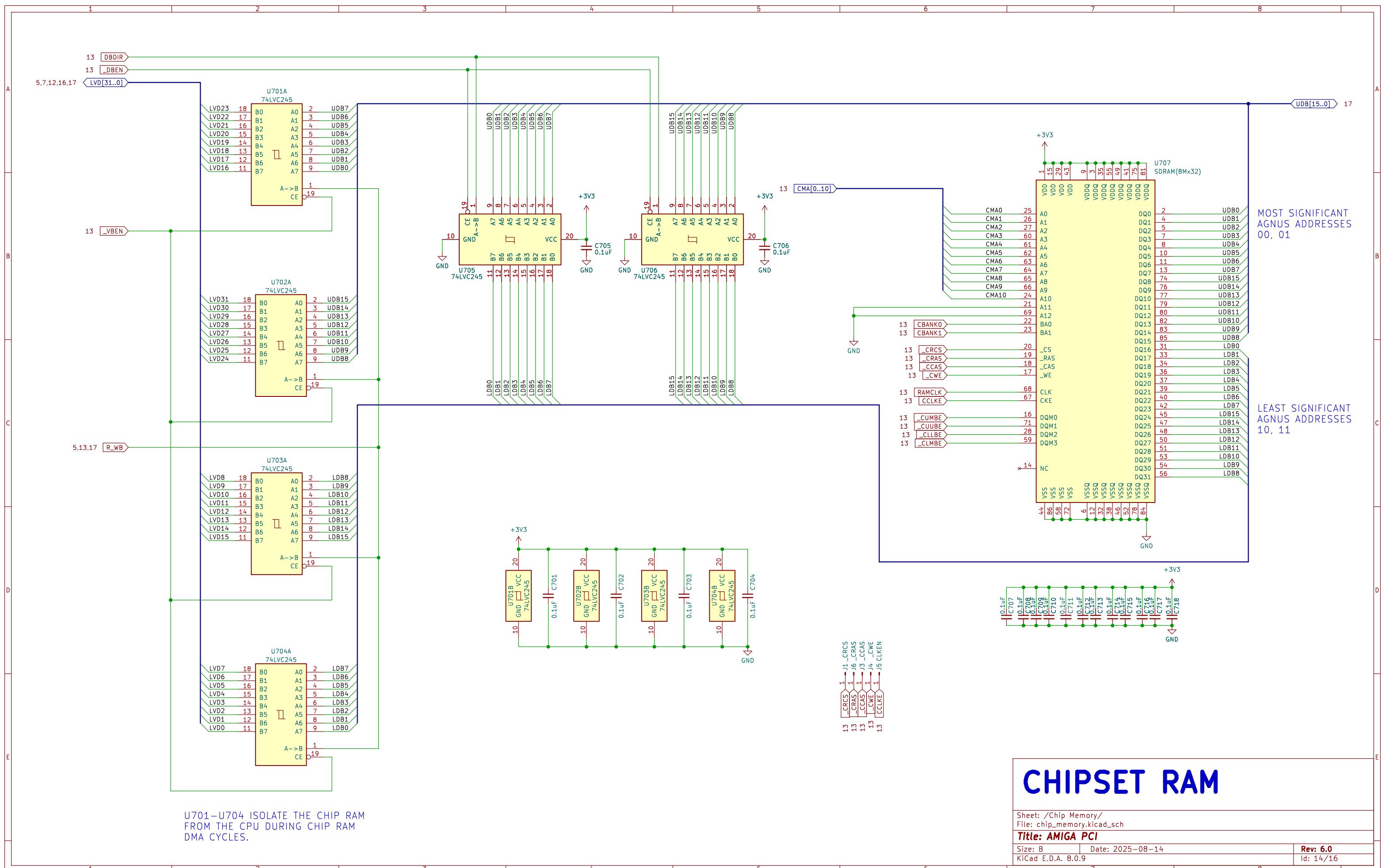
Sheet: /ATA Interface/
File: IDE.kicad_sch

Title: AMIGA PCI

Size: B Date: 2025-07-11
KiCad E.D.A. 8.0.9

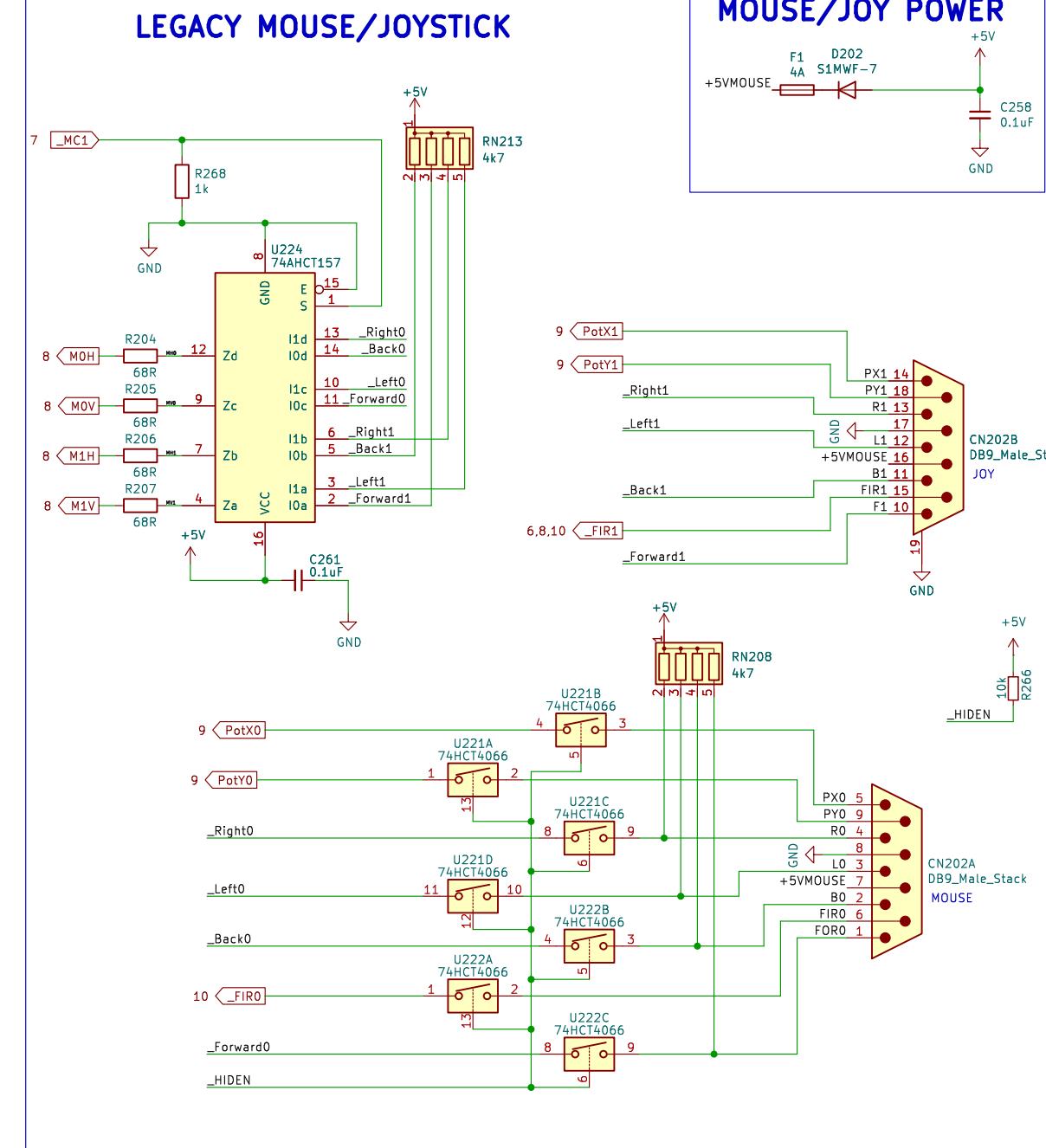
Rev: 6.0
Id: 12/16



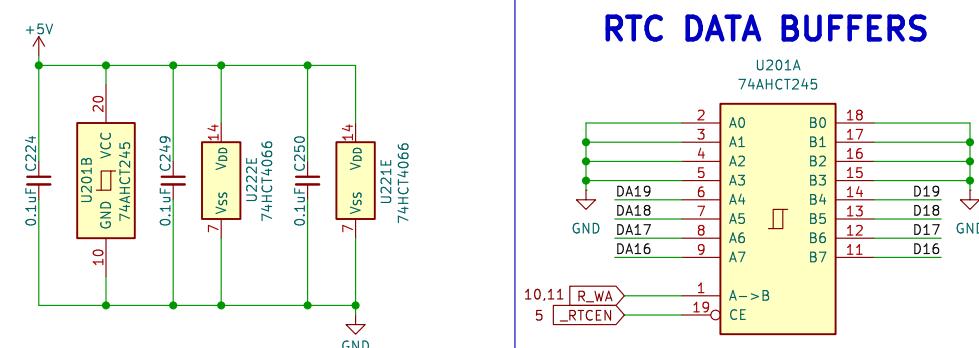


LEGACY MOUSE/JOYSTICK

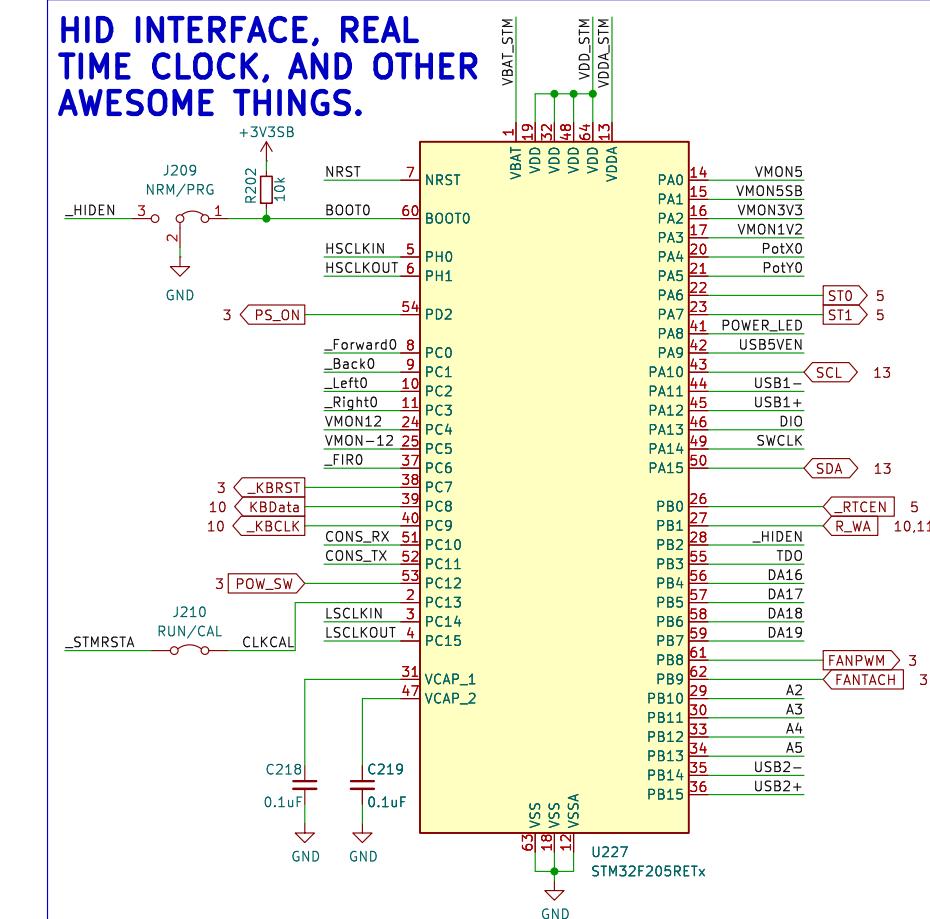
MOUSE/JOY POWER



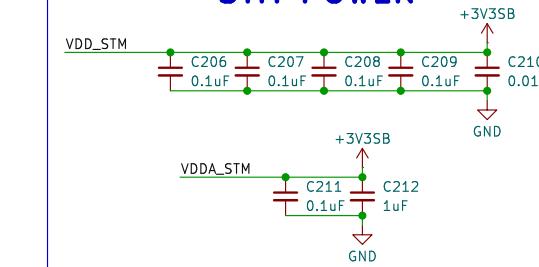
RTC DATA BUFFERS



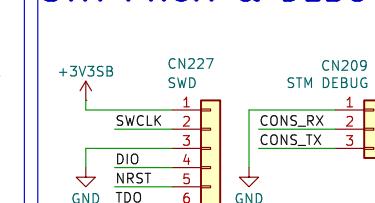
HID INTERFACE, REAL TIME CLOCK, AND OTHER AWESOME THINGS.



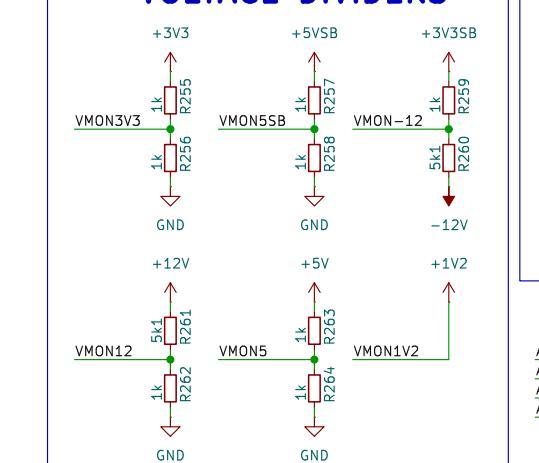
STM POWER



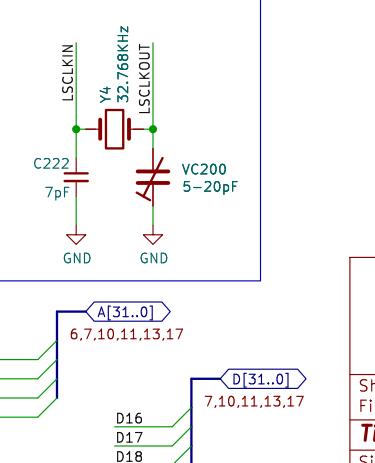
STM PRGM & DEBUG



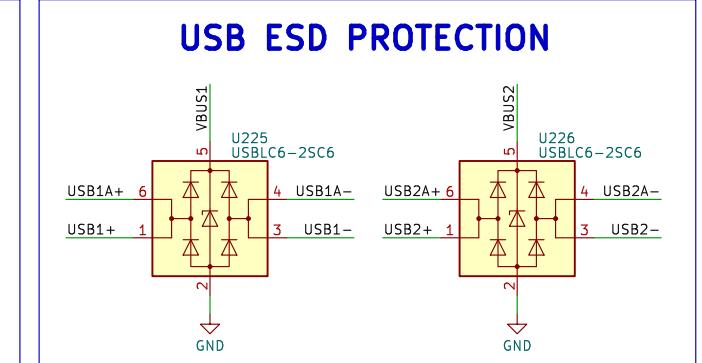
VOLTAGE DIVIDERS



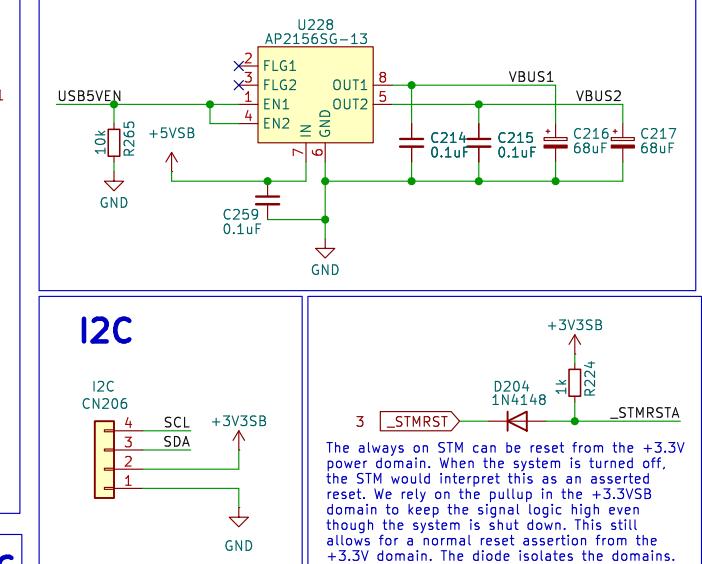
CALENDAR OSC



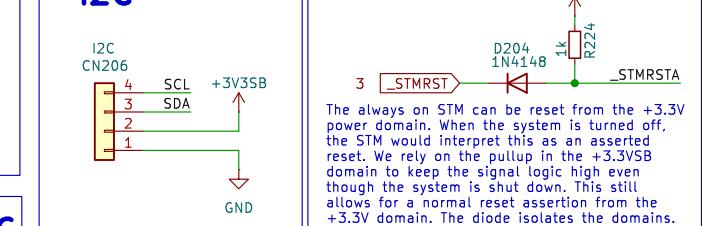
USB ESD PROTECTION



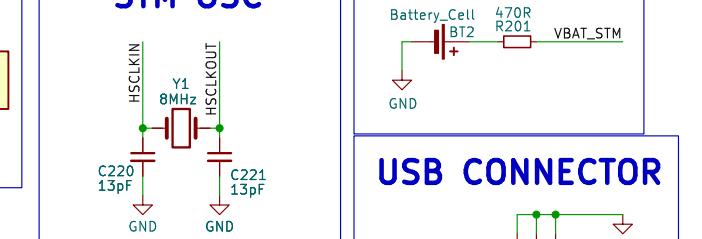
USB POWER



I2C



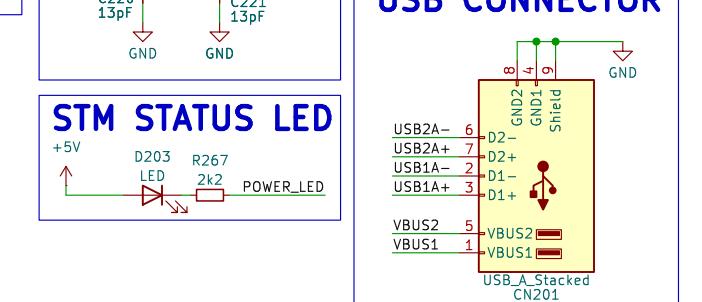
STM OSC



BACKUP BAT



USB CONNECTOR



HID and USER PORTS

Sheet: /HID and User Ports/

File: hid.kicad_sch

Title: AMIGA PCI

Size: B Date: 2025-07-11

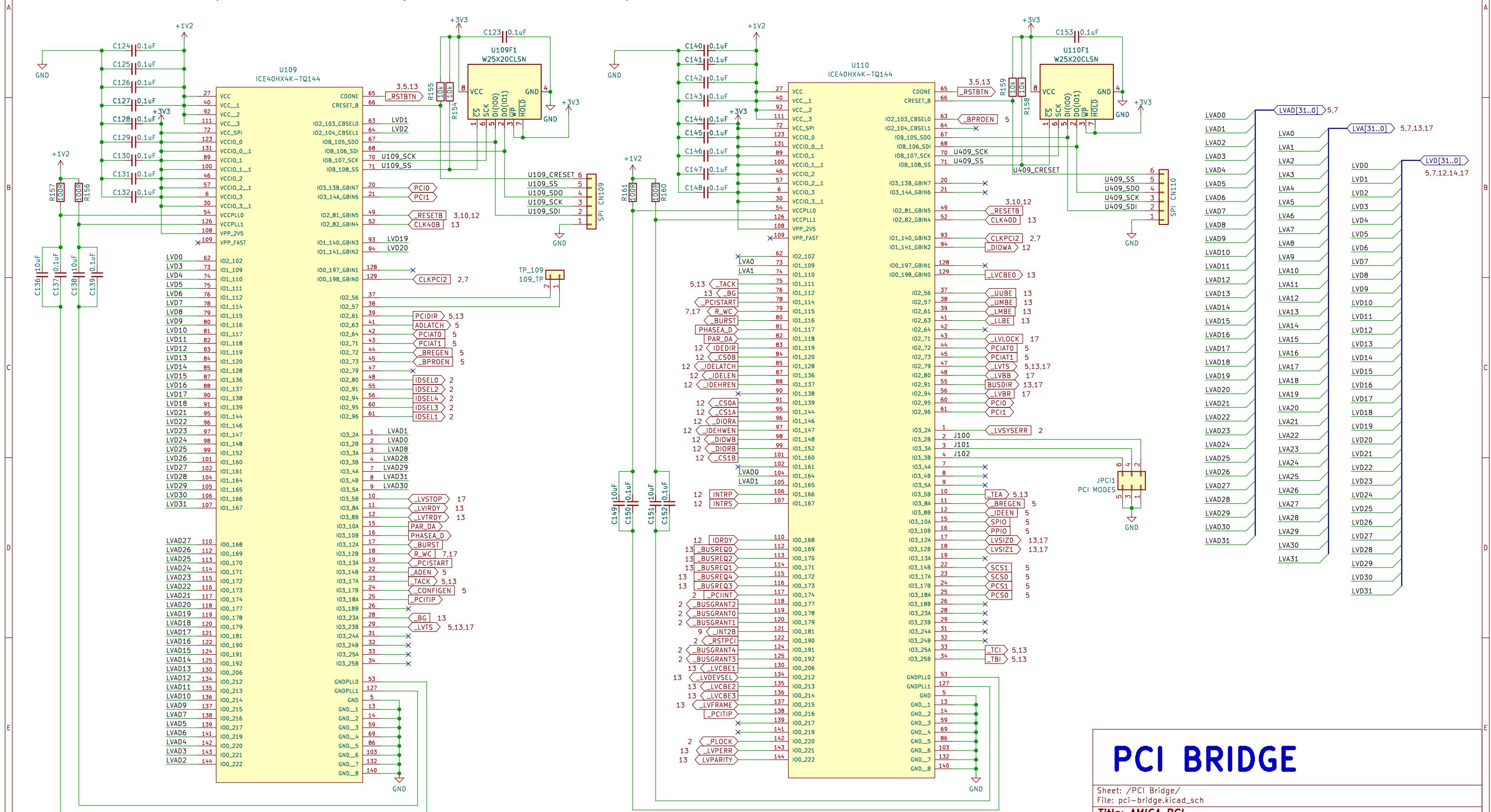
KiCad E.D.A. 8.0.9

Rev: 6.0

Id: 15/16

U109: BUFFERS AD/D/A BUSSES, AD PARITY CALC, BRIDGE REGISTERS

U110: BUS ARBITOR, INTERRUPT HANDLING, PARITY ERROR DETECTION, ATA CONTROLLER



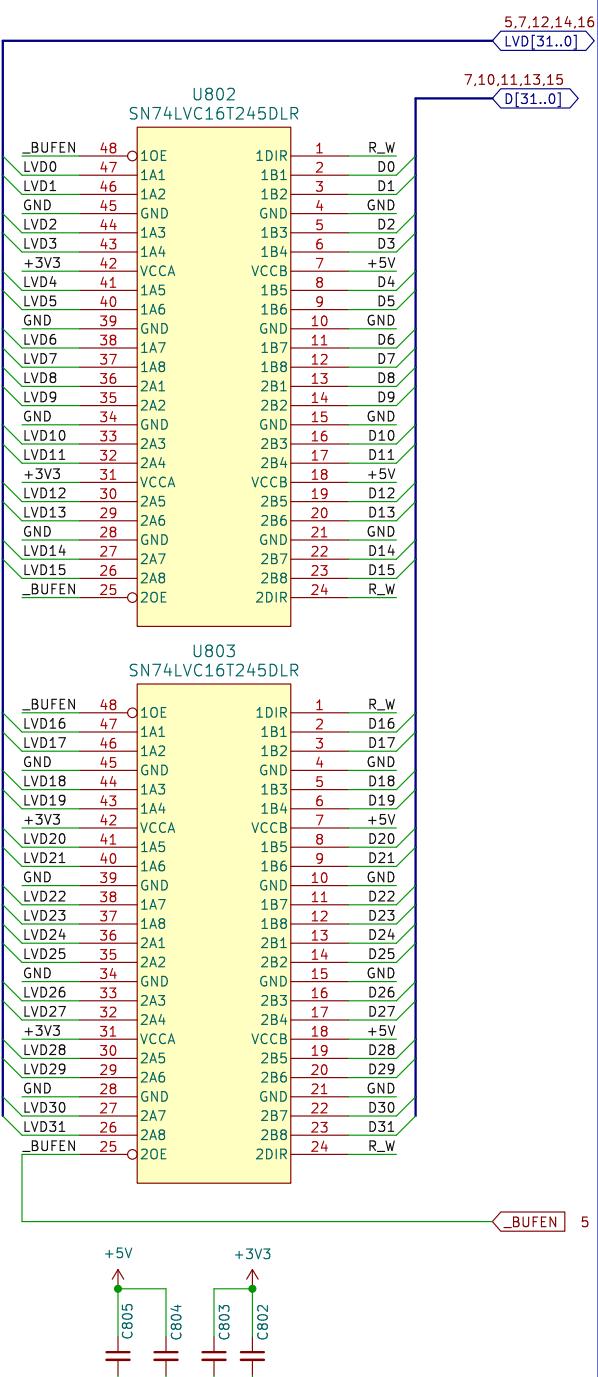
PCI BRIDGE

Sheet: /PCI Bridge/
File: pci-bridge.kicad_sch
Title: AMIGA PCI

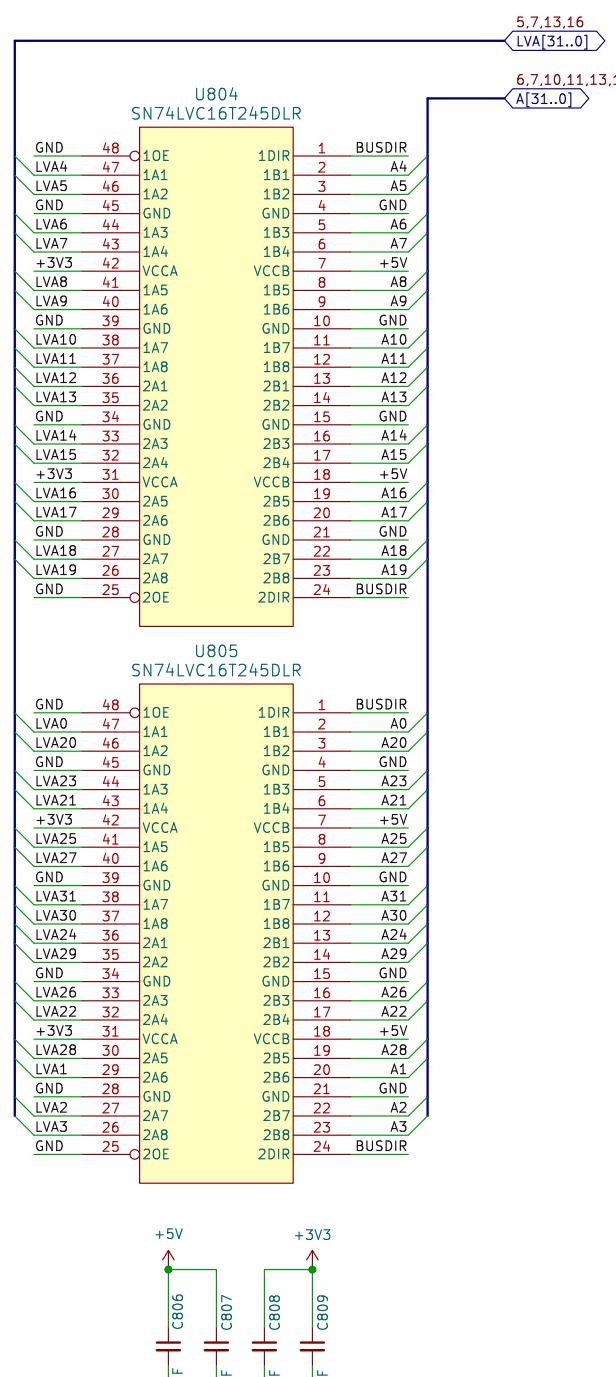
Size: B Date: 2025-07-25
KiCad E.D.A. 8.0.9

Rev: 6.0
Id: 16/16

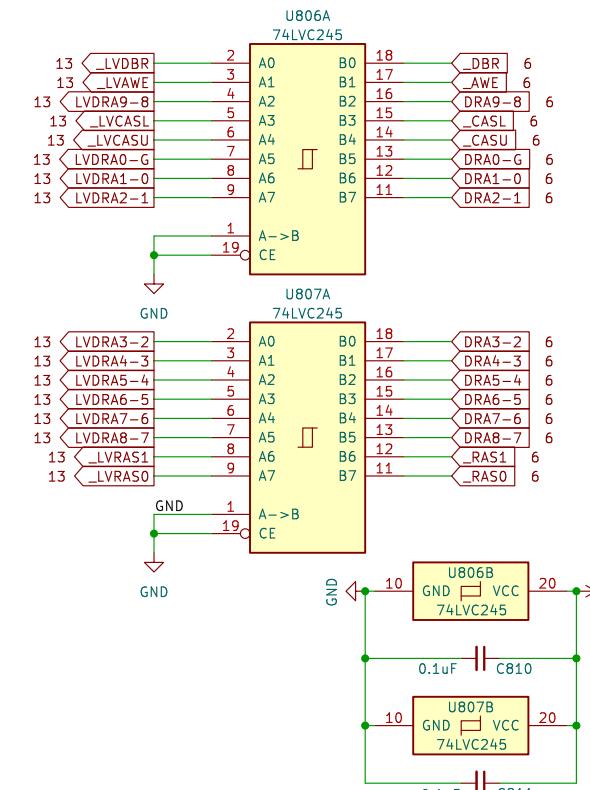
LOW VOLTAGE DATA BUFFERS



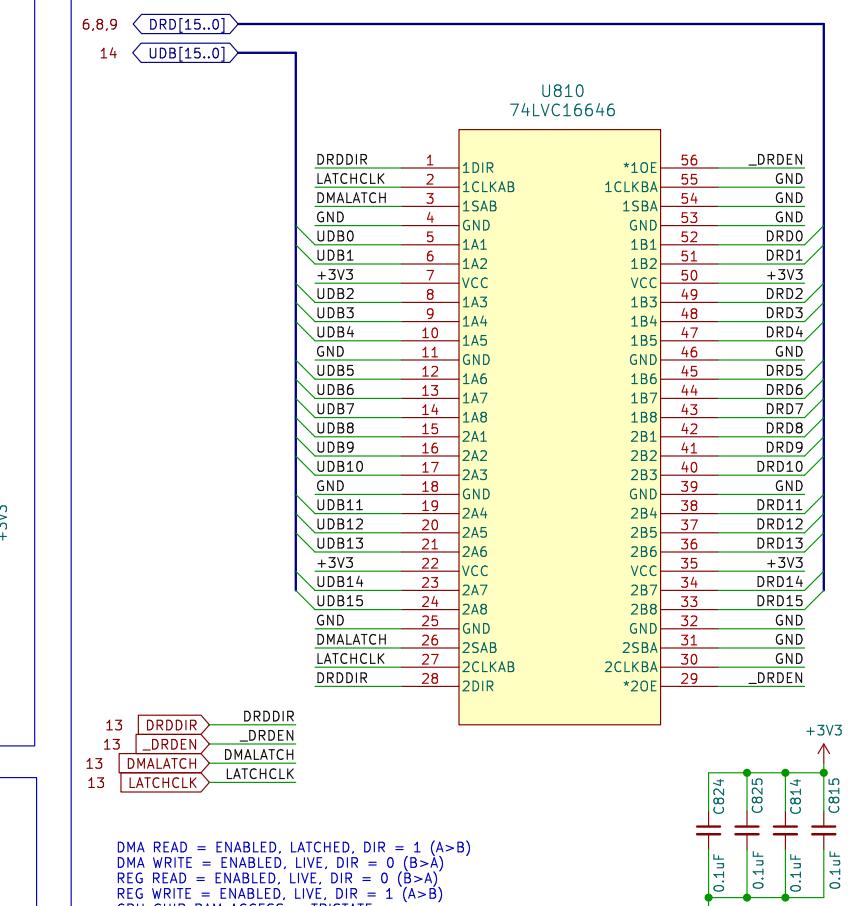
LOW VOLTAGE ADDRESS BUFFERS



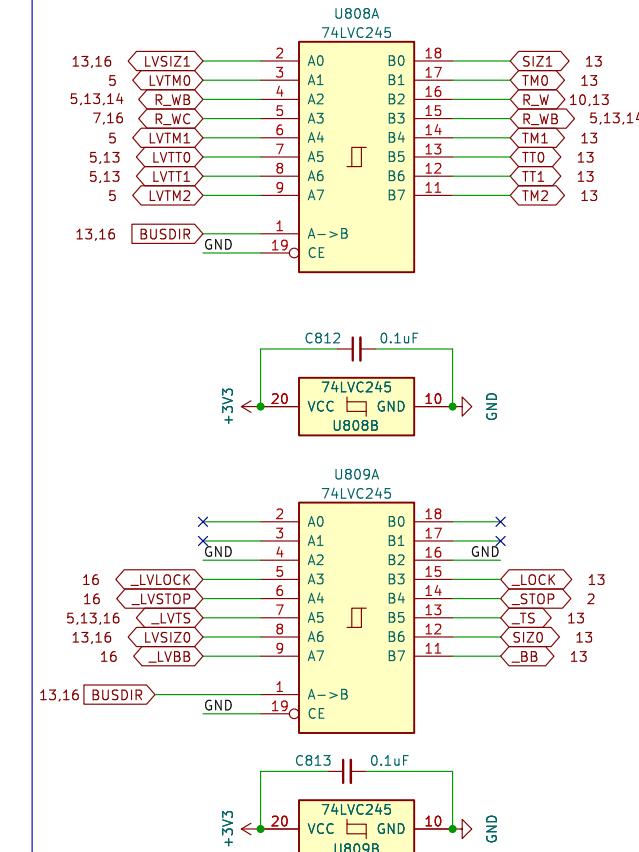
LOW VOLTAGE AGNUS SIGNALS



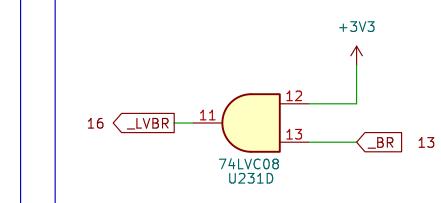
LOW VOLTAGE CHIPSET RAM DATA BUS BUFFER/LATCH



BIDIRECTIONAL MC68040



BUS REQUEST



MC68040 LEVEL SHIFT

Sheet: /MC68040_Level Shift/

File: mc68040_level_shift.kicad_sch

Title: AMIGA PCI

Size: B Date: 2025-07-11

KiCad E.D.A. 8.0.9

Rev: 6.0

Id: 17/16