Sequential Circuit Verification at Word Level using Algebraic Geometry

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Ph.D's Dissertation Proposal

Outline

- Introduction
- Sequential circuit verification requires reachability analysis
- From bit-level to word-level
- One step back: Sequential arithmetic circuit verification
- New inspiration: UNSAT core extraction using algebraic geometry
- The plan

Agenda

- Focus
 - Implicitly analyze the reachability of a sequential circuit at word level
 - Algebraic geometry to assist in sequential circuits verification and abstraction refinement
- Motivation
 - Data flow = word-level info
 - Conventional techniques are bit-level
 - Need bit-level to word-level abstraction
 - Verification ← Reachability ← Abstraction (at word level?)
- Target problems
 - Given: sequential circuit (FSM), with k-bit state variables, property for verification
 - Perform sequential equivalence/property checking at word level
 - Identify UNSAT cores in word-level problems

Agenda(2)

- Approach: Algebraic geometry techniques
 - Model system over $\mathbb{F}_{2^k} \implies$ Represent at word-level; compatible with bit-level: $\mathbb{F}_2 \subset \mathbb{F}_{2^k}$
 - ullet Gröbner basis methods + Elimination ordering + BFS traversal
- Challenge: Discover efficient algorithm to implement image computations, set operations, UNSAT proofs, etc. at word level
- Contributions:
 - \bullet Polynomial abstraction + algebraic geometry techniques applied to reachability analysis
 - A new algorithm based on Gröbner basis computation to extract UNSAT cores

Motivation

- Importance of reachability analysis in sequential circuits verification
 - Circuits \rightarrow state machines; errors \rightarrow bad states
 - Bad states are reachable implies errors affect circuit behavior
- Advantages exploiting word-level verification
 - Many circuit datapaths/system models are described at word level
 - Reduce state space, avoid "bit-blasting"
- Why use algebraic geometry?
 - ullet A symbolic representation for bit-level & word-level: \mathbb{F}_{2^k}
 - Algebraic geometry: allows reasoning on solutions by analyzing polynomials, solution ← states
 - Recent work [Tim, Abstraction using GB, DAC'14] [Lv, Equivalence, TCAD'13] [M. Brickenstein, GB for Boolean Poly, J.Symb.Comp.'09] shows it is practical to apply algebraic geometry to circuit verification

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Finite state machine (FSM)

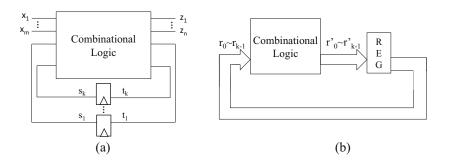


Figure: FSM models of sequential circuits

- (a) A typical model for sequential circuits
- (b) sequential datapath without primary inputs

State transition graph (STG) and breadth-first search (BFS) state space traversal

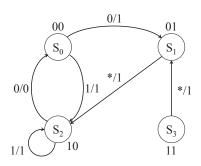


Figure: State Transition Graph

- Initial state: {00}
- Iteration 1:
 - Start from {00}
 - One-step transition: $\{01, 10\}$
 - Newly reached: {01, 10}
- Iteration 2:
 - Start from {01, 10}
 - One-step transition: $\{00, 10\}$
 - Newly reached: Ø
- All reachable states detected.
 Final reached states:
 {00, 01, 10}

BFS traversal with gate-level circuits

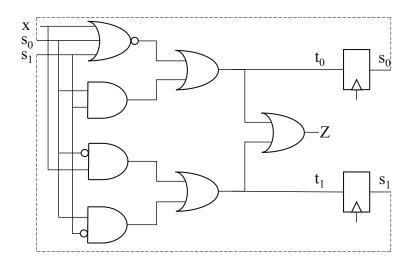


Figure : A Gate-level Circuit corresponding to last STG

Breadth-First Traversal Algorithm based on Boolean formulas

ALGORITHM 1: Breadth-first Traversal Algorithm

```
Input: Transition functions \Delta, initial state S^0

1 from^0 = reached = S^0;

2 repeat

3 i \leftarrow i + 1;

4 to^i \leftarrow Img(\Delta, from^{i-1});

5 new^i \leftarrow to^i \cap \overline{reached};

6 reached \leftarrow reached \cup new^i;

7 from^i \leftarrow new^i;

8 until\ new^i == 0;

9 return\ reached
```

Breadth-First Traversal Algorithm based on Boolean formulas(2)

- Image function: $Img(\Delta, from) = \exists_s \exists_x [T(s, x, t) \land from] = \exists_s \exists_x \bigwedge_{i=1}^n (t_i \overline{\oplus} \Delta_i) \land from$
- Initial state: $from^0 = \overline{s_0} \wedge \overline{s_1} \ (\{00\})$
- Transition function:

$$egin{array}{ll} \Delta_1: t_0\overline{\oplus}(\left(\overline{xee s_0ee s_1}
ight)ee s_0\wedge s_1) \ \Delta_2: t_1\overline{\oplus}\left(\overline{s_0}\wedge xee ee \overline{s_1}\wedge s_0
ight) \end{array}$$

- Iteration 1:
 - One-step transition $to^1 = \exists_{s_0,s_1,x} (\Delta_1 \wedge \Delta_2 \wedge \textit{from}^0) = \overline{t_0} \wedge t_1 \ \lor \ t_0 \wedge \overline{t_1} \ \big(\{01,10\} \big)$
 - Newly reached $new^1 = (\overline{t_0} \wedge t_1 \vee t_0 \wedge \overline{t_1}) \wedge \overline{(\overline{t_0} \wedge \overline{t_1})} = \overline{t_0} \wedge t_1 \vee t_0 \wedge \overline{t_1}$
- Iteration 2: same fashion
- Return value: $reached = \{\overline{t_0} \lor \overline{t_1}\} \ (\{00, 01, 10\})$

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Galois Field Overview

Galois field(GF) \mathbb{F}_q is a finite field with q elements, $q = p^k$

- Commutative Ring with unity, associate, distributive laws
- Closure property: $+, -, \times$, inverse (\div)
- $\mathbb{F}_p \equiv (\mathbb{Z} \pmod{p})$, where p = prime, is a field
 - $\mathbb{Z}_7 = \{0, 1, 2, 3, 4, 5, 6\}$

Our interest: $\mathbb{F}_q = \mathbb{F}_{2^k}$, i.e. $q = 2^k$

- \mathbb{F}_{2^k} : k-dimensional extension of \mathbb{F}_2
 - k-bit bit-vector, AND/XOR arithmetic

To construct \mathbb{F}_{2^k}

- $\bullet \ \mathbb{F}_{2^k} \equiv \mathbb{F}_2[x] \ (\mathsf{mod} \ P(x))$
- $P(x) \in \mathbb{F}_2[x]$, irreducible polynomial of degree k

Field Elements: e.g. \mathbb{F}_8

Consider:
$$\mathbb{F}_{2^3} = \mathbb{F}_2[x] \pmod{x^3 + x + 1}$$

$$A \in \mathbb{F}_2[x]$$

A
$$(\text{mod } x^3 + x + 1) = a_2 x^2 + a_1 x + a_0$$
. Let $P(\alpha) = 0$:

•
$$\langle a_2, a_1, a_0 \rangle = \langle 0, 0, 0 \rangle = 0$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 0, 0, 1 \rangle = 1$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 0, 1, 0 \rangle = \alpha$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 0, 1, 1 \rangle = \alpha + 1$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 1, 0, 0 \rangle = \alpha^2$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 1, 0, 1 \rangle = \alpha^2 + 1$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 1, 1, 0 \rangle = \alpha^2 + \alpha$$

•
$$\langle a_2, a_1, a_0 \rangle = \langle 1, 1, 1 \rangle = \alpha^2 + \alpha + 1$$

Polynomial functions in Galois field

• Polynomial functions over $\mathbb{F}_{2^k} \Leftrightarrow \text{Boolean functions on } k\text{-bit vectors}$

$$f: \mathbb{B}^k \to \mathbb{B}^k \Leftrightarrow \mathcal{F}: \mathbb{F}_{2^k} \to \mathbb{F}_{2^k}$$

• Example: Lagrange's interpolation

$\{\alpha_2\alpha_1\alpha_0\}$	A	\rightarrow	$\{z_2z_1z_0\}$	Z
000	0	\rightarrow	000	0
001	1	\rightarrow	000	0
010	α	\rightarrow	001	1
011	$\alpha + 1$	\rightarrow	001	1
100	α^2	\rightarrow	010	α
101	$\alpha^2 + 1$	\rightarrow	010	α
110	$\alpha^2 + \alpha$	\rightarrow	011	$\alpha + 1$
111	$\alpha^2 + \alpha + 1$	\rightarrow	011	$\alpha + 1$

Polynomial functions in Galois field(2)

• 8 pairs of (A, Z), use Lagrange's interpolation to abstract a polynomial function

$$Z = \mathcal{F}(A) = \sum_{n=1}^{8} \left[\frac{\prod_{i \neq n} (A - A_i)}{\prod_{i \neq n} (A_n - A_i)} \cdot Z_n \right]$$

- Result = $(\alpha^2 + 1)A^4 + (\alpha^2 + 1)A^2$ implies polynomial representation of the function
- $P(\alpha) = \alpha^3 + \alpha + 1 = 0$

Algebraic Geometry Terminology

Let
$$\mathbb{F}_q = GF(2^k)$$
:

- $\mathbb{F}_q[x_1,\ldots,x_n]$: ring of all polynomials with coefficients in \mathbb{F}_q
- Given a set of polynomials:
 - $f, f_1, f_2, \ldots, f_s \in \mathbb{F}_q[x_1, \ldots, x_n]$
 - Find solutions to $f_1 = f_2 = \cdots = f_s = 0$
- Variety: Set of ALL solutions to a given system of polynomial equations: $V(f_1, \ldots, f_s)$
 - In $\mathbb{R}[x,y]$, $V(x^2+y^2-1) = \{all \ points \ on \ circle : x^2+y^2-1=0\}$
 - In $\mathbb{R}[x]$, $V(x^2 + 1) = \emptyset$
 - In $\mathbb{C}[x]$, $V(x^2+1) = \{(\pm i)\}$
- Variety depends on the ideal generated by the polynomials.
- Reason about the Variety by analyzing the Ideals

Ideals and Gröbner basis

Definition

Ideals of Polynomials: Let $f_1, f_2, \ldots, f_s \in \mathbb{F}_q[x_1, \ldots, x_n]$. Let

$$J = \langle f_1, f_2, \dots, f_s \rangle = \{ f_1 h_1 + f_2 h_2 + \dots + f_s h_s \}$$

 $J = \langle f_1, f_2 \dots, f_s \rangle$ is an ideal generated by f_1, \dots, f_s and the polynomials are called the generators.

- Different generators can generate the same ideal
- $\bullet \ \langle f_1, \cdots, f_s \rangle = \cdots = \langle g_1, \cdots, g_t \rangle$
- Some generators are a "better" representation of the ideal
- A (reduced) **Gröbner basis** is a "canonical" representation of an ideal

Given $F = \{f_1, f_2, \dots, f_s\}$, Compute a Gröbner Basis (using Buchberger's algorithm) $G = \{g_1, g_2, \dots, g_t\}$, such that $I = \langle F \rangle = \langle G \rangle$

$$V(F) = V(G)$$

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Elimination Theorem [Tim, Abstraction using GB, DAC'14]

- ullet Let $J\subset \mathbb{F}_q[x_1,\ldots,x_d]$ be an ideal
- Let G be a Gröbner basis of J with respect to a lex ordering where $x_1 > x_2 > \cdots > x_d$.
- Then for every $0 \le l \le d$:
 - The set $G_l = G \cap \mathbb{F}_q[x_{l+1}, \dots, x_d]$ is a Gröbner basis of the *l*th elimination ideal J_l .

The Ith elimination ideal does not contain variables x_1, \ldots, x_I , nor do the generators of it.

Elimination Term Ordering Example

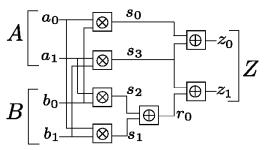
- Let ideal $I = \langle f_1, f_2, f_3 \rangle$ where
 - $f_1 = x^2 + v + z 1$
 - $f_2 = x + y^2 + z 1$
 - $f_3 = x + y + z^2 1$
- The Gröbner basis of I with lex order (x > y > z) is
 - $g_1 = x + y + z^2 1$
 - $g_2 = y^2 y z^2 + z$
 - $g_3 = 2yz^2 + z^4 z^2$
 - $g_4 = z^6 4z^4 + 4z^3 z^2$
- Notice that g_2 and g_3 only contain variables y and z
 - Eliminates variable $x \Leftrightarrow \exists_x$ in Boolean formula!
- ullet Similarly, g_4 only contains the variable z and eliminates x and y

Abstraction Term Ordering[Tim, Abstraction using GB, DAC'14]

Derived from applying elimination theorem to our problem set

- Given a circuit C implementing $Z = \mathcal{F}(A)$ over \mathbb{F}_q
- Using the variable order $x_1 > x_2 > \cdots > x_d > Z > A$
 - x_1, \ldots, x_d are the circuit variables
- Impose a lex term order > on the polynomial ring $R = \mathbb{F}_q[x_1, \dots, x_d, Z, A]$.
- This elimination term order > is defined as the Abstraction Term
 Order.
- ullet Compute a Gröbner basis G of ideal $(J+J_0)$ using >
 - G will contain a polynomial of the form $Z + \mathcal{F}(A)$ $(Z = \mathcal{F}(A))$
 - $Z = \mathcal{F}(A)$ is a unique, canonical, polynomial representation of C over \mathbb{F}_q

Abstraction Term Order Example



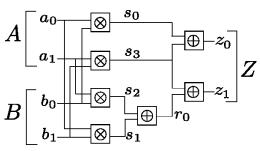
$$(z_0 > z_1 > r_0 > s_0 > s_3 > s_1 > s_2 > a_0 > a_1 > b_0 > b_1 > Z > A > B)$$

$$f_1: s_0 + a_0 \cdot b_0; \quad f_2: s_1 + a_0 \cdot b_1; \quad f_3: s_2 + a_1 \cdot b_0; \quad f_4: s_3 + a_1 \cdot b_1$$

 $f_5: r_0 + s_1 + s_2; \quad f_6: z_0 + s_0 + s_3; \quad f_7: z_1 + r_0 + s_3; \quad f_8: a_0 + a_1\alpha + A$
 $f_9: b_0 + b_1\alpha + B; \quad f_{10}: z_0 + z_1\alpha + Z$

$$J = \langle f_1, \ldots, f_{10} \rangle$$

Abstraction Term Order Example

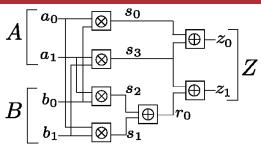


$$(z_0 > z_1 > r_0 > s_0 > s_3 > s_1 > s_2 > a_0 > a_1 > b_0 > b_1 > Z > A > B)$$

$$f_{11}: a_0^2 + a_0;$$
 $f_{12}: a_1^2 + a_1\alpha;$ $f_{13}: b_0^2 + b_0;$ $f_{14}: b_1^2 + b_1;$ $f_{15}: s_0^2 + s_0;$ $f_{16}: s_1^2 + s_1;$ $f_{17}: s_2^2 + s_2;$ $f_{18}: s_3^2 + s_3;$ $f_{19}: r_0^2 + r_0;$ $f_{20}: z_0^2 + z_0$ $f_{21}: z_1^2 + z_1;$ $f_{22}: A^4 + A;$ $f_{23}: B^4 + B;$ $f_{24}: Z^4 + Z$

$$J_0 = \langle f_{11}, \dots, f_{24} \rangle$$

Abstraction Term Order Example



 $(z_0>z_1>r_0>s_0>s_3>s_1>s_2>a_0>a_1>b_0>b_1>Z>A>B)$ Compute the Gröbner basis, G, of $\{J+J_0\}$ with respect to abstraction term ordering >. $G=\{g_1,\ldots,g_{14}\}$

$$g_1: B^4 + B; \quad g_2: b_0 + b_1\alpha + B; \quad g_3: a_0 + a_1\alpha + A; \quad g_4: A^4 + A;$$

$$g_5: s_0 + s_1\alpha + s_2(\alpha + 1) + Z; \quad g_6: r_0 + s_1 + s_2; \quad g_7: z_1 + r_0 + s_3$$

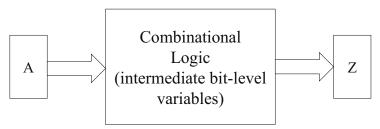
$$g_7: z_0 + z_1\alpha + Z; \quad \mathbf{g_9}: \mathbf{Z} + \mathbf{A} * \mathbf{B}; \quad g_{10}: b_1 + B^2 + B; \quad g_{11}: a_1 + A^2 + A$$

$$g_{12}: s_3 + a_1b_1; \quad g_{13}: s_2 + a_1b_1\alpha + a_1B; \quad g_{14}: s_1 + a_1b_1\alpha + b_1A$$

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Inspirations from Tim's work



- From Tim's work I learned:
 - Compute GB with term order intermediate > Z > A
 - Obtain $Z = \mathcal{F}(A)$
- Inspiration: what if we impose term order intermediate > A > Z?
- Proposed approach: add the evaluation of A into elimination ideal, then eliminate all but Z, we will get the evaluation of Z!
 - Present state $\leftarrow A$, next state $\leftarrow Z$
 - It is a way to do one-step reachability!



Recall: Breadth-First Traversal Algorithm

ALGORITHM 2: Breadth-first Traversal Algorithm

```
Input: Transition functions \Delta, initial state S^0

1 from^0 = reached = S^0;

2 repeat

3 i \leftarrow i + 1;

4 to^i \leftarrow lmg(\Delta, from^{i-1});

5 new^i \leftarrow to^i \cap \overline{reached};

6 reached \leftarrow reached \cup new^i;

7 from^i \leftarrow new^i;

8 until\ new^i == 0;

9 return\ reached
```

We want to implement this algorithm using algebraic geometry approach

Implement Image Function in Algebraic Geometry

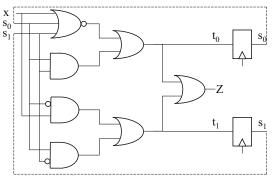
- State variables (word-level) S, T and sets of states such as fromⁱ, toⁱ can always be represented as varieties of ideals.
- ullet Boolean operators can always be converted to operations in \mathbb{F}_2

Boolean operator	operation in \mathbb{F}_2	
ā	1 + a	
a and b	ab	
a or b	a+b+ab	
$a \oplus b$	a+b	

Table : Some Boolean operators and corresponding operations in \mathbb{F}_2

 An elimination ideal can be built from circuit gates, PS/NS word definition and vanishing polynomials

Example Formulation



Model circuit as polynomials in $\mathbb{F}_2 \subset \mathbb{F}_{2^k}$:

$$t_0 = (\overline{x} \text{ and } \overline{s_0} \text{ and } \overline{s_1}) \text{ or } (s_0 \text{ and } s_1)$$

$$\implies f_1 : t_0 - (xs_0s_1 + xs_0 + xs_1 + x + s_0 + s_1 + 1)$$

$$t_1 = (\overline{s_0} \text{ and } x) \text{ or } (s_0 \text{ and } \overline{s_1})$$

$$\implies f_2 : t_1 - (xs_0 + x + s_0s_1 + s_0)$$

$$f_3 : S + s_0 + s_1\alpha, \quad f_4 : T + t_0 + t_1\alpha$$

Example Formulation(2)

- Elimination ideal to model Image function for example circuit:
 - Transition functions (bit-level): f_1, f_2
 - Word variable definitions: f₃, f₄
 - Vanishing polynomials: $f_6: x^2 x$; $f_7: t_0^2 t_0$; $f_8: t_1^2 t_1$; $f_9: S^4 S$; $f_{10}: s_0^2 s_0$; $f_{11}: s_1^2 s_1$; $f_{12}: T^4 T$
- Add the present state (e.g. initial states in first iteration $f_5:S$), compute Gröbner basis for ideal $J=\langle f_1,\ldots,f_{12}\rangle$ under elimination term order

intermediate bit-level signals > bit-level PIs/POs > S > T

• Result will include a univariate polynomial about *next states* T, e.g. $T^2 + (\alpha + 1)T + \alpha$

Recall: Breadth-First Traversal Algorithm

ALGORITHM 3: Breadth-first Traversal Algorithm

```
Input: Transition functions \Delta, initial state S^0

1 from^0 = reached = S^0;

2 repeat

3 i \leftarrow i + 1;

4 to^i \leftarrow Img(\Delta, from^{i-1});

5 new^i \leftarrow to^i \cap \overline{reached};

6 reached \leftarrow reached \cup new^i;

7 from^i \leftarrow new^i;

8 until\ new^i == 0;

9 return\ reached
```

We want to implement this algorithm using algebraic geometry approach

Intersection and union in algebraic geometry

Definition

(Sum/Product of Ideals) If $I = \langle f_1, \dots, f_r \rangle$ and $J = \langle g_1, \dots, g_s \rangle$ are ideals in $\mathbb{F}[x_1, \dots, x_n]$, then the sum of I and J is defined as

$$I+J=\langle f_1,\ldots,f_r,g_1,\ldots,g_s\rangle$$

And the **product** of I and J is defined as

$$I \cdot J = \langle f_i g_i \mid 1 \le i \le r, 1 \le j \le s \rangle$$

Theorem

If I and J are ideals in $\mathbb{F}[x_1,\ldots,x_n]$, then $\mathbf{V}(I+J)=\mathbf{V}(I)\cap\mathbf{V}(J)$ and $\mathbf{V}(I\cdot J)=\mathbf{V}(I)\mid J\mathbf{V}(J)$.

Complement set in algebraic geometry

Definition

(**Quotient of Ideals**) If I and J are ideals in $\mathbb{F}[x_1,\ldots,x_n]$, then I:J is the set

$$\{f \in \mathbb{F}[x_1,\ldots,x_n] \mid f \cdot g \in I, \forall g \in J\}$$

and is called the **ideal quotient** of I by J.

Theorem

Let I, J be ideals with vanishing polynomials over $\mathbb{F}_{2^k}[x_1,\ldots,x_n]$, then

$$\mathbf{V}(I:J) = \mathbf{V}(I) - \mathbf{V}(J)$$

Our proposed algorithm of BFS traversal based on algebraic geometry

ALGORITHM 4: Algebraic Geometry based Traversal Algorithm

```
Input: Input-output circuit characteristic polynomial ideal J_{ckt}, initial state polynomial \mathcal{F}(S)

1 from^0 = reached = \mathcal{F}(S);

2 repeat

3 i \leftarrow i+1;

4 to^i \leftarrow \mathsf{GB} \ \mathsf{w/elimination} \ \mathsf{term} \ \mathsf{order} \langle J_{ckt}, J_0, from^{i-1} \rangle;

5 new^i \leftarrow \mathsf{generator} \ \mathsf{of} \ \langle to^i \rangle + (\langle T^4 - T \rangle : \langle reached \rangle);

6 reached \leftarrow \mathsf{generator} \ \mathsf{of} \ \langle reached \rangle \cdot \langle new^i \rangle;

7 from^i \leftarrow new^i (S \setminus T);

8 until \ new^i == 1;

9 return \ reached
```

```
▶ Go to example page 2
```

▶ Go to example page 3

Full Blown traversal of example circuit using algebraic geometry

- Initial state $from^0 = S(\{00\})$
- **Iteration 1:**Compose an elimination ideal *J*

$$f_1: t_0 - (xs_0s_1 + xs_0 + xs_1 + x + s_0 + s_1 + 1)$$

$$f_2: t_1 - (xs_0 + x + s_0s_1 + s_0)$$

$$f_3: S - s_0 - s_1$$

$$f_4: T - t_0 - t_1$$

$$J_{ckt} = \langle f_1, f_2, f_3, f_4 \rangle$$

$$f_5: x^2 - x$$

$$f_6: s_0^2 - s_0, f_7: s_1^2 - s_1$$

$$f_8: t_0^2 - t_0, f_9: t_1^2 - t_1$$

$$f_{10}: S^4 - S, f_{11}: T^4 - T$$

$$J_0 = \langle f_5, f_6, \dots, f_{11} \rangle$$

Elimination term order:

$$\{x, s_0, s_1, t_0, t_1\}$$
 (all bits) $> S$ (PS word) $> T$ (NS word)

- Compute the reduced GB for $J = J_{ckt} + J_0 + \langle from^0 \rangle$
- Next state

$$to^{1} = \langle T^{2} + (\alpha + 1)T + \alpha \rangle (\{01, 10\})$$

Complement of formerly reached state:

$$\langle T^4 - T \rangle : \langle T \rangle = \langle T^3 + 1 \rangle (\{01, 10, 11\})$$

Newly reached state:

$$\langle T^3 + 1, T^2 + (\alpha + 1)T + \alpha \rangle = \langle T^2 + (\alpha + 1)T + \alpha \rangle$$

• Update current reached states

$$reach = \langle T \cdot T^2 + (\alpha + 1)T + \alpha \rangle = \langle T^3 + (\alpha + 1)T^2 + \alpha T \rangle (\{00, 01, 10\})$$

• Update the present states for next iteration

$$from^1 = \langle S^2 + (\alpha + 1)S + \alpha \rangle$$

Full Blown traversal of example circuit using algebraic geometry(3)

- Iteration 2:
 - Next state: $to^2 = \langle T^2 + \alpha T \rangle$ ({00, 10})
 - The complement of reached:

$$\langle T^4 - T \rangle : \langle T^3 + (\alpha + 1)T^2 + \alpha T \rangle = \langle T + 1 + \alpha \rangle (\{11\})$$

Newly reached state:

$$\langle T^2 + \alpha T, T + 1 + \alpha \rangle = \langle \mathbf{1} \rangle$$

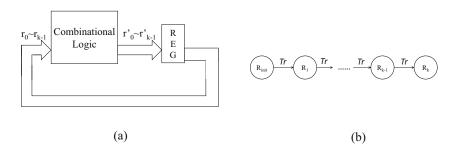
- Algorithm terminates
- Return value (final reachable states):

reached =
$$\langle T^3 + (\alpha + 1)T^2 + \alpha T \rangle$$

Outline

- Introduction
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Application on implicit unrolling



- State enumeration cannot address sequential arithmetic circuits verification
 - Need to consider various initial states
 - Exact reached states after k clock cycles directly implies desired arithmetic function
- State transitions on simplified model

$$R_k = Tr(R_{k-1}) = Tr(Tr(\cdots Tr(R_{init})\cdots)) = Tr^k(R_{init})$$

Verification of a sequential Galois field multiplier (Normal basis)

SPEC: $R = A_{init} \cdot B_{init} \pmod{P(\alpha)}$ after k clock cycles

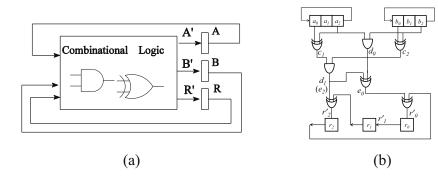
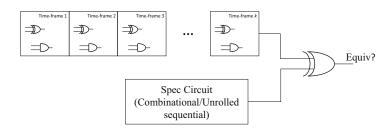


Figure: A 3-bit RH-SMPO and its Moore FSM model

Our approach vs Conventional approach



- Conventional: explicitly unroll k time-frames
 - Bit-blasting!
- New: Implicitly unroll
 - Keep only k polynomials $(R = \mathcal{F}(A, B))$ when unrolling
 - $R_1 = \mathcal{F}(A_{init}, B_{init}), R_2 = \mathcal{F}(A_1, B_1) = \mathcal{F}^2(A_{init}, B_{init}), \cdots, R_k = \mathcal{F}^k(A_{init}, B_{init}) = A_{init} \cdot B_{init}$

Experiment results

Table: Run-time (seconds) for verification of bug-free and buggy RH-SMPO using our approach

Operand size k	33	51	65	81	89	99
#variables	4785	11424	18265	28512	34354	42372
#polynomials	3630	8721	13910	21789	26255	32373
#terms	13629	32793	52845	82539	99591	122958
Runtime(bug-free)	112.6	1129	5243	20724	36096	67021
Runtime(buggy)	112.7	1129	5256	20684	36120	66929

^{*} Results from X. Sun, et al. "Formal Verification of Sequential Galois Field Arithmetic Circuits using Algebraic Geometry", to be presented in Grenoble, DATE'15

What I have done for this experiment

- Sequential arithmetic circuits over normal basis
- Optimal normal basis
- Design circuit(multiplier) over optimal normal basis
- Verify the function of the circuit

► No more details!

Normal basis representation

- Normal basis representation: $A(a_0, \ldots, a_{k-1}) = \sum_{i=0}^{k-1} a_{n(i)} \beta^{2^i}$
- Normal element: $\beta = \alpha^t$
- Squaring of elements represented in normal bases can be implemented simply by a cyclic right-shift operation.

Example

For $a, b \in \mathbb{F}_{2^k}$, $(a+b)^2 = a^2 + b^2$. Applying this rule for element squaring:

$$B = (b_0\beta + b_1\beta^2 + b_2\beta^4 + \dots + b_{k-1}\beta^{2^{k-1}})$$

$$B^2 = b_0^2\beta^2 + b_1^2\beta^4 + b_2^2\beta^8 + \dots + b_{k-1}^2\beta^{2^k}$$

$$= b_{k-1}\beta + b_0\beta^2 + b_1\beta^4 + \dots + b_{k-2}\beta^{2^{k-1}}$$

as $\beta^{2^k} = \beta$ by applying Fermat's little theorem to \mathbb{F}_{2^k} , and $b_i^2 = b_i$.

Galois field multiplication with normal basis

• Let $R=\sum_{i=0}^{k-1}r_i\beta^{2^i},\ A=\sum_{i=0}^{k-1}a_i\beta^{2^i},\ B=\sum_{i=0}^{k-1}b_i\beta^{2^i}$, then

$$R = A \cdot B = (\sum_{i=0}^{k-1} a_i \beta^{2^i}) (\sum_{j=0}^{k-1} b_j \beta^{2^j}) = \sum_{i=0}^{k-1} \sum_{j=0}^{k-1} a_i b_j \beta^{2^i} \beta^{2^j}$$

 \bullet Expressions $\beta^{2^j}\beta^{2^j}$ are called cross-product terms. Their normal basis representations are:

$$\beta^{2^{i}}\beta^{2^{j}} = \sum_{n=0}^{k-1} \lambda_{ij}^{(n)}\beta^{2^{n}}, \quad \lambda_{ij}^{(n)} \in \mathbb{F}_{2}.$$

Galois field multiplication with normal basis(2)

• The expression for the n^{th} digit of product $R = (r_0, \dots, r_n, \dots r_{k-1})$ is:

$$r_n = \sum_{i=0}^{k-1} \sum_{j=0}^{k-1} \lambda_{ij}^{(n)} a_i b_j = A \cdot M_n \cdot B^T, \quad 0 \le n \le k-1$$

- $M_n = (\lambda_{ij}^{(n)})$ is a binary $k \times k$ matrix over \mathbb{F}_2 , and it is called the λ -matrix.
- λ -matrix is unique when k and β are given!

Modified algorithm to verify the function of sequential GF multipliers

ALGORITHM 5: Abstraction via implicit unrolling for Sequential GF circuit verification

```
Input: Circuit polynomial ideal J, vanishing ideal J_0, initial state ideal R(=0), \mathcal{G}(A_{init}), \mathcal{H}(B_{init})

1 from_0(R,A,B) = \langle R,\mathcal{G}(A_{init}), \mathcal{H}(B_{init}) \rangle;

2 i=0;

3 repeat

4 i \leftarrow i+1;

5 G \leftarrow GB(\langle J+J_0+from_{i-1}(R,A,B) \rangle) with ATO;

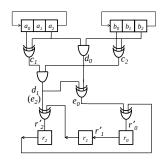
6 to_i(R',A',B') \leftarrow G \cap \mathbb{F}_{2^k}[R',A',B',R,A,B];

7 from_i \leftarrow to_i(\{R,A,B\} \setminus \{R',A',B'\});

8 until\ i==k;

9 return\ from_k(R_{final})
```

Experiment on 3-bit RH-SMPO



• The elimination ideal (first iteration):

$$\begin{split} J = & d_0 + b_2 \cdot a_2, c_1 + a_0 + a_2, c_2 + b_0 + b_2, d_1 + c_1 \cdot c_2, \\ e_0 + d_0 + d_1, e_2 + d_1, r'_0 + r_2 + e_0, r'_1 + r_0, r'_2 + r_1 + e_2, \\ A + & a_0 \alpha^3 + a_1 \alpha^6 + a_2 \alpha^{12}, B + b_0 \alpha^3 + b_1 \alpha^6 + b_2 \alpha^{12}, \\ R + & r_0 \alpha^3 + r_1 \alpha^6 + r_2 \alpha^{12}, R' + r'_0 \alpha^3 + r'_1 \alpha^6 + r'_2 \alpha^{12}; \end{split}$$

Experiment on 3-bit RH-SMPO(2)

- " J_0 " is the ideal of vanishing polynomials in all bit-level variables (e.g. $a_0^2 a_0$) and word-level variables (e.g. $A^8 A$).
- $from_0 = \{R, A_{init} + a_0\alpha^3 + a_1\alpha^6 + a_2\alpha^{12}, B_{init} + b_0\alpha^3 + b_1\alpha^6 + b_2\alpha^{12}\}$
- $to_1: R' + (\alpha^2)A_{init}^4B_{init}^4 + (\alpha^2 + \alpha)A_{init}^4B_{init}^2 + (\alpha^2 + \alpha)A_{init}^4B_{init} + (\alpha^2 + \alpha)A_{init}^2B_{init}^4 + (\alpha^2 + \alpha + 1)A_{init}^2B_{init}^2 + (\alpha^2)A_{init}^2B_{init} + (\alpha^2 + \alpha)A_{init}B_{init}^4 + (\alpha^2)A_{init}B_{init}^2$
- from₁ = $\{R' + (\alpha^2)A_{init}^4B_{init}^4 + (\alpha^2 + \alpha)A_{init}^4B_{init}^2 + (\alpha^2 + \alpha)A_{init}^4B_{init} + (\alpha^2 + \alpha)A_{init}^4B_{init} + (\alpha^2 + \alpha)A_{init}^4B_{init}^4 + (\alpha^2 + \alpha + 1)A_{init}^2B_{init}^2 + (\alpha^2)A_{init}^2B_{init} + (\alpha^2 + \alpha)A_{init}B_{init}^4 + (\alpha^2)A_{init}B_{init}^2, A_{init} + a_2\alpha^3 + a_0\alpha^6 + a_1\alpha^{12}, B_{init} + b_2\alpha^3 + b_0\alpha^6 + b_1\alpha^{12} \}$
- • •
- After 3 iterations: $to_3 = \{R' + A_{init}B_{init}, A_{init} + a'_0\alpha^3 + a'_1\alpha^6 + a'_2\alpha^{12}, B_{init} + b'_0\alpha^3 + b'_1\alpha^6 + b'_2\alpha^{12}\}$

Outline

- Introduction
- Sequential circuit verification requires reachability analysis
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An example of abstraction refinement algorithm

ALGORITHM 6: k-BMC with Abstraction Refinement (L. Zhang Thesis)

```
Input: M is the original machine, p is the property to check, k is the number of
         steps in k-BMC
1 k = InitValue:
<sub>2</sub> if k-BMC(M, p, k) is SAT then
      return "Found error trace"
4 else
      Extract UNSAT proof \mathcal{P} of k-BMC;
      M' = ABSTRACT(M, \mathcal{P});
7 end
8 if MODEL-CHECK(M', p) returns PASS then
      return "Passing property"
10 else
      Increase bound k;
      goto Line 2;
13 end
```

11

12

An example of abstraction refinement algorithm(2)

 k-BMC: unroll the machine for k times, represent reachable states with CNF formula to check property p

$$I(s_0) \wedge \bigwedge_{i=0}^{k-1} T(s_i, s_{i+1}) \wedge \neg p$$

- UNSAT core: a subset of clauses that is still UNSAT
- State variables not in UNSAT core: no matter what their values are,
 p will NOT be violated
- In abstracted model, ignore these "irrelevant" variables

An example of abstraction refinement algorithm(3)

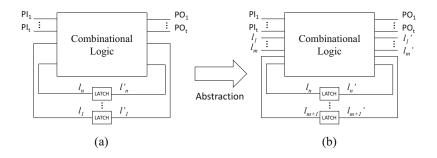


Figure: Abstraction by reducing latches

- Remove "irrelevant" latches, reduce state space
- Provide an over-approximation
- This algorithm requires UNSAT core extraction

Buchberger's Algorithm Computes a Gröbner Basis

Buchberger's Algorithm

```
INPUT : F = \{f_1, \dots, f_s\}

OUTPUT : G = \{g_1, \dots, g_t\}

G := F;

REPEAT

G' := G

For each pair \{f, g\}, f \neq g in G' DO

S(f, g) \xrightarrow{G'}_{+} r

IF r \neq 0 THEN G := G \cup \{r\}

UNTIL G = G'
```

- S-poly: $S(f,g) = \frac{L}{lt(f)} \cdot f \frac{L}{lt(g)} \cdot g$ L = LCM(lm(f), lm(g)), lm(f): leading monomial of f
- Multivariate division: $f \stackrel{g}{\longrightarrow} r$: $Im(g)|Im(f) \rightarrow r = f \frac{It(f)}{It(g)}g$

Observation when executing Buchberger's algorithm

ullet Translate CNF clauses to polynomials in \mathbb{F}_2

$$c_1: \overline{a} \vee \overline{b}$$
 $c_2: a \vee \overline{b}$
 $c_3: \overline{a} \vee b$
 $c_4: a \vee b$
 $c_5: x \vee y$
 $c_6: y \vee z$
 $c_7: b \vee \neg y$

$$f_1: ab$$

 $f_2: ab + a$
 $f_3: ab + b$
 $f_4: ab + a + b + 1$
 $f_5: xy + y + x + 1$
 $f_6: yz + y + z + 1$
 $f_7: by + y$
 $f_8: axz + az + xz + z$

 $c_8: a \lor x \lor \neg z$

Observation when executing Buchberger's algorithm(2)

- Compute a GB using Buchberger's algorithm
- Term ordering: a > b > x > y > z
- Compute $Spoly(f_1, f_2) \xrightarrow{F}_+ r_1 = a$
 - 2 Update $F = F \cup r_1$
 - **3** Compute $Spoly(f_1, f_3) \xrightarrow{F}_+ r_2 = b$

 - lacktriangle Use a directed acyclic graph (DAG) to represent the process to get r_1, r_2
 - **1** Compute $Spoly(f_1, f_4) = s_3 = a + b + 1$, a + b + 1 can be reduced r_1 , the intermediate remainder $r_3 = b + 1$. Then divided by r_2 , and the remainder is "1", terminate the Buchberger's algorithm
 - ② Draw a DAG depicting the process through which we obtain remainder "1". From leaf "1" we backtrace the graph to roots f_1 , f_2 , f_3 , f_4 .

Observation when executing Buchberger's algorithm(3)

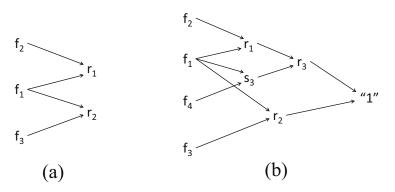


Figure: DAG representing Spoly computations and multivariate divisions

Observation when executing Buchberger's algorithm(3)

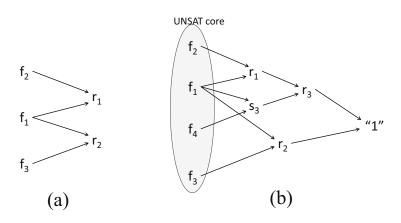


Figure: DAG representing Spoly computations and multivariate divisions

Proposed algorithm to extract UNSAT core

ALGORITHM 7: Extract UNSAT core using a variation of Buchberger's algorithm

```
Input: A set of polynomials F = \{f_1, f_2, \dots, f_s\}
Output: An UNSAT core \{f_{m_1}, f_{m_2}, \dots, f_{m_t}\}
1 repeat
```

Pick a pair $f_i, f_i \in F$ that has never been computed S-poly;

if
$$Spoly(f_i, f_j) \xrightarrow{F}_+ r_l \neq 0$$
 then

 $F = F \cup r_I$;

Create a DAG G_l with f_i, f_j as roots, r_l as leaf, recording the S-poly, all intermediate remainders and $f_k \in F$ that cancel monomial terms in the S-poly;

end end

7 **until** $r_l == 1$;

- 8 Backward traverse the DAG for remainder "1", replace r_l with corresponding DAG G_l ;
- 9 return All roots

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Objectives¹

- Explore an implementation of algebraic geometry based reachability analysis
- CAD tool design
 - \bullet SINGULAR's data structure not optimized \to design a standalone CAD tool implemented in C++
 - Specifically aims to solve word-level sequential verification problems
 - Lower complexity: borrow techniques from [T. Pruss, Abstraction, DAC'14], [J. Lv, Equivalence, TCAD'13] and [C. Eder, F-4 reduction, ISSAC'11]
- Fine-tune the tool for sequential GF arithmetic circuits verification
- Explore a new abstraction-refinement paradigm based on information from UNSAT cores

Time table

- Current status: Experiments have been performed to run implicit state enumeration on sequential circuits benchmarks such as ISCAS'89 circuits. Current problem is the algorithm spends too much time on multivariate division procedure;
- Spring 2015: Implement the tool which can efficiently do multivariate division and test the performance of our tool based on [Tim, Abstraction, DAC'14] approach;
- Summer 2015: Integrate the refined multivariate division routine into our verification tool, test its performance on circuits with various sizes;
- Fall 2015: Evaluate data and write the dissertation.