

All programs have already been compiled.

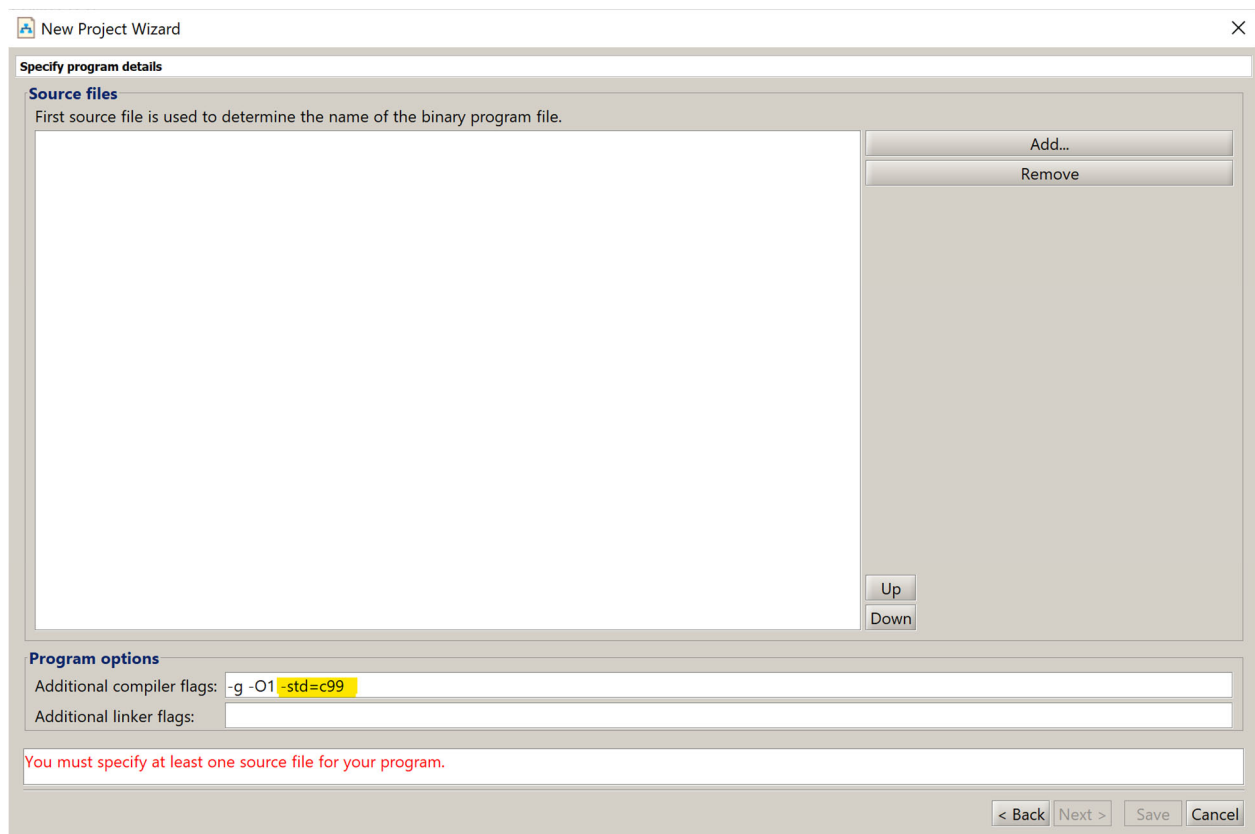
Simply open the monitor program and select file> open project
then navigate to the desired project folder and choose the .amp file

Notes for new projects

When creating a new project there are two changes that need to be made

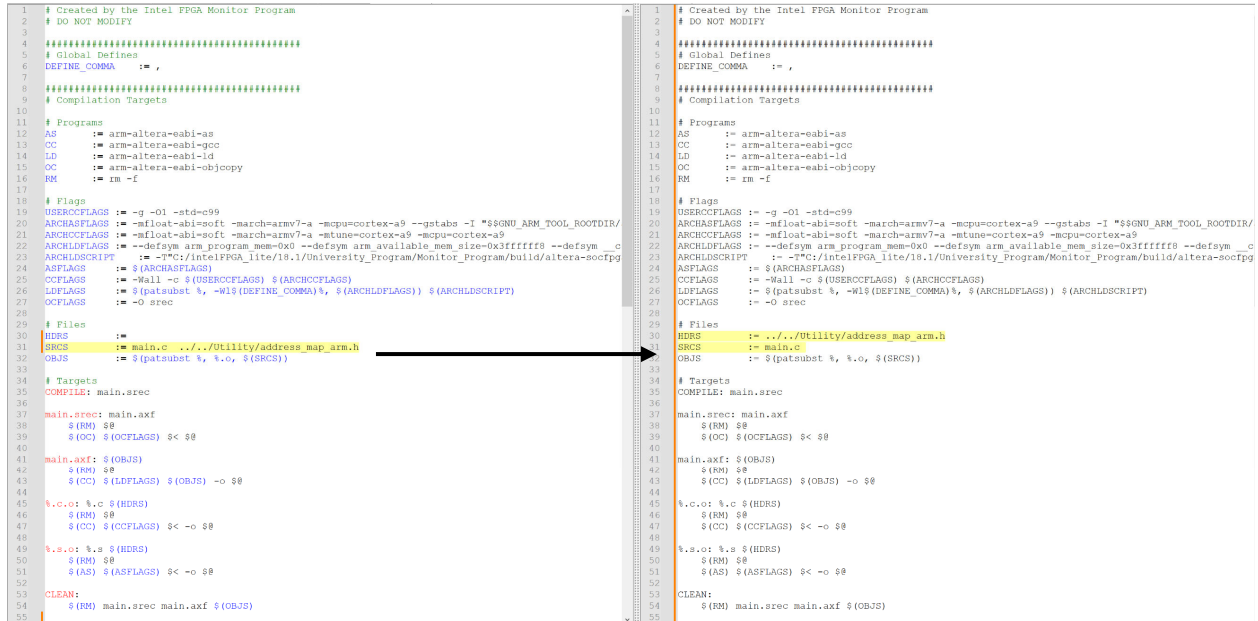
1.

In the source files window, add the compiler flag -std=c99 for compatibility



2.

After attempting to compile the project a makefile will be made. Edit the file with notepad to move any header files to the correct line. Additionally, an existing makefile can be copy and paste into another project directory if the projects use the same files.



```
1 # Created by the Intel FPGA Monitor Program
2 # DO NOT MODIFY
3
4 #####
5 # Global Defines
6 DEFINE_COMMA := ,
7
8 #####
9 # Compilation Targets
10
11 # Programs
12 AS := arm-altera-eabi-as
13 CC := arm-altera-eabi-gcc
14 LD := arm-altera-eabi-ld
15 OC := arm-altera-eabi-objcopy
16 RM := rm -f
17
18 # Flags
19 USERCFLAGS := -g -O1 -std=c99
20 ARCHASFLAGS := -mfloat-abi=soft -march=armv7-a -mcpu=cortex-a9 --getabi -I "${GNU_ARM_TOOL_ROOTDIR}/
21 ARCHCFLAGS := -mfloat-abi=soft -march=armv7-a -mtune=cortex-a9 -mcpu=cortex-a9
22 ARCHLDFLAGS := --defsym arm_program_mem=0x0 --defsym arm_available_mem_size=0x3fffffff --defsym _c
23 ARCHLDSOFT := -T"C:/intelFPGA_lite/18.1/University_Program/Monitor_Program/build/altera-soctpg
24 ASFLAGS := $(ARCHASFLAGS)
25 CFLAGS := -Wall -c $(USERCFLAGS) $(ARCHCFLAGS)
26 LDFLAGS := $(patsubst %, -Wl$(DEFINE_COMMA)%, $(ARCHLDFLAGS)) $(ARCHLDSOFT)
27 OCLFLAGS := -O srec
28
29 # Files
30 HDRS :=
31 SRCS := main.c ../Utility/address_map_arm.h
32 OBJS := $(patsubst %, %.o, $(SRCS))
33
34 # Targets
35 COMPILER: main.srec
36
37 main.srec: main.axf
38 $(RM) $@
39 $(CC) $(OCLFLAGS) $< $@
40
41 main.axf: $(OBJS)
42 $(RM) $@
43 $(CC) $(LDFLAGS) $(OBJS) -o $@
44
45 %.c.o: %.c $(HDRS)
46 $(RM) $@
47 $(CC) $(CFLAGS) $< -o $@
48
49 %.s.o: %.s $(HDRS)
50 $(RM) $@
51 $(AS) $(ASFLAGS) $< -o $@
52
53 CLEAN:
54 $(RM) main.srec main.axf $(OBJS)
55
```

```
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