



6-CHANNEL ELECTRONIC VOLUME

■ GENERAL DESCRIPTION

The **NJW1150** is a 6-CHANNEL ELECTRONIC VOLUME, which also includes tone control, balance and trim level control. The **NJW1150** is suitable for multi-channel audio system, such as AV amplifier, mini stereo component, speaker system, and others.

All of internal status and variables are controlled by I²C BUS interface.

■ PACKAGE OUTLINE



NJW1150M

■ FEATURES

Operating Voltage

Analog Supply ± 4.5 to ± 7.5 V Digital Supply +4.5 to ± 5.5 V

I²C BUS Interface

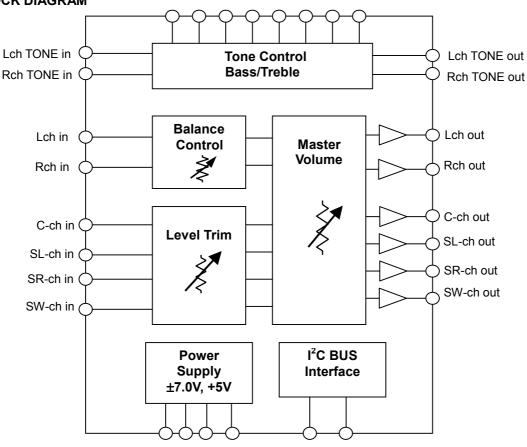
● 6-Chnnel Master Volume
 ● Balance control for L, R-ch
 0 to -79dB, MUTE
 0 to -30dB, MUTE

Trim Level Control for C, SL, SR, SW-ch
 Independent Tone Control (Bass, Treble)
 for L, R-ch

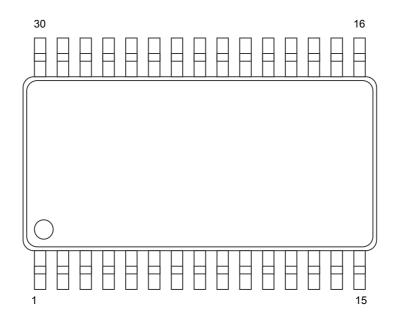
Bi-CMOS Technology

Package OutlineSDMP30

■ BLOCK DIAGRAM



■ PIN FUNCTION



No.	SYMBOL	FUNCTION	No.	SYMBOL	FUNCTION
1	GND	Ground	16	SCL	I ² C bus clock input
2	V+	Positive power supply voltage	17	SDA	I ² C bus data input
3	VDD	Power supply voltage (Digital)	18	SWOUT	Volume Sub Woofer channel output
4	RTIN	Tone control Right channel input	19	SROUT	Volume Surround Right channel output
5	RTOUT	Tone control Right channel output	20	SLOUT	Volume Surround Left channel volume output
6	RTC	Tone control Right channel Treble filter capacitor	21	COUT	Volume Center channel output
7	RBC1	Tone control Right channel Bass filter capacitor	22	ROUT	Volume Right channel output
8	RBC2	Tone control Right channel Bass filter capacitor	23	LOUT	Volume Left channel output
9	RBC3	Tone control Right channel Bass DC cut capacitor	24	LBC3	Tone control Left channel Bass DC cut capacitor
10	LIN	Volume Left channel input	25	LBC2	Tone control Left channel Bass filter capacitor
11	RIN	Volume Right channel input	26	LBC1	Tone control Left channel Bass filter capacitor
12	CIN	Volume Center channel input	27	LTC	Tone control Left channel Treble filter capacitor
13	SLIN	Volume Surround Left channel input	28	LTOUT	Tone control Left channel output
14	SRIN	Volume Surround Right channel input	29	LTIN	Tone control Left channel input
15	SWIN	Volume Sub Woofer channel input	30	V-	Negative power supply voltage (IC substrate)

■ ABSOLUTE MAXIMUM RATING (Ta=25°C)

PARAMETER	SYMBOL	RATING	UNIT
Supply Voltage 1	V ⁺ /V ⁻	+7.5/-7.5	V
Supply Voltage 2	V_{DD}	7.0	V
Maximum Input Voltage	V _{IM}	V ⁺ /V ⁻ (*)	V
Power Dissipation	P_D	700	mW
Operating Temperature Range	Topr	-20 to +85	°C
Storage Temperature Range	Tstg	-40 to +125	°C

(*) For the maximum input voltage less than V⁺/V

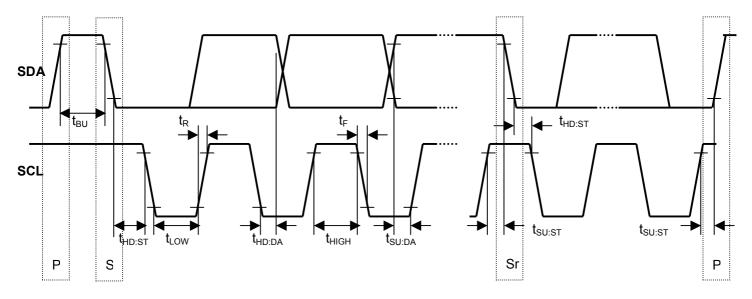
■ ELECTRICAL CHARACTERISTICS (Ta=25°C,V*/V=±7V, V_{DD}=5V)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply	•			•		
Operating Voltage 1	V ⁺ /V ⁻		4.5	7.0	7.5	V
Operating Voltage 2	V_{DD}		4.5	5.0	5.5	V
Supply Current 1	I _{CC}	No Signal	-	8	14	mA
Supply Current 2	I _{EE}	No Signal	-	8	14	mA
Supply Current 3	I _{DD}	No Signal	-	10	100	μA
Input/Output Characteristics	S					
Voltage Gain	G _V	V _{IN} =1Vrms, f=1kHz Master=0dB, Balance=0dB Trimmer=0dB	-0.5	0	0.5	dB
Voltage Gain Error	ΔG_V	V _{IN} =1Vrms, f=1kHz Master=0dB	-0.5	0	0.5	dB
Maximum Attenuation 1	A _{TT1}	f=1KHz, V _{IN} =1Vrms Master=-79dB	-	-79	-	dB
Maximum Attenuation 2	A _{TT2}	f=1KHz, V _{IN} =1Vrms Mute	-	-90	-	dB
Attenuation Error	ΔΑ _{ΤΤ}	f=1KHz, V _{IN} =1Vrms Master=-50dB Trimmer=-10dB	-1	0	1	dB
Maximum Output Voltage	V _{OM}	f=1KHz,THD=1% Master=0dB	3.0	4.0	-	Vrms
Output Noise	V _{NO}	Master=0dB, Rg=0,A-weight	-	-110 (3.2μ)	-100 (10μ)	dBV (Vrms)
Total Harmonic Distortion	T.H.D	f=1KHz,Vo=1Vrms, Master=0dB,Trimmer=0dB	-	0.005	0.05	%
Channel Separation	CS	f=1KHz,Vo=1Vrms Master=0dB,A-weight	-	-90	-80	dB
Tone Control Characteristic	s					
Treble Boost Gain1	HF _{BST}	Vo=1Vrms f=10KHz Treble=10dB	8	10	12	dB
Treble Boost Gain2	HF _{CUT}	Vo=1Vrms f=10KHz Treble=-10dB -1		-10	-8	dB
Bass Boost Gain1	LF _{BST}	Vo=1Vrms f=50Hz Bass=10dB	8	10	12	dB
Bass Boost Gain2	LF _{CUT}	Vo=1Vrms f=50Hz Bass=-10dB	-12	-10	-8	dB

■ I²C BUS BLOCK CHARACTERISTICS (SDA, SCL)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
High Level Input Voltage	V _{IH}	3.0	-	5.0	V
Low Level Input Voltage	V _{IL}	0	-	1.5	V
High Level Input Current	I _{IH}	-	-	10	μΑ
Low Level Input Current	I _{IL}	-	-	10	μA
Low Level Output Voltage (3mA at SDA pin)	V _{OL}	0	-	0.4	V
Maximum Output Current	I _{OL}	-3.0	-	-	mA
Maximum Clock Frequency	f _{SCL}	0	-	100	kHz
Data Change Minimum Waiting Time	t _{BUF}	4.7	-	_	μs
Data Transfer Start Minimum Waiting Time	t _{HD:STA}	4.0	-	-	μs
Low Level Clock Pulse Width	t _{LOW}	4.7	-	-	μs
High Level Clock Pulse Width	T _{HIGH}	4.0	-	_	μs
Minimum Start Preparation Waiting Time	t _{SU:STA}	4.7	-	-	μs
Minimum Data Hold Time	t _{HD:DAT}	5.0	-	-	μs
Minimum Data Preparation Time	t _{SU:DAT}	250	-	_	ns
Rise Time	t _R	-	-	1.0	μs
Fall Time	t _F	-	-	300	ns
Minimum Stop Preparation Waiting Time	t _{su:sto}	4.7	-	-	μs

 $\mbox{I}^2\mbox{C BUS Load Condition: Pull up resistance } 4k\Omega$ (Connected to +5V) Load capacitance 200pF (Connected to GND)



■ TERMINAL DESCRIPTION

PIN NO.	AL DESCRIPT	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
1	GND	Ground	V+ V+ V+ V- (SUB)	0
2 3	V+ VDD	Positive power supply voltage Power supply voltage (Digital)	V- (SUB)	V ⁺ VDD
4 29	RTIN LTIN	Tone control Right channel input Left channel input	300 S 50K S GND	0
5 28	RTOUT LTOUT	Tone control Right channel output Left channel output	₩ 10K V- (SUB)	0

NJW1150

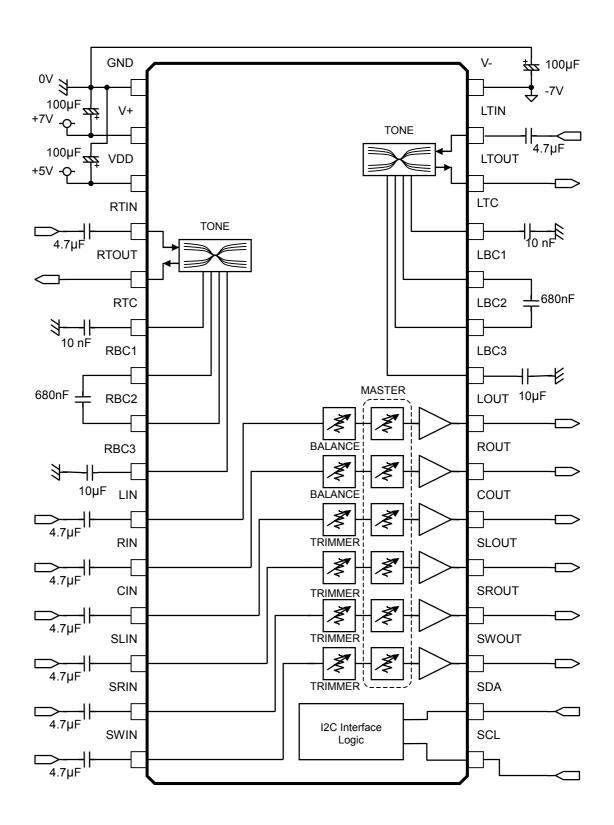
■ TERMINAL DESCRIPTION

PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
6 27	RTC LTC	Tone control Right channel Treble filter capacitor Left channel Treble filter capacitor	30 4K V- (SUB)	0
7 26	RBC1 LBC1	Tone control Right channel Bass filter capacitor Left channel Bass filter capacitor	V- (SUB)	0
8 25	RBC2 LBC2	Tone control Right channel Bass filter capacitor Left channel Bass filter capacitor	30 2K W V- (SUB) 2K W	0
9 24	RBC3 LBC3	Tone control Right channel Bass DC cut capacitor Left channel Bass DC cut capacitor	2K 30 V- (SUB)	0

■ TERMINAL DESCRIPTION

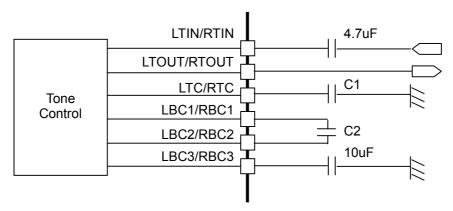
PIN NO.	SYMBOL	FUNCTION	EQUIVALENT CIRCUIT	TERMINAL VOLTAGE
10 11 12 13 14 15	LIN RIN CIN SLIN SRIN SWIN	Volume Left channel input Right channel input Center channel input Surround Left channel input Surround Right channel input Sub Woofer channel input	300 50K V- (SUB)	0
16	SCL	I ² C bus clock input	VDD Rp 300 V- (SUB)	5
17	SDA	I ² C bus data input	VDD Rp 300 W 10K 10K GND	5
18 19 20 21 22 23	SWOUT SROUT SLOUT COUT ROUT LOUT	Volume Left channel output Right channel output Center channel output Surround Left channel output Surround Right channel output Sub Woofer channel output	300 300 V- (SUB)	0

■APPLICATION CIRCUIT



■ Definition of cut-off frequency for Tone Control

Cut-off frequency for Tone Control is adjustable with changing the capacitor C1, C2 in below circuit. See the next function for the cut-off frequency.



1) TREBLE =+/-10dB

$$fc = \frac{39.2 * 10^{-6}}{C1}$$

$$fc = \frac{161.7 * 10^{-6}}{C2}$$

■ DEFINITION OF I²C REGISTER

• I²C BUS FORMAT

S: Starting Term A: Acknowledge Bit P: Ending Term

		MSB LSB		MSB LSB		MSB LS	В		
	S	Slave Address	Α	Select Address	Α	Data	Α	Р	
,	1bit	8bit	1bit	8bit	1bit	8bit	1bit	1bit	

SLAVE ADDRESS

MSB							TESB	
1	0	0	0	1	0	0	R/W	l

R/W=0: Receive Only R/W=1: No Output Data

• CONTROL REGISTER TABLE

Select	BIT									
Address	D7	D6	D5	D4	D3	D2	D1	D0		
00H	*			M	aster Volum	ie				
01H		*			Left	channel Ba	ance			
02H		*		Right channel Balance						
03H		*		Center Channel Trimmer						
04H		*		SL Channel Trimmer						
05H		*		SR Channel Trimmer						
06H		*		SW Channel Trimmer						
07H		Tone	(Treble)	Tone (Bass)						
08H	,	*		MUTE						

^{* :} Don't care.

On Power up, the master volume mute is activated.

• CONTROL REGISTER DEFAULT VALUE

Select	BIT										
Address	D7	D6	D5	D4	D3	D2	D1	D0			
00H	0	1	0	1	0	0	0	0			
01H	0	0	0	0	0	0	0	0			
02H	0	0	0	0	0	0	0	0			
03H	0	0	0	0	1	0	1	0			
04H	0	0	0	0	1	0	1	0			
05H	0	0	0	0	1	0	1	0			
06H	0	0	0	0	1	0	1	0			
07H	0	0	0	0	0	0	0	0			
08H	0	0	0	0	0	0	0	0			

● CONTROL COMMAND TABLE

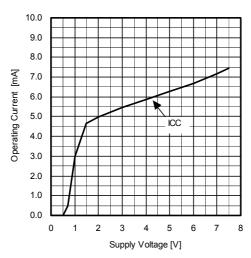
Select					IT				
Address	D7	D6	D5	D4	D3	D2	D1	D0	REMARKS
				1	VOL				VOL : Volume control for all channel (1dB/step)
		0	0	0	0	0	0	0	Ex.) "0000000"= 0dB
		0	0	0	0	0	0	1	"0000001"=-1dB
		0	0	0	0	0	1	0	"0000010"=- 2dB
00H	*								
									"1001111"=-79dB "1010000"=MUTE:Default Value
			0	0		4	4	4	Maximum Attenuation : Master Volume :-79dB
		1	0	0	1	1	1	1	Trimmer -20dB Minimum Attenuation : Master Volume 0dB
		1	0	1	0	0	0	0	Trimmer 0dB L-BAL : Balance control for Left channel (1dB/step)
						L-BAL			Ex.)
				0	0	0	0	0	"00000"=0dB "00001"=-1dB
01H				0	0	0	0	1	: :
•		*		0	0	0	1	0	: "11110"=-30dB
						:			"11111"=MUTE
				1	1	1	1	1	B BAL Bulletin (1971)
						R-BAL			R-BAL : Balance control for Right channel (1dB/step) Ex.)
				0	0	0	0	0	"00000"=0dB
02H				0	0	0	0	1	"00001"=-1dB :
0211		*		0	0	0	1	0	"44440"— 20dD
						:			"11110"=-30dB "11111"= MUTE
				1	1	1	1	1	
						C TRIM			C TRIM :Center Channel Trimmer Control
				0	0	0	0	0	Ex.) "00000"=0dB
03H		*		0	0	0	0	1	"00001"=-1dB
ОЗП				0	0	0	1	0	"00010"=-2dB
						:			:
				1	0	1	0	0	"10100"=-20dB "00001010"=-10dB Default Value
						SL TRIM			SL TRIM :SL Channel Trimmer Control
				0	0	0	0	0	Ex.) "00000"=0dB
0.41.1				0	0	0	0	1	"00000"=0dB "00001"=-1dB
04H		*		0	0	0	1	0	"00010"=-2dB
						:			: "40400" 00 ID "60004040" 40 ID D 5 I IVVI
				1	0	1	0	0	"10100"=-20dB "00001010"=-10dB Default Value
						SR TRIM			SR TRIM :SR Channel Trimmer Control
				0	0	0	0	0	Ex.)
				0	0	0	0	1	"00000"=0dB "00001"=-1dB
05H		*		0	0	0	1	0	"0001" =-1dB "00010" =-2dB
					-	:		-	:
				1	0	1	0	0	"10100"=-20dB "00001010"=-10dB Default Value
					-	SW TRIM			SW TRIM :SW Channel Trimmer Control
				0	0	0	0	0	Ex.)
				0	0	0	0	1	"00000"=0dB
06H		*		0	0	0	1	0	"00001"=-1dB "00010"=-2dB
					v			Ü	:
				1	0	1	0	0	"10100"=-20dB "00001010"=-10dB Default Value

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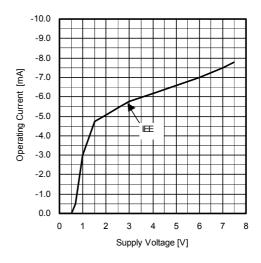
● CONTROL COMMAND TABLE

00111			111D 17						
Select	BIT								DEMARKO
Address	D7	D6	D5	D4	D3	D2	D1	D0	REMARKS
	TREBLE				BASS				TONE CONTROL
07H	1	1	0	1	1	1	0	1	Ex.) "11011101"=+10dB "11001100"=+8dB : "10001000"=0dB "00000000"=0dB :
	1	1	0	0	1	1	0	0	
					:				
	1	0	0	0	1	0	0	0	
	0	0	0	0	0	0	0	0	
					:				"01000111"=-8dB "01010101"=-10dB
	0	1	0	0	0	1	0	0	OTOTOTOT TOUB
	0	1	0	1	0	1	0	1	
08H					MUTE				MUTE CONTROL
			0 1						Ex.) "0"=OFF
		*							"1"=MUTE
									D5=L ch,D4=R ch,D3=C ch,D2=SL ch,D1=SR ch,D0=SW ch

Operating Current vs. Supply Voltage Ta=25°C

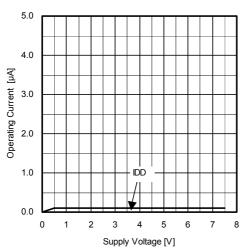


Operating Current vs. Supply Voltage Ta=25°C

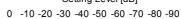


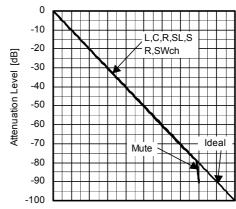
Operating Current vs. Supply Voltage

Ta=25°C



Attenuation Level vs. Setting Level Ta=25°C Setting Level [dB]

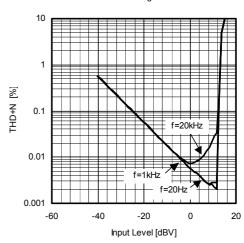


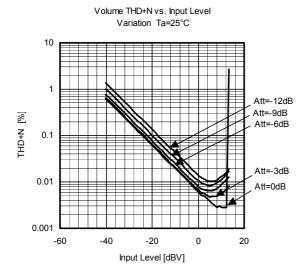


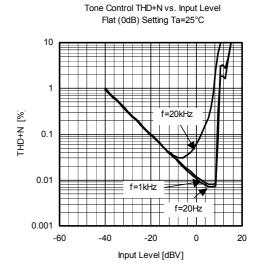
Tone Control Gain vs Frequency Ta=25°C

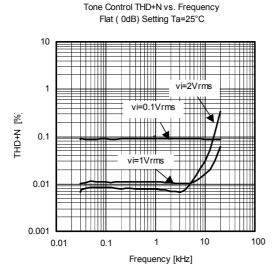
15 TREBLE 10 5 Gain [dB] 0dB 0 -2dB -5 -10 -10dB -15 0.01 0.1 10 100 Frequency [kHz]

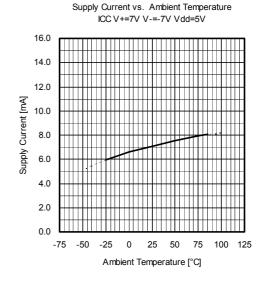
 $\label{eq:Volume THD+N vs. Input Level} Volume \ THD+N \ vs. \ Input \ Level$ 0dB Setting Ta=25°C

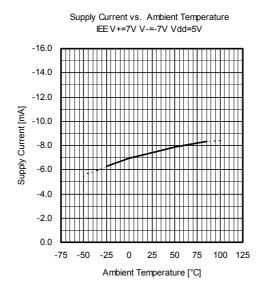


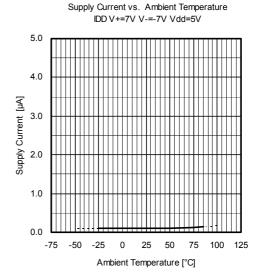




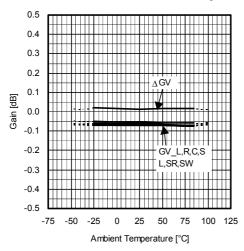




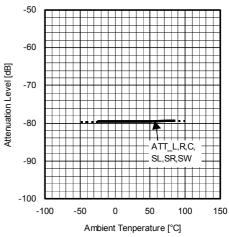




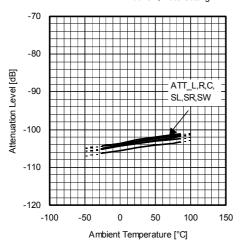
Volume Gain vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, 0dB Setting



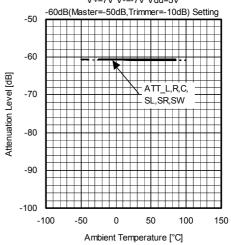
Volume Attenuation Level vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, -79dB Setting



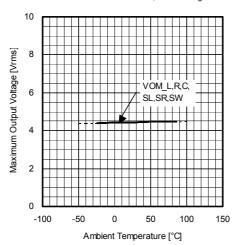
Volume Attenuation Level vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, Mute Setting



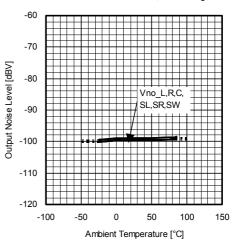
Volume Attenuation Level vs. Ambient Temperature V+=7V V-=-7V Vdd=5V



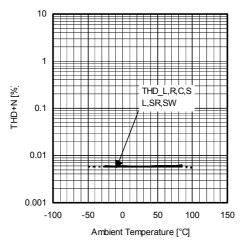
Volume Maximum Output Voltage vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, 0dB Setting



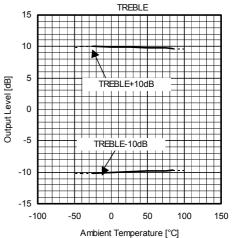
Volume Output Noise vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, 0dB Setting



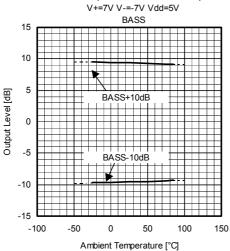
Volume THD+N vs. Ambient Temperature V+=7V V-=-7V Vdd=5V, 0dB Setting



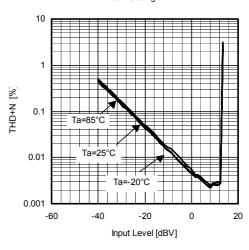




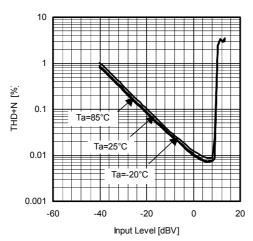
Tone Control Output Level vs. Ambient Temperature



Volume THD+N vs. Input Level 0dB Setting f=1kHz



Tone Control THD+N vs. Input Level Flat (0dB) Setting f=1kHz



■ NOTE

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Datasheets for electronics components.