First come First Serve (FCFS) CPU Schooling Algo with Example.

Browse No.	Arrival Time	Buyat Time Execution Tim	Completion	TAT C-A	WT TAT-5T	RT COU FURT-AT
P2 P3 P4	0 1 5 6	2 2 3 4	2 4 8 12	2 3 3 6	0 1 0 2	0-0 → 0 2-1 → 1 5-5 → 0 8-6 → 2.
	13.5 -55	useful fortal	Ot is the time when P, P2, P3, Py got executed completely.	Actually fortall	Actual apastil useful Time	

CPU sith idea here box here CPU has no process to execute.	FC FS, Criteria = "Arival Time" Mode = "Non-Perenptire".
Ganti chaet P1 P2 P3	P4 12
Hast with Time Zero (PL assived at Lero)	Lat 12 au the perocesses got executed.

Ang TAT =
$$\frac{14}{4}$$
 = $\frac{3}{4}$ = $\frac{3}{$

test Job First (CJF) Scheduling Algorithm with Example :-

Pricess No.	Assival Time	Bust	Completion	C-A	TAT-BT	CPUFUSE - A
Pa	1	3	6	5	2	3-102
Pa	2	4	10	8	4	6-2=4
3	1	2	3	2	0	1-1-00
Py !	.4	4	14	10	6	10-4-36

Ideal - Critera = Burst time Py Mode :- Non-premptive Ganta chart P3

Time on Non- Dre-emptive

First BT then AT then Processed No

Time First (STF with Preemption Shortest Remaining Mode or Pre-emptive Cesteria: - Burst time Algo:-Completion RT WT TAT Burst conforit-A Process Assival TAT-BT C-A Time 9-5=4 9-0=9 54 9 PI 3 - 3=0 371 4-1 = 3 4 11-4=7 P2 13-2-11 13 2 1-1=0 P3 4-4 =0 5

jard chart Pa at arrived

> your execute chesas Contain less Burst time

Terre

L-2.7 with Example:-Algo. Robin (RR) CPU Scheduling Completion TAT Burst RI WT Assival Peroross Time Tilma Timo CPUTITIST - AT TAT BT 12 0 12 42 10 11 20 Py Py Mode: - "Pre-emptive" Context Switching: - Lyne Seq. when one Processed at the Processed at the Processes in Ready give in Criteria: "Time - Quantum" Geven time quantum = 2. First check it any other process is needy to renter the Ready Gleene then after that add Depending upon the the process from Running to Ready due to Switch parrival time Gants Chart sequence matters here) the Scheduling 6CS 7 10 11 12 Context switching (No of Context Shritching = 6)

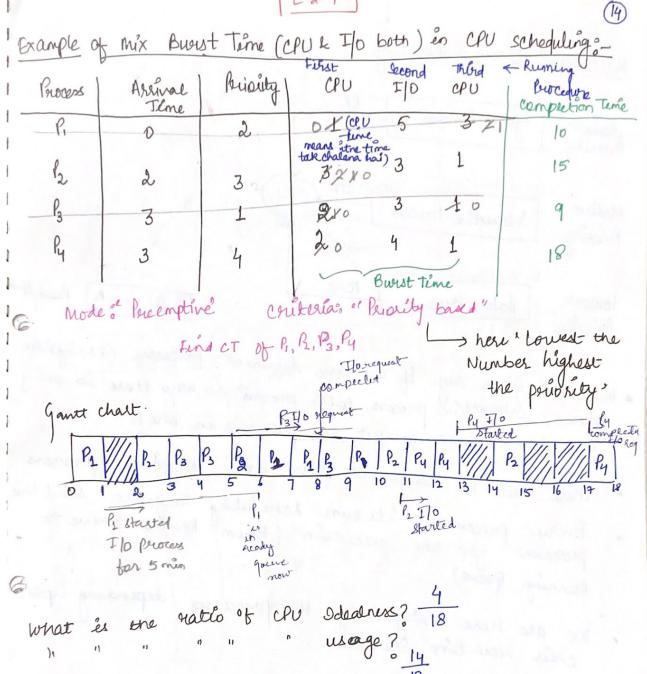
le happening here, ending box there no Lawrenge Switching was done

Bowe the running fewers Control block (contains out

process into Contains and Context Switching Not contikt save the running process and one info sulated to that particular Pick the new Parouss) . Advantage to sussume the Process) last one Brocers from the not to grestait it from PCB Starting gree were

pather

L-2.8 with Examples-De-emptive Pariority Scheduling Algo. WT TAT Completion' -(TAT-BT) Priority Processino Agrinal Busst CT-AT) Ti Me Time C 10 12 12 3 4320 7 P2 20 C 0 2 PB C 30 0 5 20 40 Mode & "Precomptive" Criteria: - Priority" garret chart Pzy P2 P3 P3 have Read at 3, we have no new porces. yes MA Now P3 agriver, neady Tohatover the at 2, 1, Bassin new we will in Ready grave and PI is already check the Critaria It in ourning 20 vitera 'es, sun the now we will process first who P2 check the is arewing at 10 20 certain Priordy 0. Pi have same palorety then process of two processes the region time Assival time and of for the process number.



Muti Revel Queve Scheduling ?-

Medium
Periority

Anteriority

Ex. Background processes

Poriority

Readyquees

Priority

System Process

Readyquees

Readyquees

Readyquees

Readyquees

Readyquees

Readyquees

Readyquees

Poriority

Readyquees

- Multi level queue says if we have different processes Lex. System process, interactive process, Batch process) so why there is only one ready queue to put the processes in one?
 - · there must be different preve for different process.
 - · Every process has Ests own Scheduling algo to Send-the processes for the execution (forom Ready Queue to Running Queue)
 - · We are here categorizing the processes depending upon a their real time Scenario.

Thos: If we have no. of system processes coming continuously!

then based upon their priority they will mun

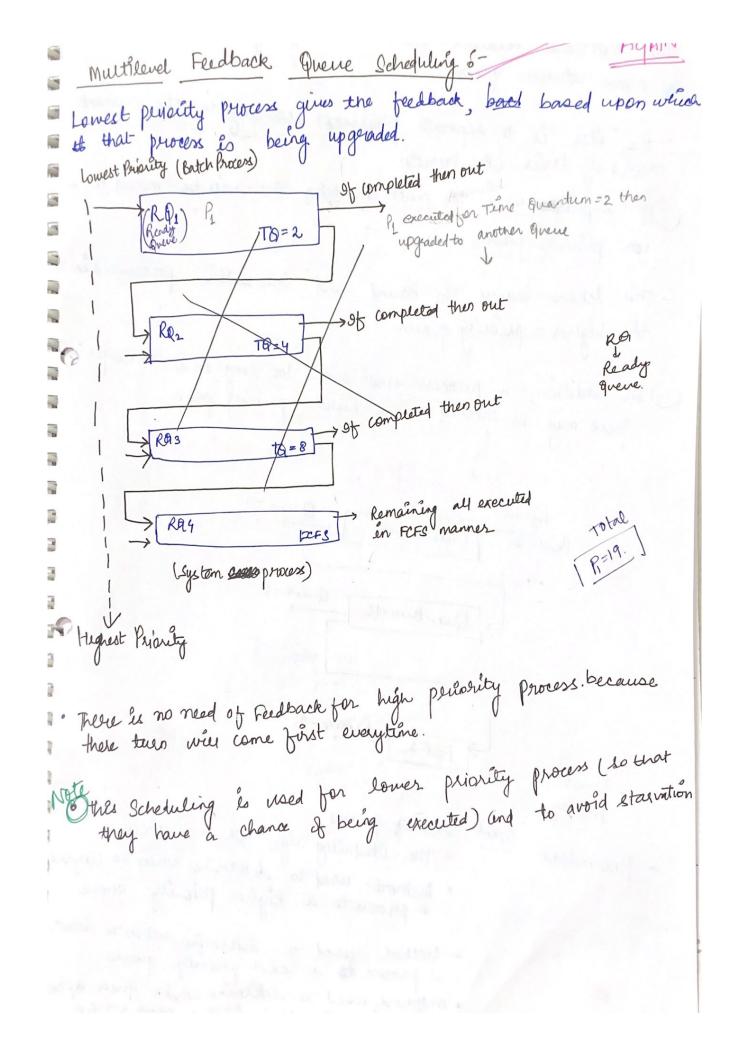
before Interactive and Batch process which leads

to wait for long of for Interactive a batch

processes

And > Atawation (Low palarity processes will wait for s long for their turn)

Solu' Multilevel Feedback Quere



the muttlevel Feedback-queve Landuling Algo allow a priocess to move between queves. - the idea is to separate processes according to the characte ristics of their CPU bursts. - of a process uses too much cousting of low princity queue. - This beheme leaves I/o bound and Interactive processes in the higher - priority grene On addition, a process that waits too long in a lover-pries of queve may be moved to a higher-priority queve. o' So this form of aging prevents Harvation Quantum=8 Quantum=16 FCFS lowest a No of guerres · the scheduling algo for each queue · Method used to determine when to upgra a peroces to a higher priority queue a process to a lower planty greve. · method used to determine which queve apr will enter when that Process needs service.