JASSIM ABDUL GAFOOR

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PERSONAL PROFILE

Looking for a challenging engineering role to apply my design and fabrication skills to meet client requirements. I am an adaptable result-oriented person with good attention to detail in all my assignments. Fast-learner who can quickly take on new responsibilities and achieve desired results.

EDUCATION

University of British Columbia *Vancouver, BC*
Bachelor of Applied Science, Integrated Engineering (3.0 GPA) *Aug 2016 – May 2021*

Major in Electrical Engineering, Minor in Computer Engineering. Specialized in VLSI Design, Digital and Integrated Circuit Design. Supplemented with material engineering, microsystems designs, silicon photonics, control systems, and sustainable development. Integrated Engineering Mentor.

EXPERIENCE

B-Wave Start Up *Doha, Qatar*
Product Designer *February 2022 – June 2022*

- Developed personal listening device for live commentary in football stadiums
- Pitched start-up idea to potential investors in Qatar Business Incubation Hackathon and won 8000 CAD grant
- Spearheaded user-research and usability testing for our minimum viable product (MVP)
- Created B-Wave company logo and launched website landing page/call-to action

TELUS *Vancouver, Canada*
JIRA Administrator *July 2019 – April 2020*

- Overhauled 3000+ documents database with JIRA Confluence platform
- Trained engineers to utilize the new platform and document templates to enhance document creation output
- Automated data migration task to improve document transfer rate by 60%
- Standardized procedures and implemented tags for engineering documents to increase accessibility
- Improvised and revitalized the 10000 CAD Enterprise Bridge software to migrate 9000 items instantly saving man-hours

ACTIVITIES & PROJECTS

Virtual Rubik's Cube *October 2022*

- Utilized Unity3d, Figma and C# programming to create a virtual 3x3 Rubik's cube
- Developed human-user interface design to interact with the cube using dual shock 4 controller

Smart Skin (Workout Tracking Device) *October 2020 - August 2021*

- Conducted market-research and gained user feedback on device prototype through online surveys
- Manufactured 8 modules for housing ultra-wideband sensors in a small wearable form-factor using 3D printer
- Mapped data from sensors and plotted using MATLAB for real-time tracking of body limb position

CMOS Clock Tree Integrated Circuit – UBC ELEC 403 Advanced VLSI Design *February 2021*

- Designed circuit layout for a clock driver in Cadence Virtuoso to minimize clock skew between different clock domains
- Modelled delay and fanout of 4 inverter chains to meet delay and rise/fall timing requirements

Chip Layout 45nm architecture – UBC ELEC 402 Introduction to VLSI Systems *October 2020*

- Synthesized a State Machine using SystemVerilog to control an LED lighting system and generated RTL netlist
- Performed Design Rule Check and Layout Versus Schematic using Calibre, Tcl to ensure design functionality
- Completed Place and Route using Cadence Encounter to generate Graphic Data Stream file for fabrication

High School Robotics – Botball, National Robot Olympiad *September 2012 – May 2016*

- Engineered robots using LEGO Mindstorms for specific challenges and won accolades in international competitions
- Coordinated and collaborated with teams of 3-10 to consistently participate in multiple national robotics events

SKILLS & INTERESTS

- **Technical:** Microsoft Office, Cadence Virtuoso, Machine Shop, Solidworks CAD, Unity3d, Adobe Photoshop, and Figma
- **Programming:** C#, Python, SystemVerilog, Assembly, and C
- **Personal:** Interpersonal, research, punctual, leadership, collaborative, systematic, organization, efficient communication, stress management, problem-solving, and critical thinking
- **Interests:** IEEE, Mentoring, volleyball, biking, PC gaming