

# Jett A Street

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## EDUCATION

### University of Washington

Seattle, WA

Bachelor of Science, Electrical and Computer Engineering

Grad. Jun 2024

Concentration: VLSI Design / Digital Systems Design

GPA 3.42

## TECHNICAL QUALIFICATIONS

**Languages:** C, C++, Java, Python, HTML, CSS, JavaScript, Lisp, Bash, Make, MATLAB, R, SQL, System Verilog, ARM

**Software:** LTSpice, MultiSim, Quartus, ModelSim, Cadence Virtuoso/Innovus, VMWare, IntelliJ, Git, MS Office

**Systems/Libraries:** GNU/Linux, FreeBSD, Cisco IOS, SciPy, NumPy, Pandas, Pylance, SQLAlchemy

**Skills:** Soldering, oscilloscopes, function generators, EDA tools, leadership, communication, organization

## EXPERIENCE

### University of Washington Engineering Student Council

Seattle, WA

Chairman

June 2023 – Present

- Presides over monthly UWESC meetings to bring dialogue between Engineering Student Organizations
- Currently writing a new UWESC constitution and developing a budget for 2023-24
- Worked with the College of Engineering Career Center to organize fundraising events for student clubs

### Li3Go

Las Vegas, NV (Remote)

Engineer

June 2022 – Aug 2023

- Implemented a patented multi-grid power management system using Python on a prototype class A motorhome retrofitted with solar panels
- Designed hardware agnostic database schema to support multiple brands of solar inverters
- Experience socket programming with UDP and Modbus over TCP
- Attended Quartzsite RV trade show and presented an informational seminar

### Husky Flying Club

Seattle, WA

Vice President

Sept 2020 - Present

- Successfully planned, pitched, and managed a \$105,811 grant to build the first UW light-sport aircraft
- Created the first FPV-drone racing team on campus, awarded \$9,000 towards managing a HFC drone fleet
- Partnered with local flight schools to offer club members discounted flying lessons and free ground school

### Spokane Public Schools

Spokane, WA

Student CTE Tech Support

June - Sept, 2018 - 2021

- Provided technical support to teachers and administrators in the summer months
- Installed and configured Aruba networking hardware in newly constructed schools
- Assisted in preparing 60,000 student laptops for the rapid adoption of online learning due to COVID-19

## PROJECTS

### Circuit Design and Analysis

- Proficient in DC, AC, and nonlinear circuit design and analysis
- Designed and built an adjustable output AC to DC power supply with less than 100 mV of ripple voltage

### Signals Processing

- Implemented programs in Python to synthesize, plot, play, analyze and filter time functions
- Proficient with convolution of signals, Fourier series and transforms, and linear time-invariant filters

### Computer Architecture

- Implemented a 32-bit pipelined 5-cycle ARM CPU in SystemVerilog using Intel Quartus and ModelSim
- Wrote an IEEE-754 floating point addition algorithm in ARMv7 assembly
- Gained proficiency in digital logic and SystemVerilog programming using an Intel DE1-SoC FPGA

### VLSI / ASIC Design

- Designed a functional 45nm 16-bit register file from scratch using Cadence Virtuoso
- Worked with Google's skywater 130nm PDK to implement gate-level Verilog using Cadence Innovus
- Proficient with Static Timing Analysis, Power Grid Analysis, and Signal Integrity Analysis