

Jett A Street

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EDUCATION

University of Washington

Bachelor of Science, Electrical and Computer Engineering
Concentration: VLSI Design / Digital Systems Design

Seattle, WA

Grad. Jun 2024
GPA 3.42

TECHNICAL QUALIFICATIONS

Languages: C, C++, Java, Python, HTML, CSS, JavaScript, Lisp, Bash, Make, MATLAB, R, SQL, System Verilog, ARM

Software: SPICE, ModelSim, Cadence Virtuoso/Innovus, Accuver XCAL/XCAP, Altera Quartus, Git, MS Office

Systems/Libraries: GNU/Linux, FreeBSD, Cisco IOS, SciPy, NumPy, Pandas, Pylance, MySQL, Plotly

Skills: Soldering, oscilloscopes, telecommunications, EDA tools, leadership, communication, organization

EXPERIENCE

T-Mobile/University of Washington

Seattle, WA

Capstone Control Systems Lead

Jan 2024 – Present

- Currently working on a team with T-Mobile to implement 3D RF coverage mapping using aerial drone data
- Responsible for writing automated flight mapping software to collect RF data in Python and Ardupilot
- Developing a machine learning model to interpolate 3D RF data in order to reduce flight times

University of Washington Engineering Student Council

Seattle, WA

Chairman

June 2023 – Present

- Presides over monthly UWESC meetings to bring dialogue between Engineering Student Organizations
- Currently writing a new UWESC constitution and developing a budget for 2023-24
- Worked with the College of Engineering to organize a career fair that raised \$40,000 for student clubs

Li3Go

Las Vegas, NV (Remote)

Engineer

June 2022 – Aug 2023

- Implemented a patented multi-grid power management system using Python on a prototype class A motorhome retrofitted with solar panels
- Designed hardware agnostic database schema to support multiple brands of solar inverters
- Experience socket programming with UDP and Modbus over TCP
- Inventor on USPTO Patent Number US11130422B1

Husky Flying Club

Seattle, WA

Vice President

Sept 2020 - Present

- Successfully planned, pitched, and managed a \$105,811 grant to build the first UW light-sport aircraft
- Created the first FPV-drone racing team on campus, awarded \$9,000 towards managing a HFC drone fleet
- Partnered with local flight schools to offer club members discounted flying lessons and free ground school

PROJECTS

Circuit Design and Analysis

- Proficient in DC, AC, and nonlinear circuit design and analysis
- Designed and built an adjustable output AC to DC power supply with less than 100 mV of ripple voltage

Signals Processing

- Implemented programs in Python to synthesize, plot, play, analyze and filter time functions
- Proficient with convolution of signals, Fourier series and transforms, and linear time-invariant filters

Computer Architecture

- Implemented a 32-bit pipelined 5-cycle ARM CPU in SystemVerilog using Intel Quartus and ModelSim
- Wrote an IEEE-754 floating point addition algorithm in ARMv7 assembly
- Gained proficiency in digital logic and SystemVerilog programming using an Intel DE1-SoC FPGA

VLSI / ASIC Design

- Designed a functional 45nm 16-bit register file from scratch using Cadence Virtuoso
- Worked with Google's skywater 130nm PDK to implement RTL Verilog using Cadence Innovus
- Proficient with Static Timing Analysis, Signal Integrity Analysis, DRC/LVS