

IU  
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Iran University  
of  
Science and Technology

Department of  
Computer Engineering

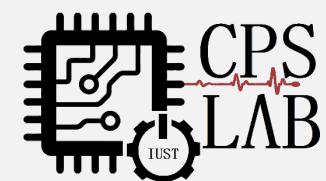
# Computer Architecture Laboratory

## Introduction SPARTAN3 dev. Board

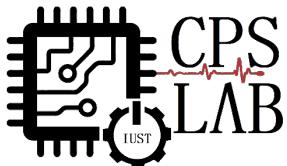
Lecturer: Ali Javadi

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2<sup>nd</sup> session

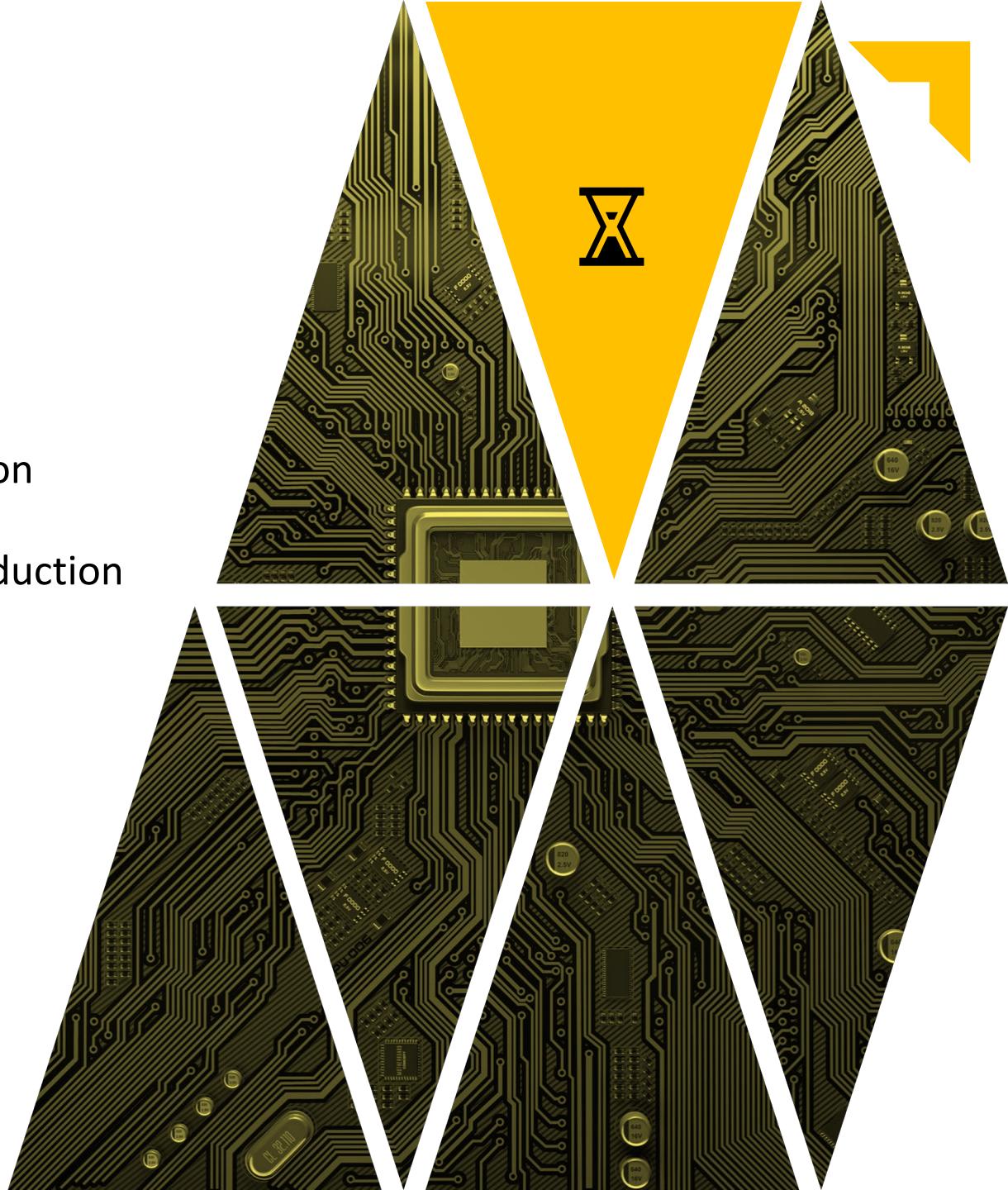


Cyber-Physical Systems Laboratory



# Overview

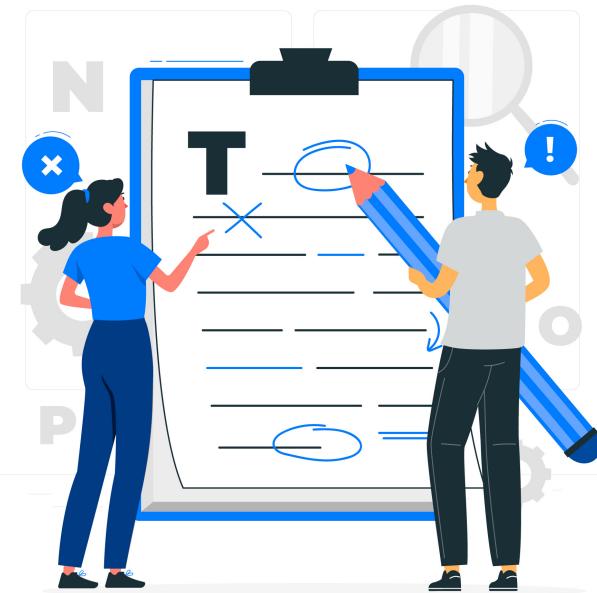
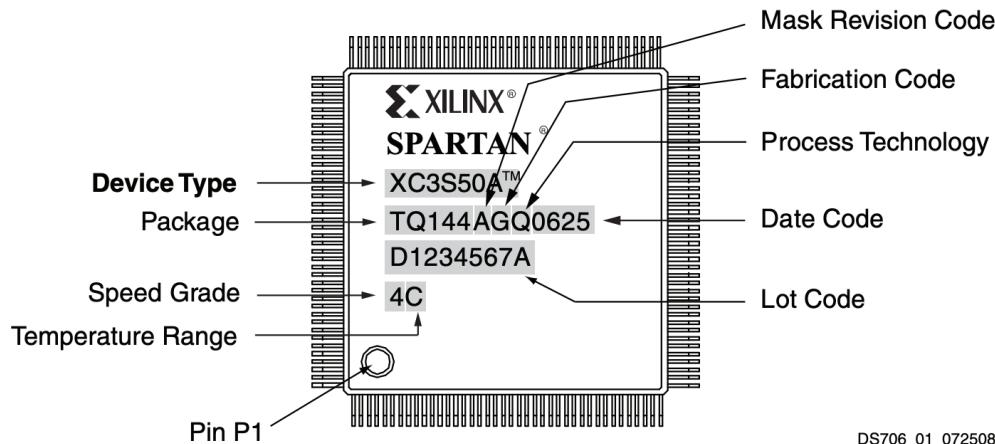
- SPARTAN3 Introduction
- AVA Dev. board Introduction
- Experiment 1
- Experiment 2



# SPARTAN3

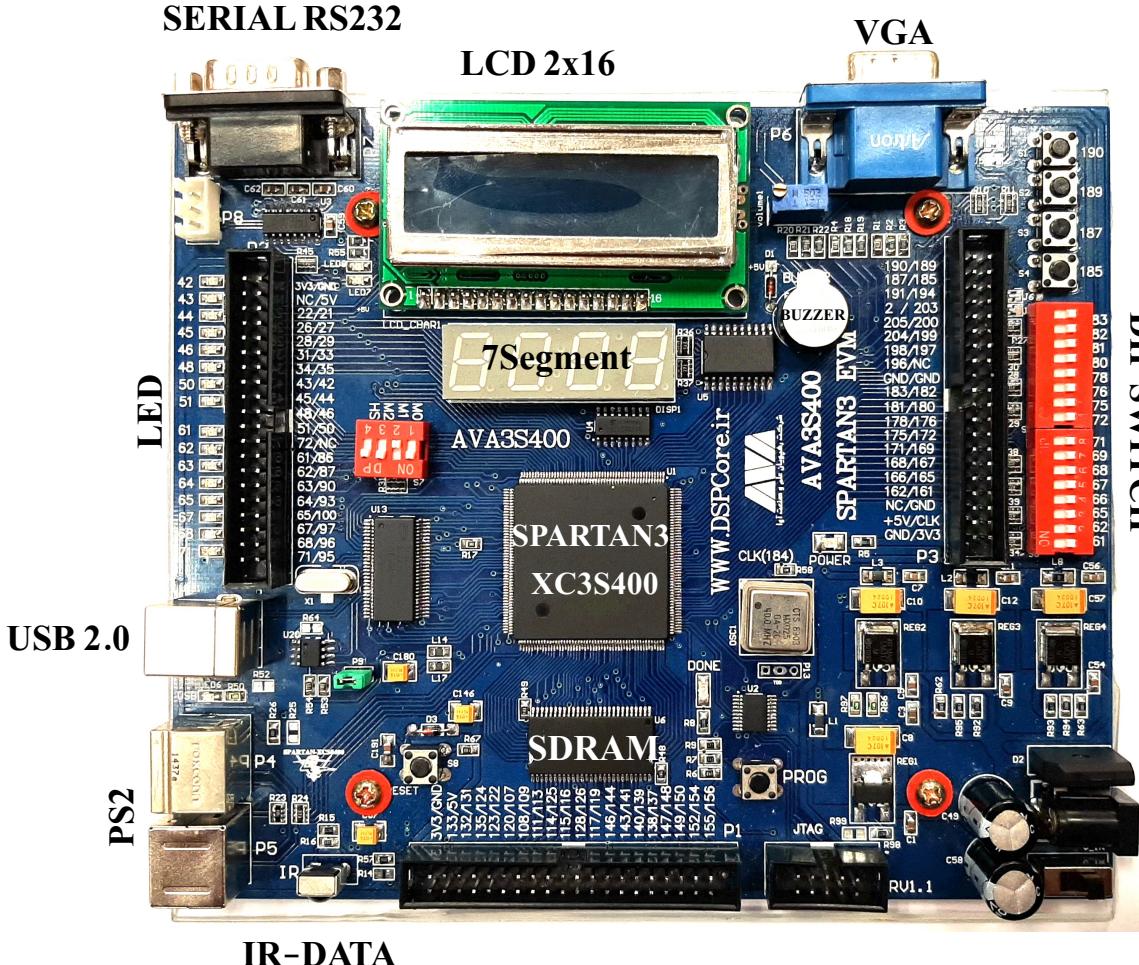
## Introduction

Device	System Gates	Equivalent Logic Cells	CLBs	Slices	Distributed RAM Bits	Block RAM Bits	In-System Flash Bits	Dedicated Multipliers	DCMs	Maximum User I/O
XC3S400A/AN	400K	8,064	896	3,584	56K	360K	4M	20	4	311



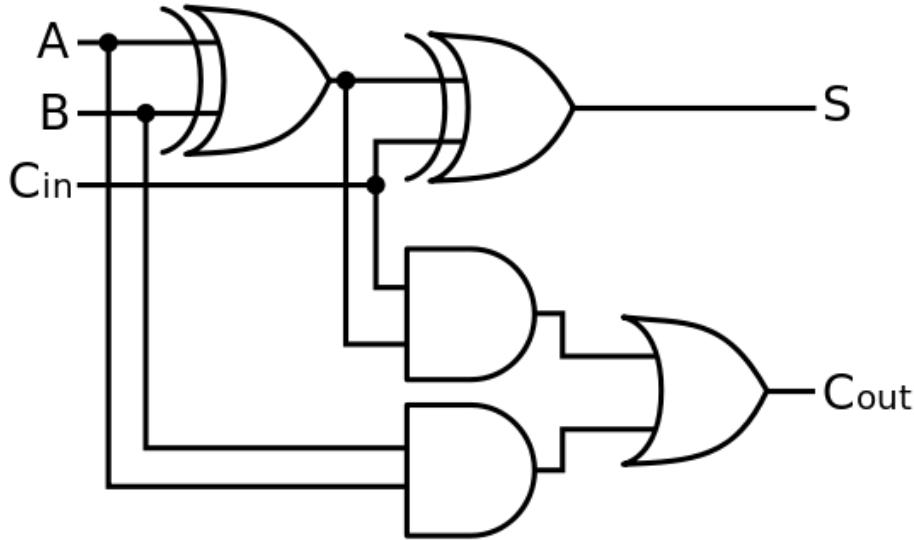
# AVA Dev. board

## Introduction



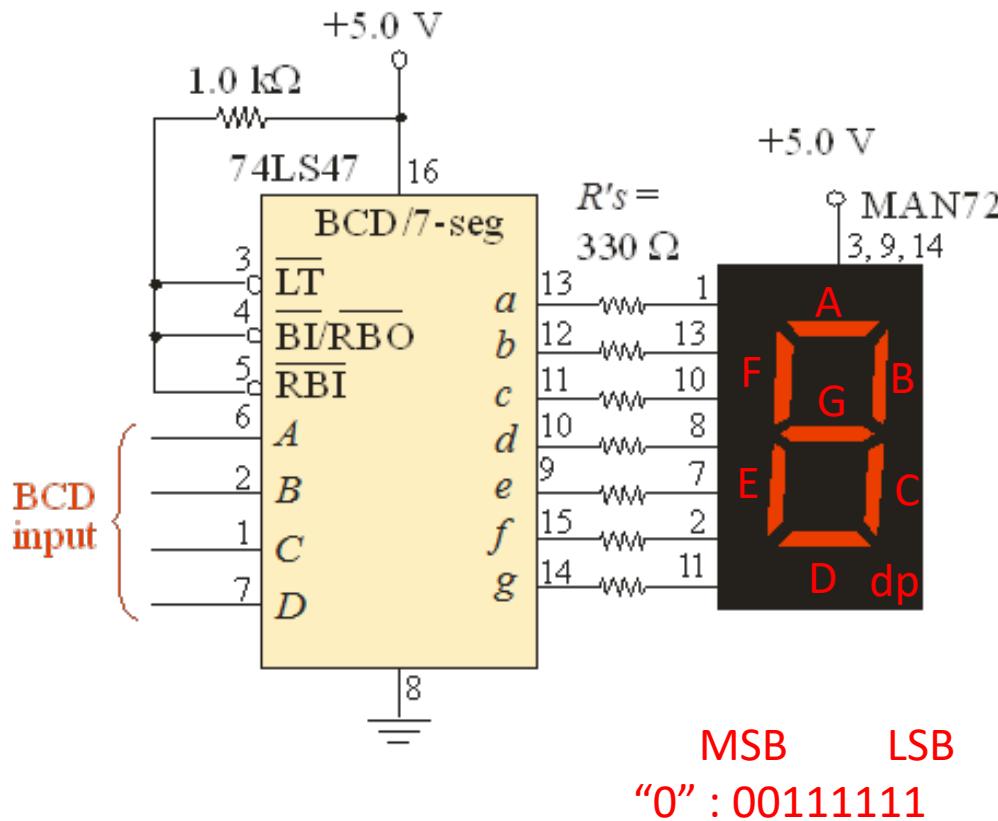
# Experiment 1

## Full Adder



# Experiment 2

## BCD to 7-Segment





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# THANK YOU

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