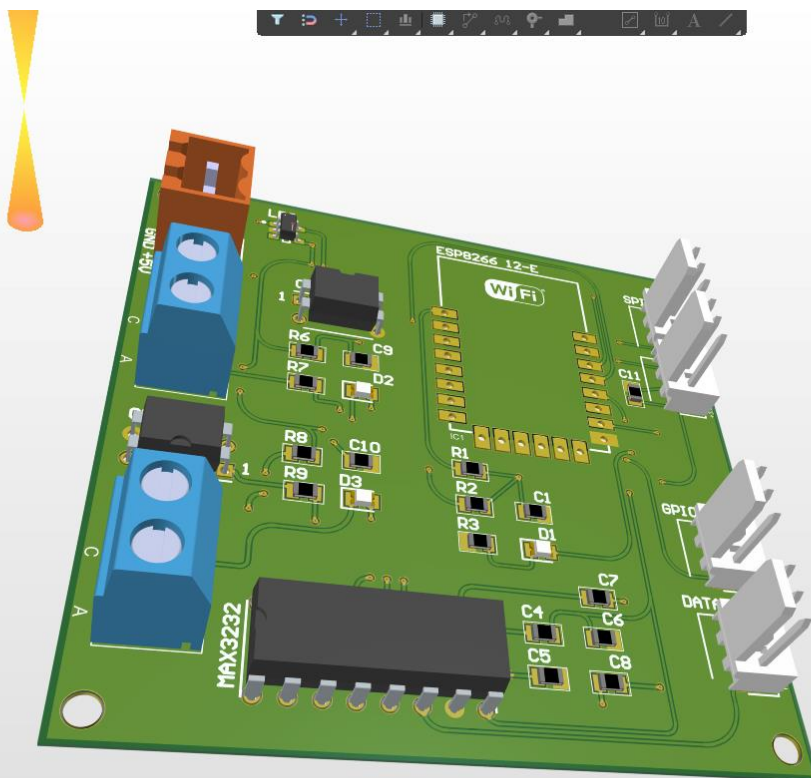
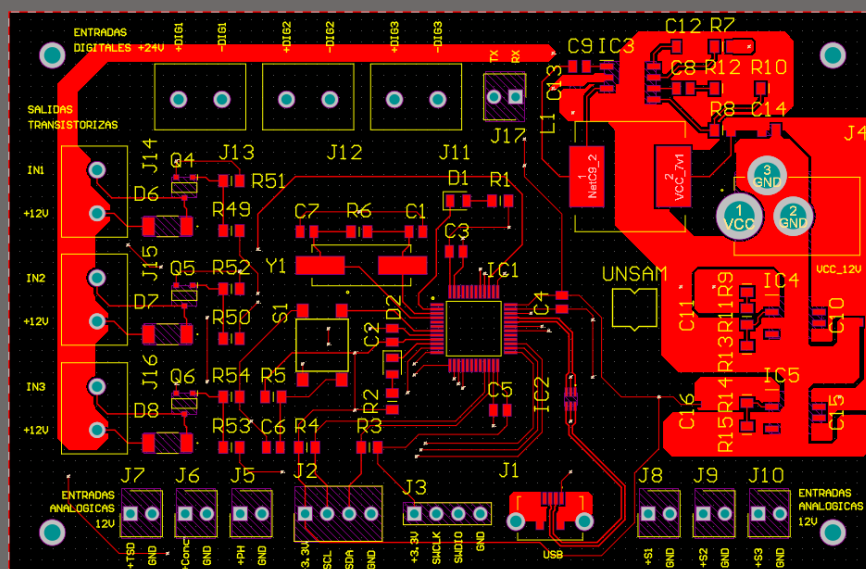
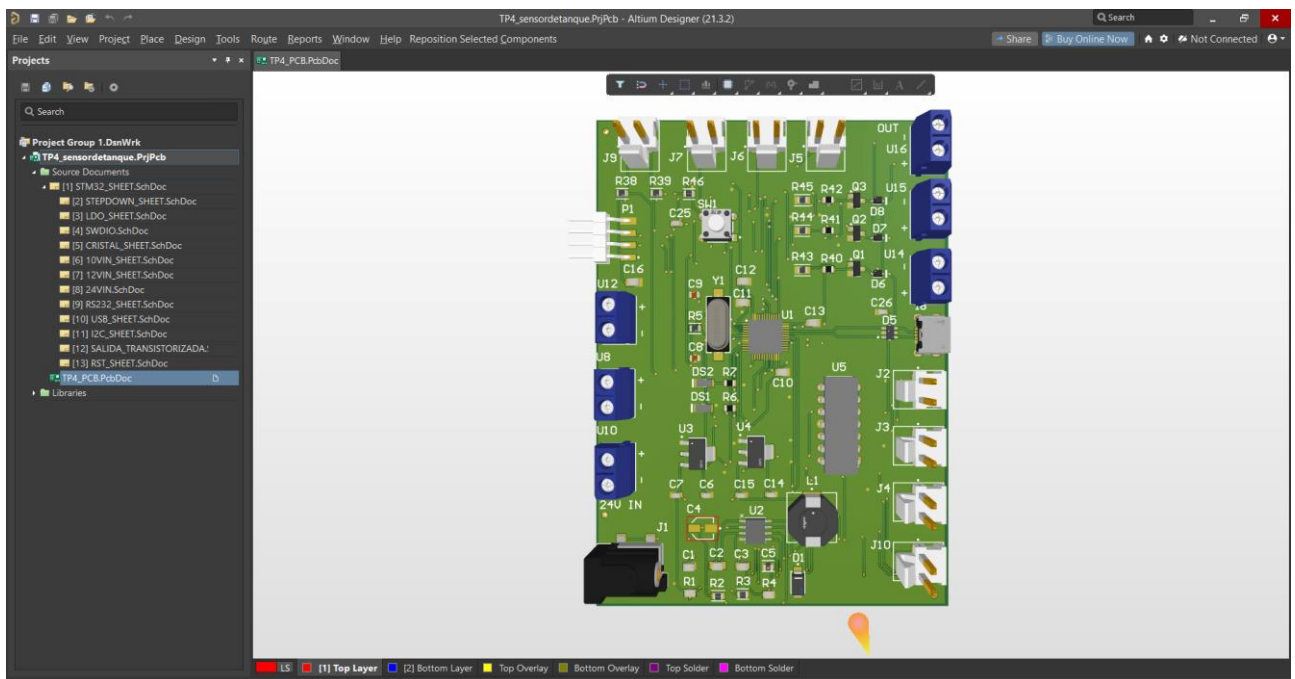
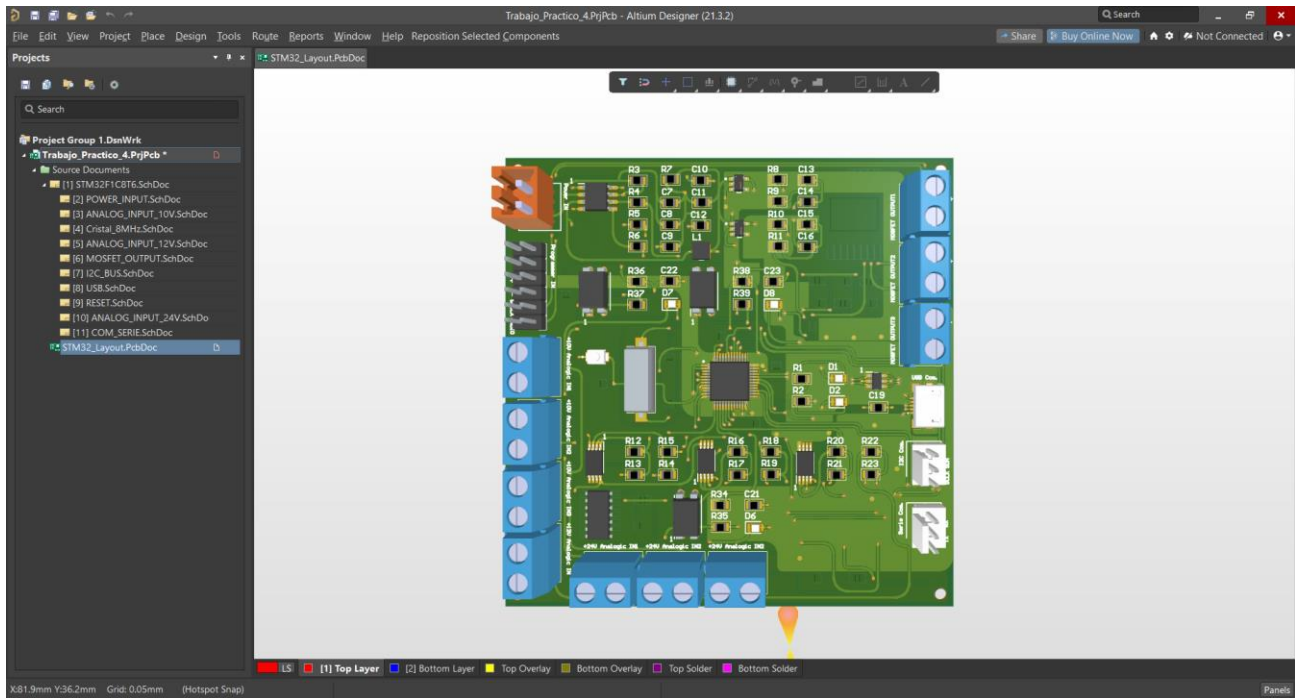


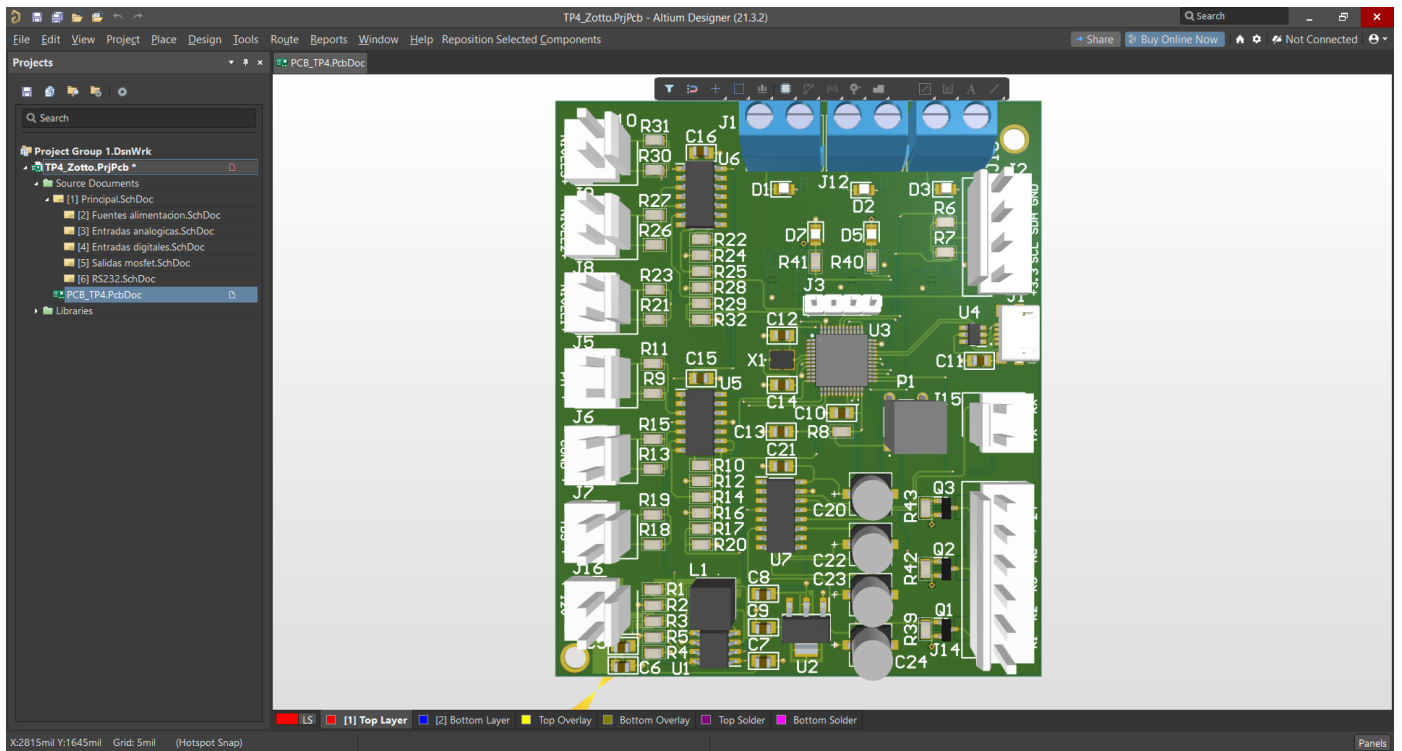
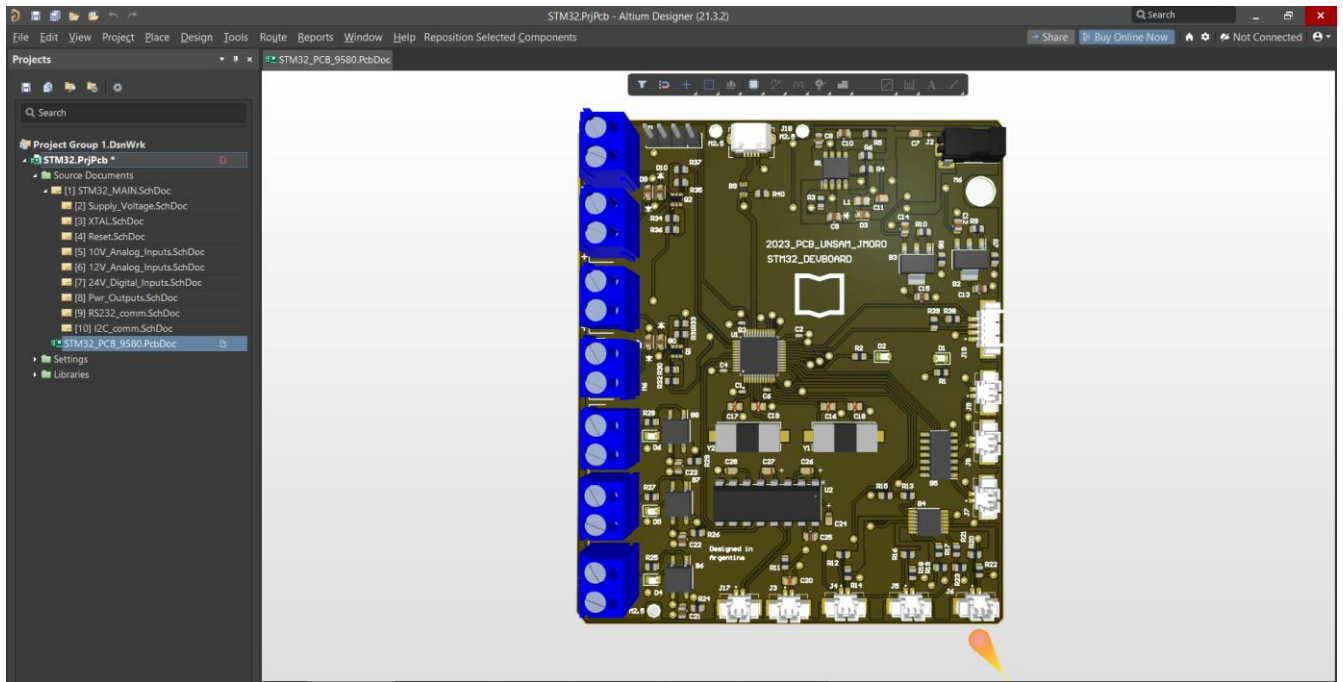
x: 1540,000 dx: 1540,000 mil
y: 5295,000 dy: 5295,000 mil
Top Layer
Snap: 5mil Hotspot Snap: 8mil

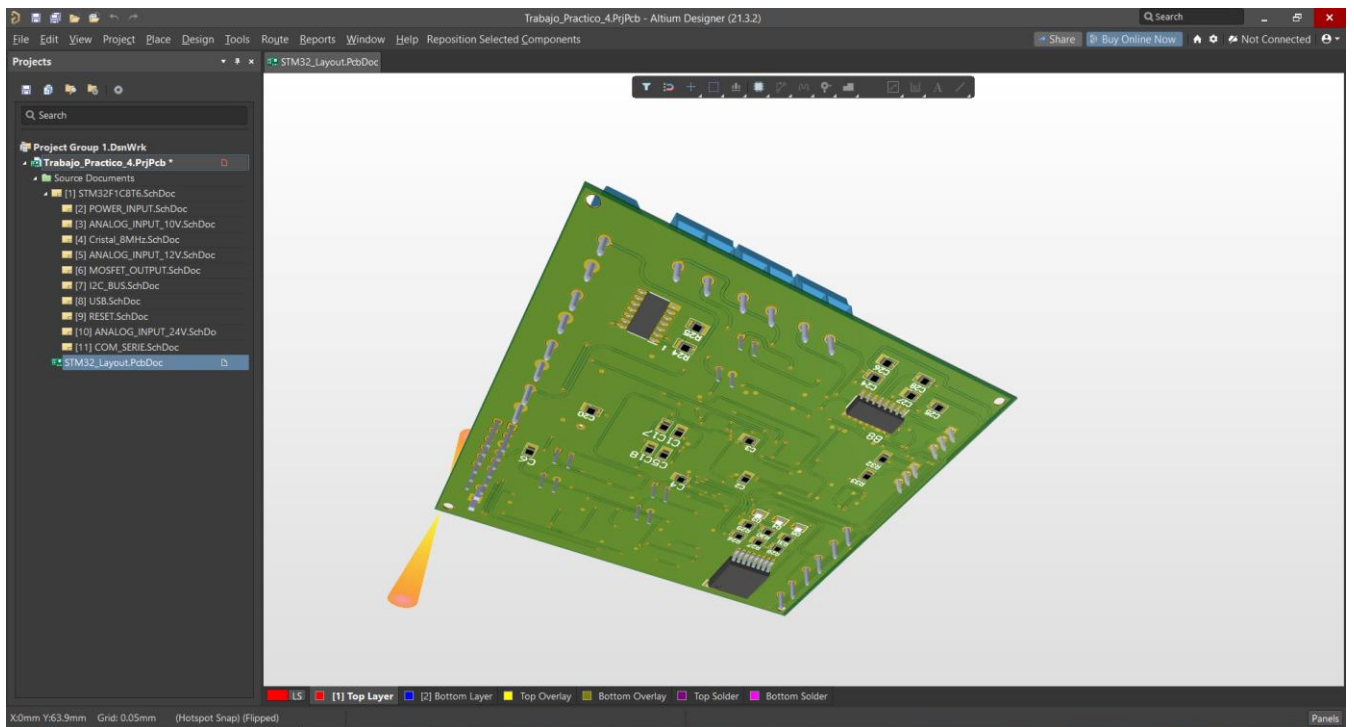
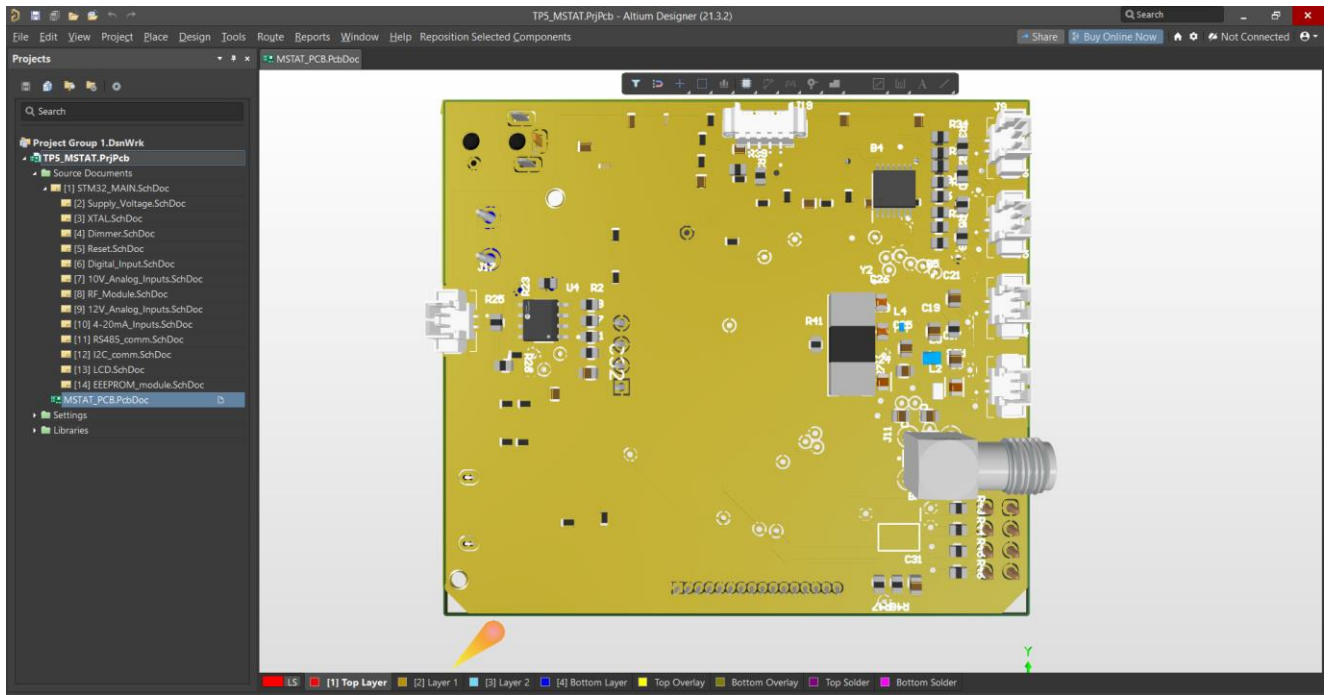


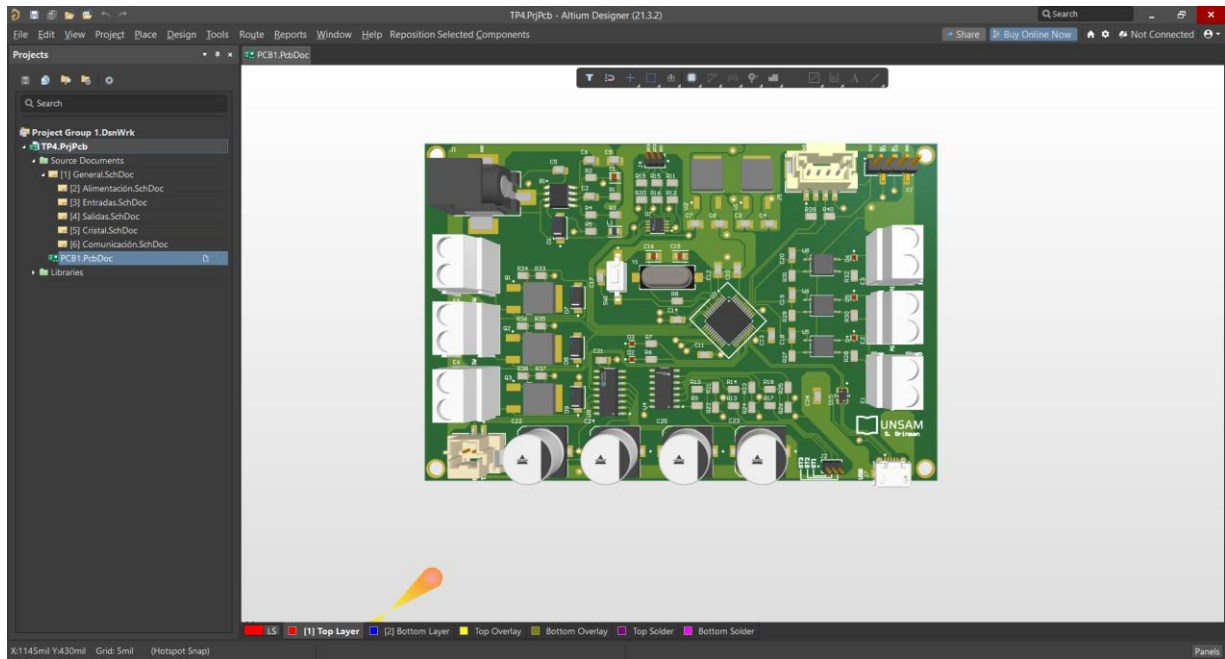
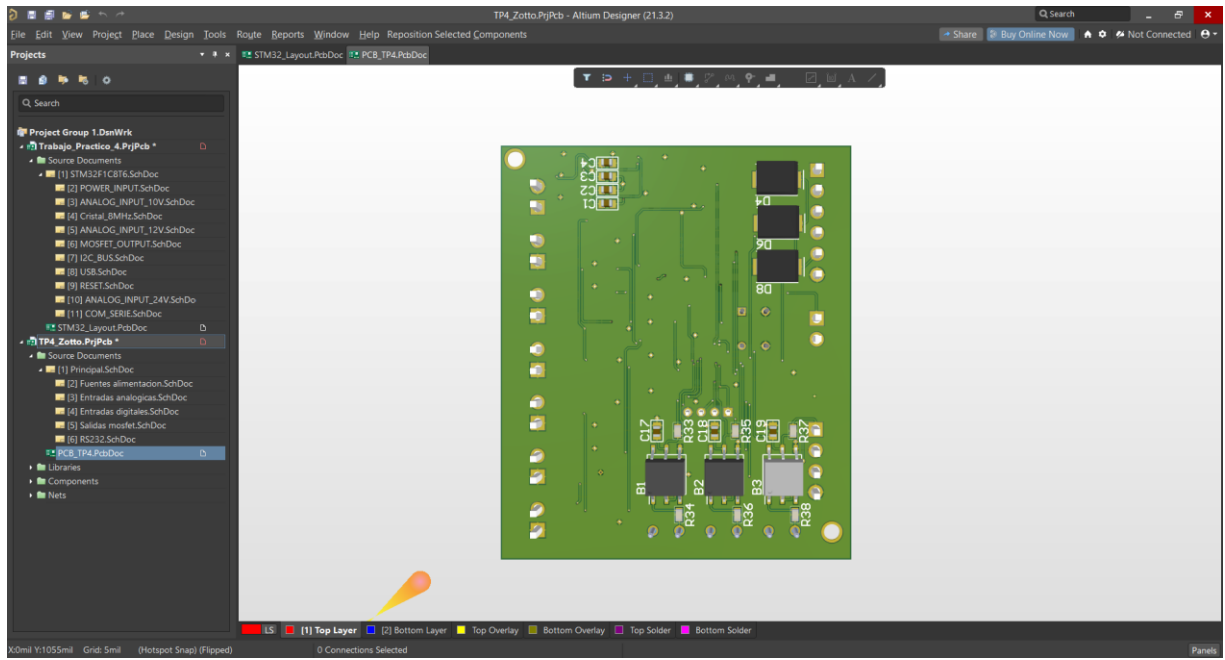
x: 0,000 dx: 0,000 mil
y: 3950,000 dy: 3950,000 mil
Top Overlay
Snap: 10mil Hotspot Snap: 8mil

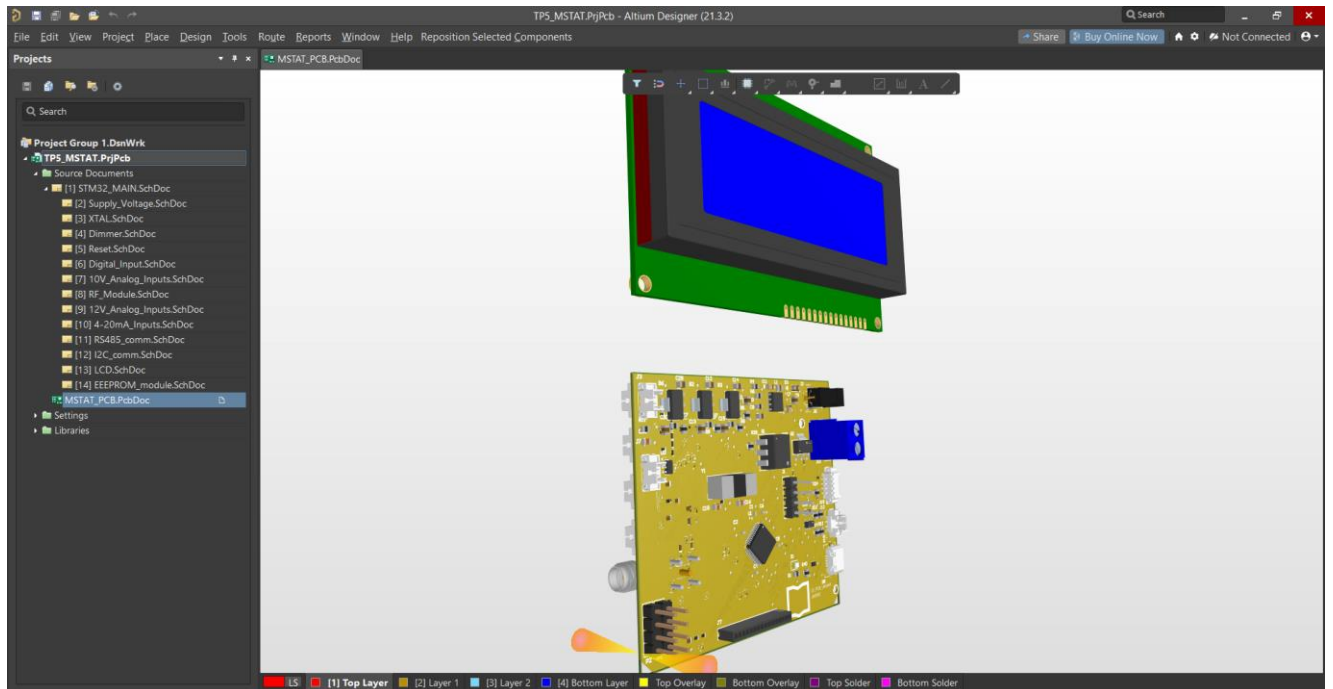
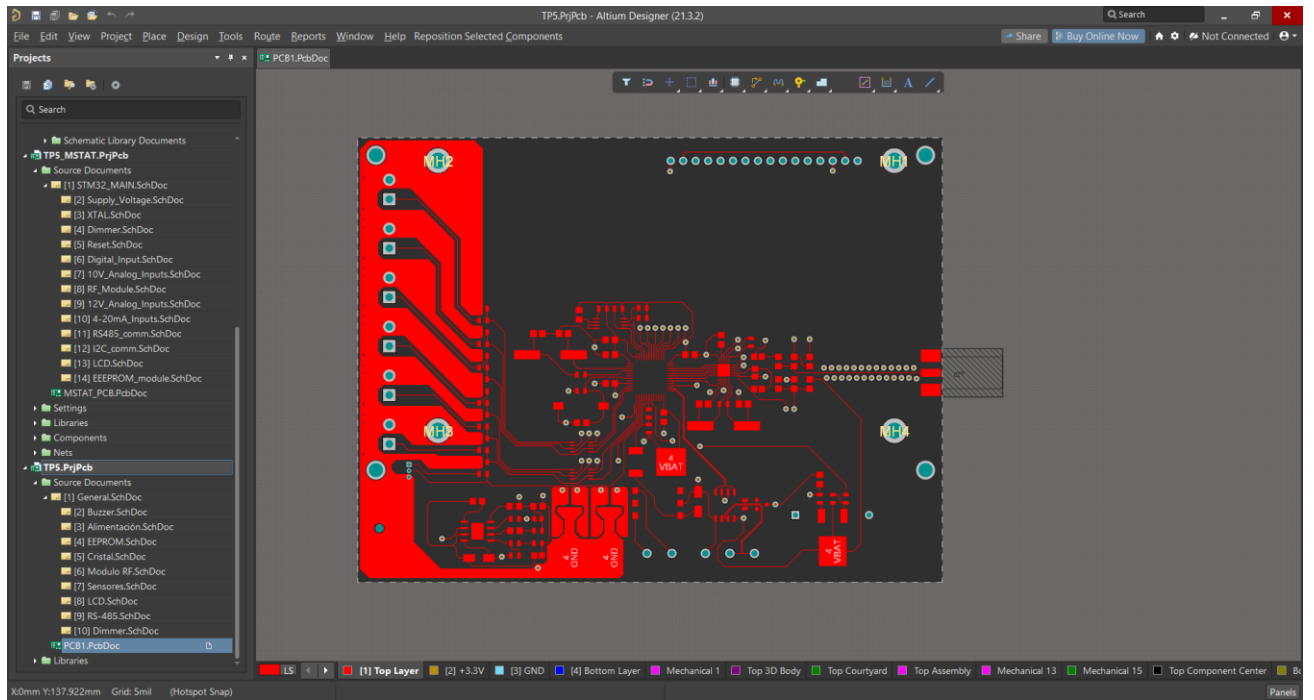


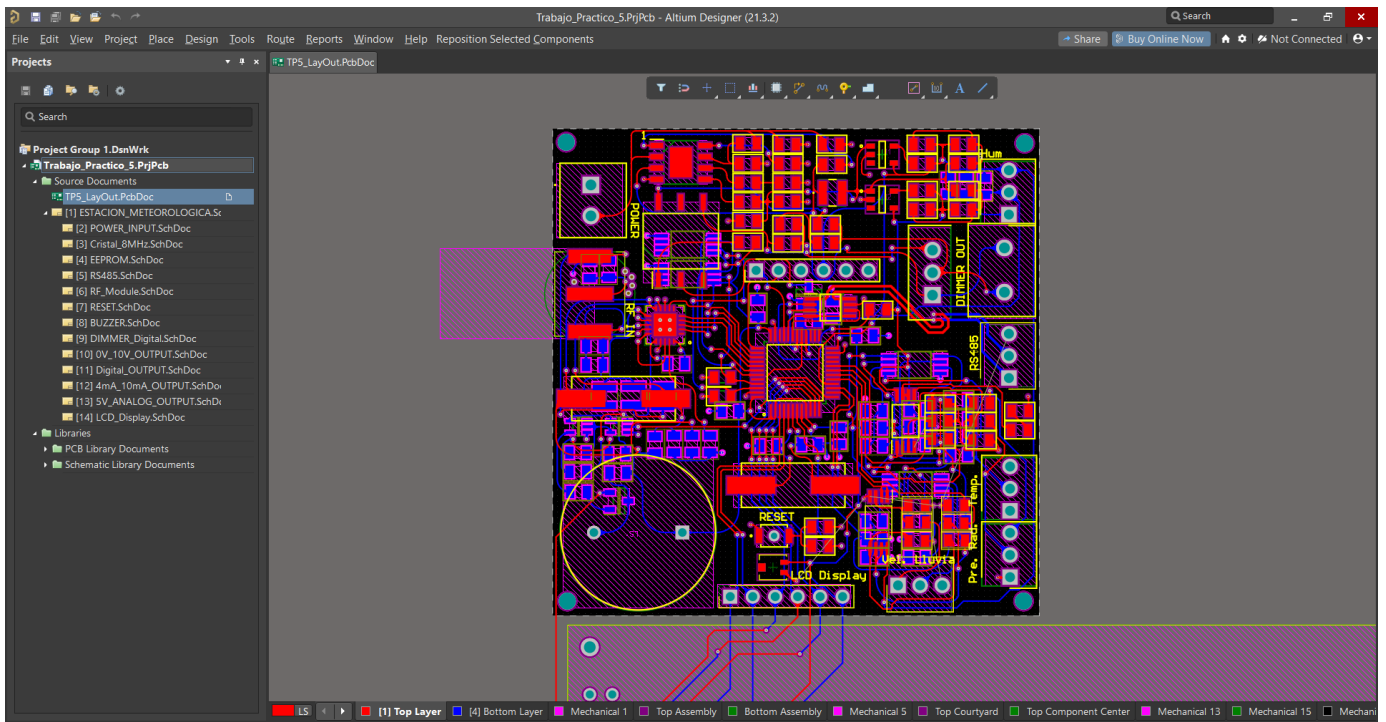
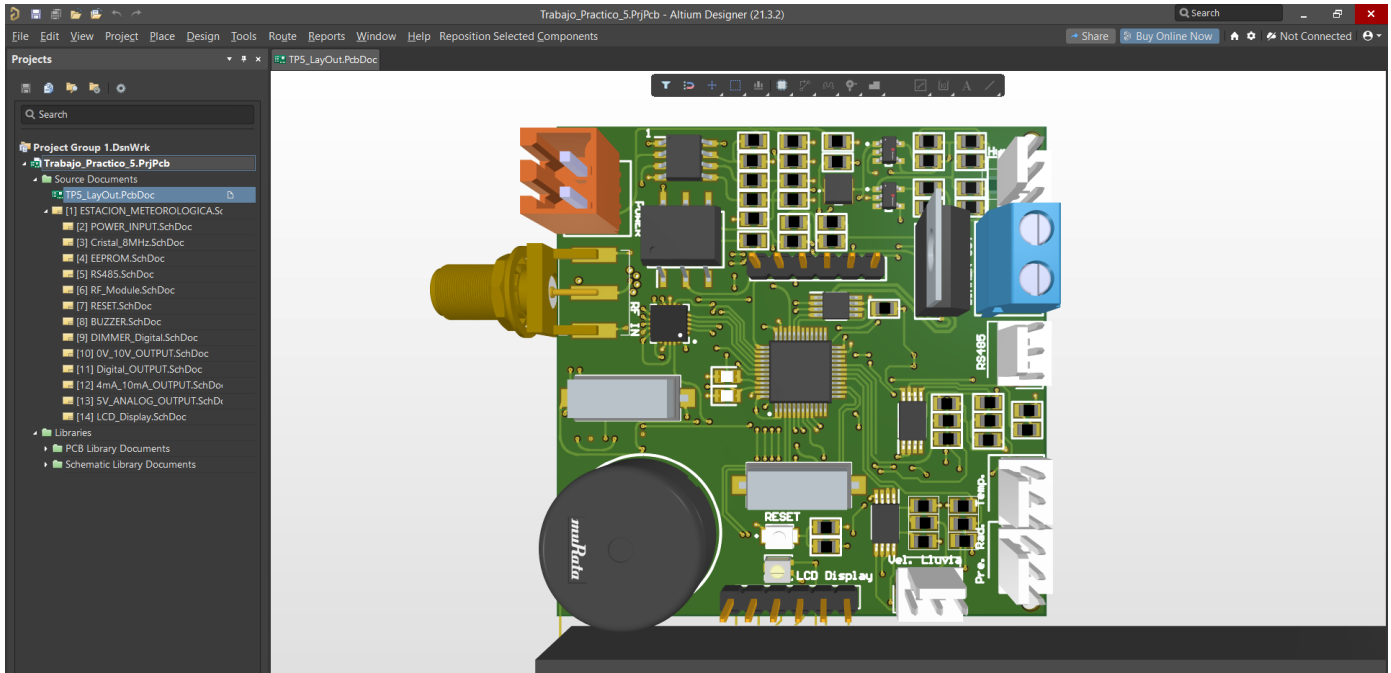


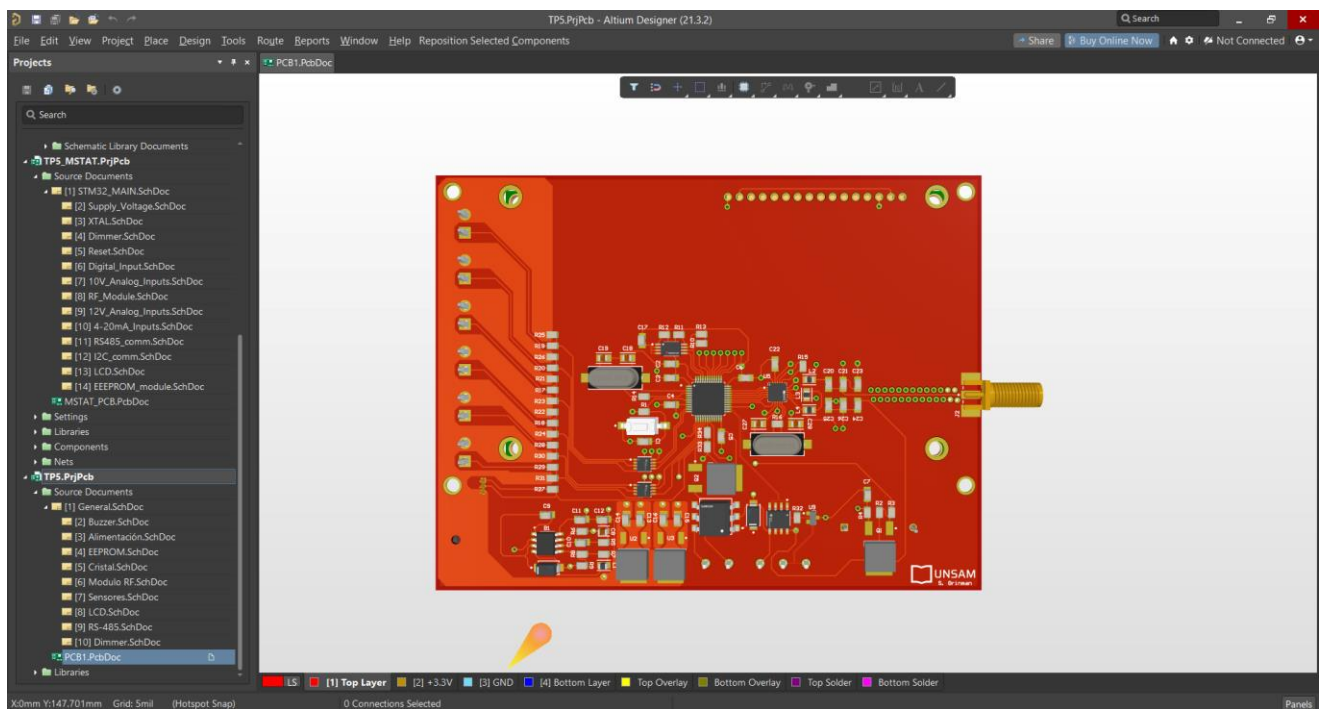
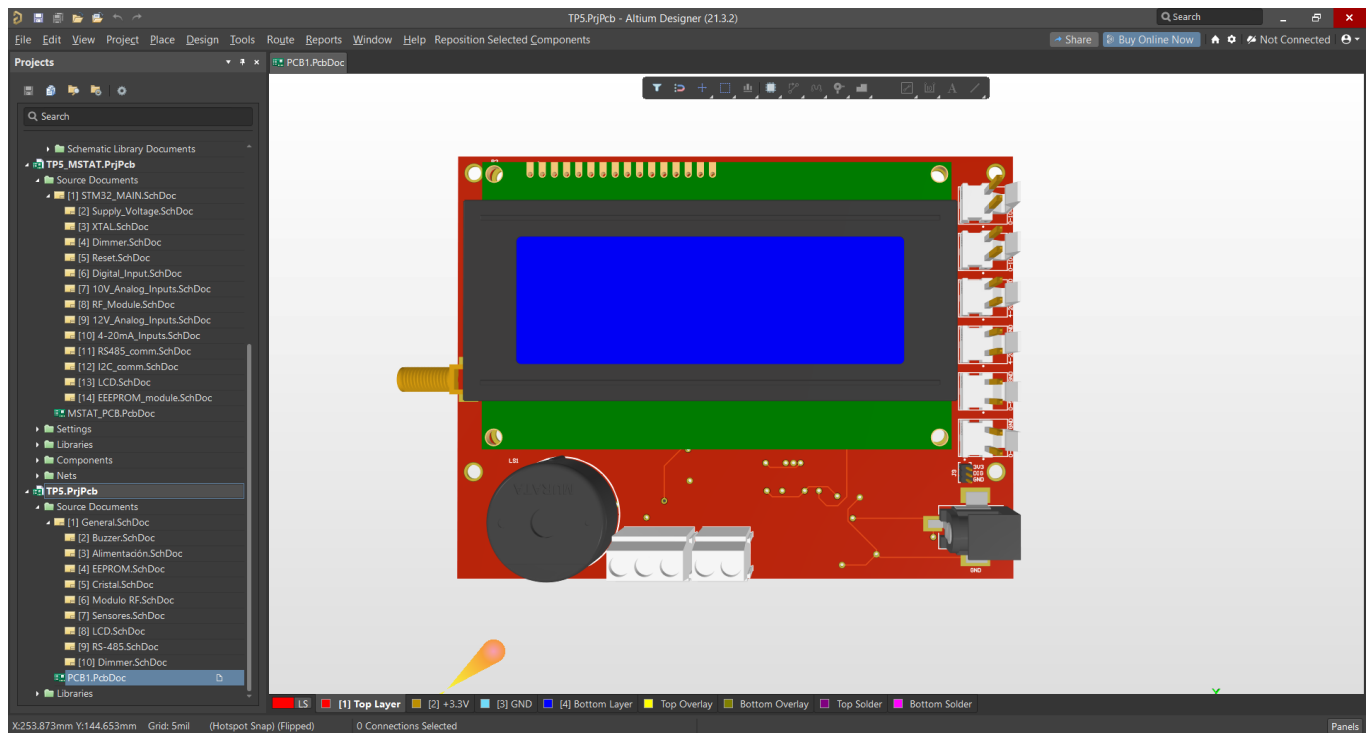


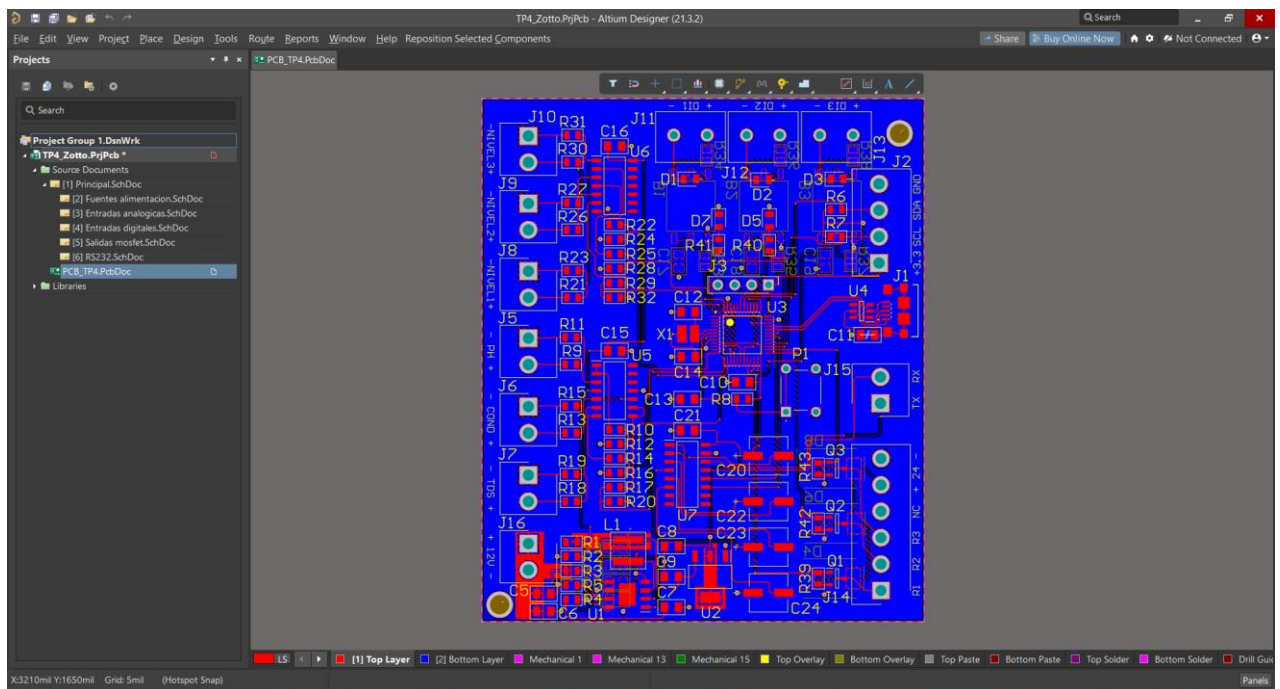
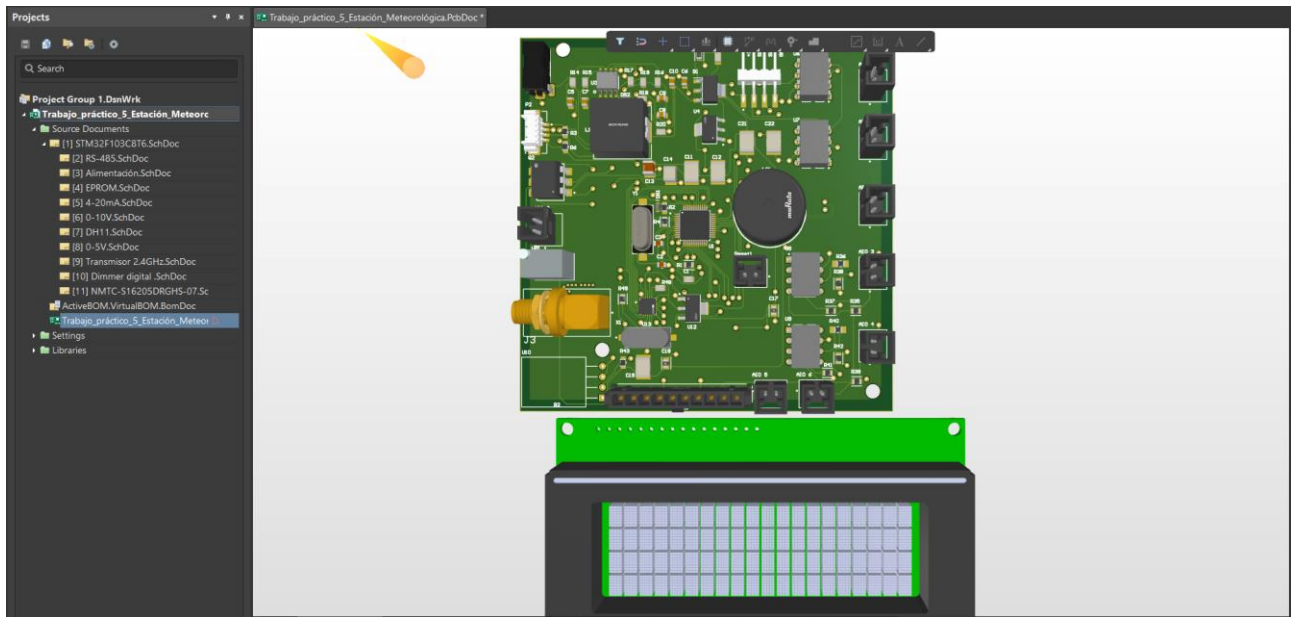


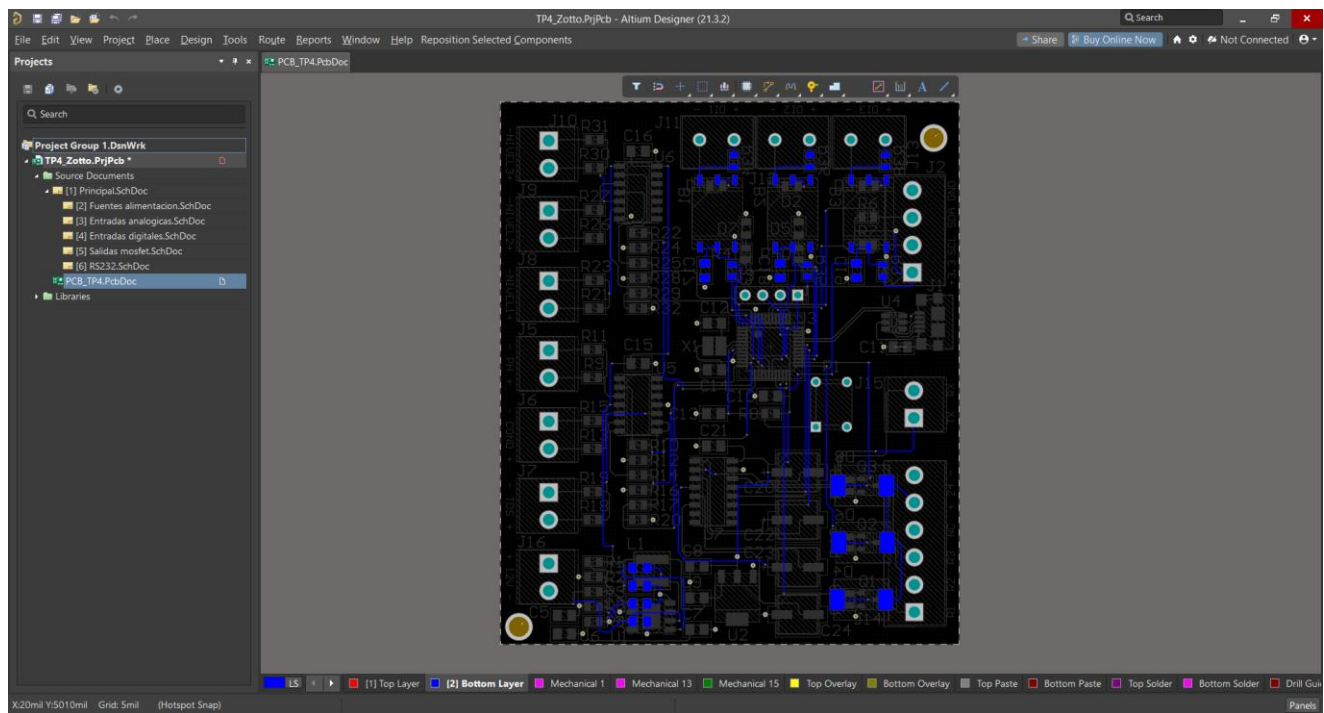
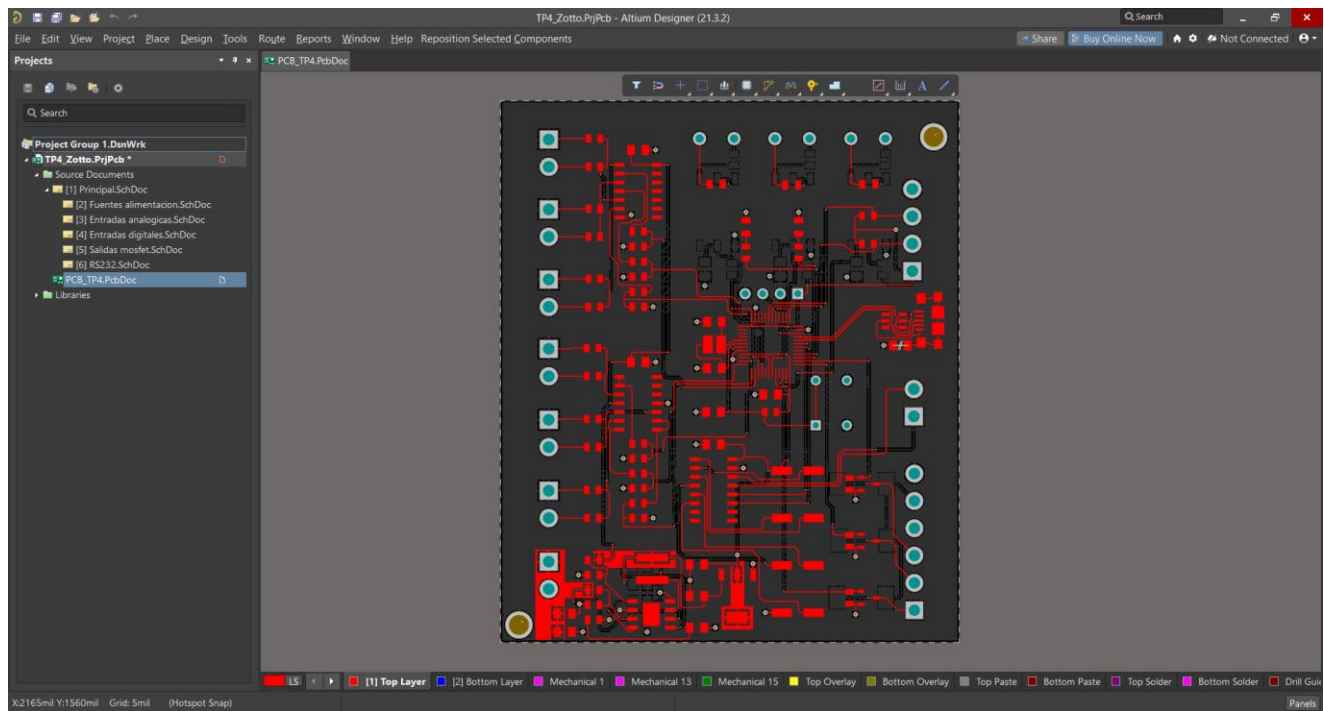


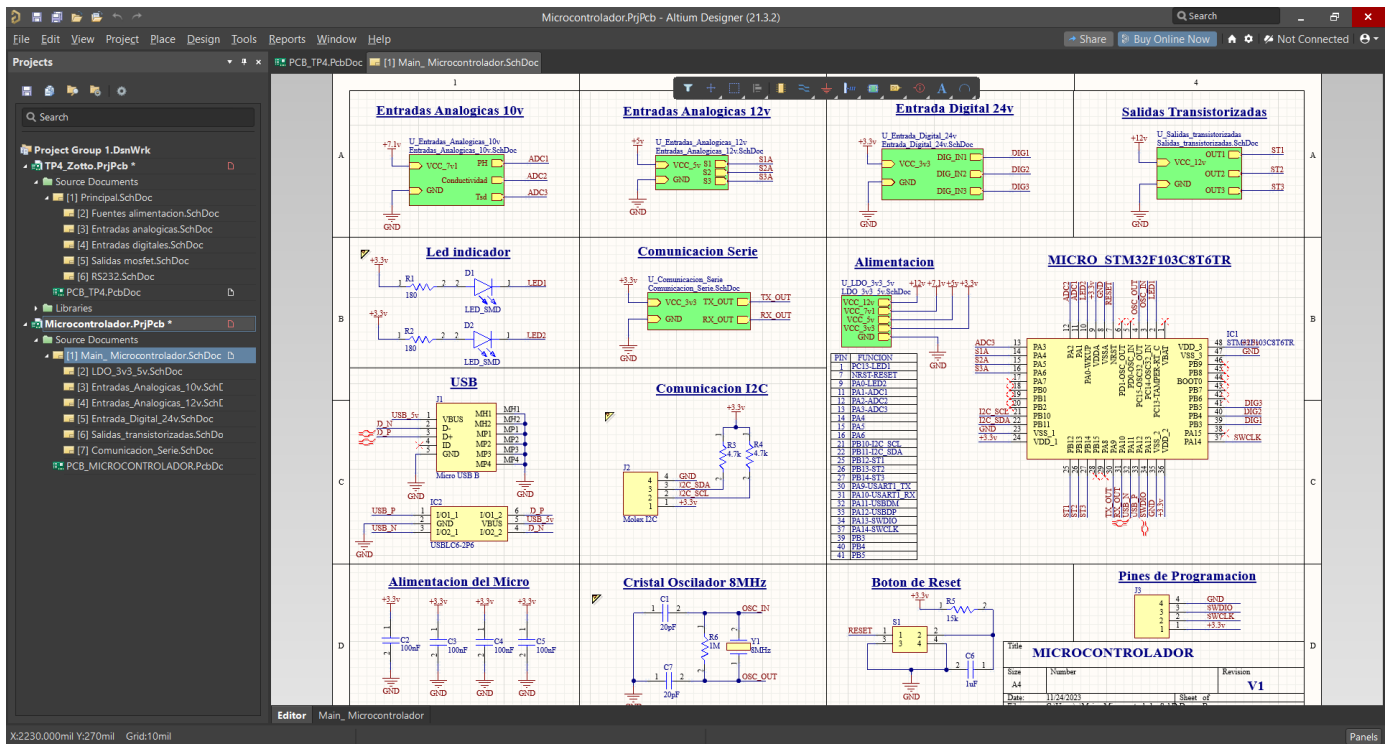
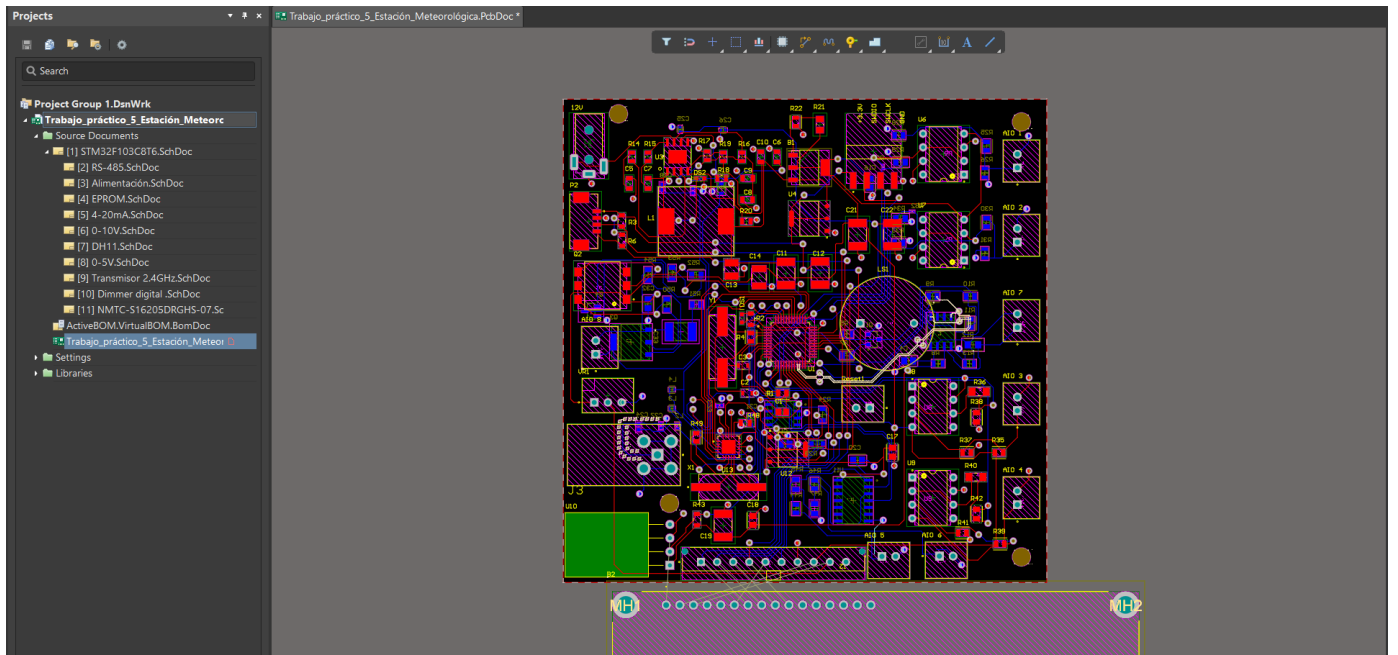












Properties

Layer Stack Manager

Q, Search

Impedance Profile

Description

USART

Type

Differential

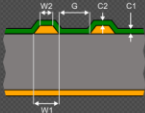
Target Impedance

120

Target Tolerance

10%

Transmission Line



Simulated with SIMBEOR® software

Use Solder Mask

☒

Trace Inverted

☐

Etch

0

Width (W1)

2.907mil

Width (W2)

2.907mil

Covering (C1)

0.4mil

Covering (C2)

0.4mil

Trace Gap (G)

19.685mil

Impedance (Zdiff)

119.99

Deviation

0.01%

Trabajo_práctico_5_Estación_Meteorológica.PcbDoc

Trabajo_práctico_5_Estación_Meteorológica.PcbDoc [Stackup]

+ Add

Remove

D120 (USART)

Features

#	Name	Material	Type	Weight	Thickness	Dk	Copper Orientat...	Top Ref	Bottom Ref	Width (W1)	Trace Gap...	Impe...	Devia...	
	Top Overlay		Overlay											
	Top Solder	Solder Resist	Solder Mask		0.4mil	3.5								
1	Top Layer		Signal	1oz	1.4mil		Above	<input checked="" type="checkbox"/>	2 - L1	2.907mil	19.685mil	119.99	0.01%	
	Dielectric 2	PP-006	Prepreg		2.8mil	4.1								
2	L1	CF-004	Signal	1oz	1.378mil		Above	<input checked="" type="checkbox"/>	1 - Top Layer	3 - L2	1.414mil	19.685mil	119.99	0.01%
	Dielectric 1	FR-4	Dielectric		12.6mil	4.8								
3	L2	CF-004	Signal	1oz	1.378mil		Below	<input checked="" type="checkbox"/>	2 - L1	4 - Bottom L...	1.414mil	19.685mil	119.99	0.01%
	Dielectric 3	PP-006	Prepreg		2.8mil	4.1								
4	Bottom Layer		Signal	1oz	1.4mil		Below	<input checked="" type="checkbox"/>	3 - L2	2.907mil	19.685mil	119.99	0.01%	
	Bottom Solder	Solder Resist	Solder Mask		0.4mil	3.5								
	Bottom Overlay		Overlay											

Stackup

Impedance

Via Types

x:581.693mil y:7005.905mil

Grid: 0.984mil

(Hotspot Snap)

Panels